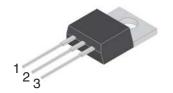
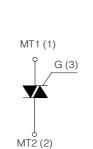


#### **TO-220AB**





#### **On-State Current**

### **Gate Trigger Current**

25 Amp

≤ 50 mA ≤ 35 mA

#### **Off-State Voltage**

200 V ÷ 800 V

#### **FEATURES**

- Glass/passivated die junctions
- High current Triac
- Low thermal resistance
- High commutation
- High surge current capability
- Low forward voltage drop
- Solder dip 260°C, 10s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- Meets MSL level 3, per J-STD-020, LF maximum peak of 260° C

#### **MECHANICAL DATA**

- Case: TO-220AB. Epoxy meets UL 94V-0 flammability rating.
- Polarity: As marked on the body.
- **Terminals:** Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test.

#### **TYPICAL APPLICATIONS**

 Used on inductive loads, thanks to their high commutation performances.

## Maximun Ratings and Electrical Characteristics at 25°C

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
I <sub>T(RMS)</sub>	RMS On-state Current (full sine wave)	All Conduction Angle, T <sub>c</sub> = 100 °C	25	А
I <sub>TSM</sub>	Non-repetitive On-State Current	Full Cycle, 60 Hz (t = 16.7 ms)	215	А
I <sub>TSM</sub>	Non-repetitive On-State Current	Full Cycle, 50 Hz (t = 20 ms)	200	А
I <sup>2</sup> t	Fusing Current	tp = 10 ms, Half Cycle	313	A <sup>2</sup> s
I <sub>GM</sub>	Peak Gate Current	20 μs max. Tj = 125 °C	4	А
P <sub>G(AV)</sub>	Average Gate Power Dissipation	Tj = 125 °C	1	W
dI/dt	Critical rate of rise of on-state current	$I_G = 2x I_{GT}, t_r \le 100 \text{ns}$	50	A/µs
		f = 120 Hz, T <sub>j</sub> = 125 °C		
T <sub>j</sub>	Operating Temperature		(-40 +125)	°C
T <sub>stg</sub>	Storage Temperature		(-40 +150)	°C
T <sub>sld</sub>	Soldering Temperature	10s max	260	°C

SYMBOL	PARAMETER	VOLTAGE					Unit
OTWIDOL		В	D	М	S	N	OTHE
$V_{DRM}/V_{RRM}$	Repetitive Peak Off State Voltage	200	400	600	700	800	V

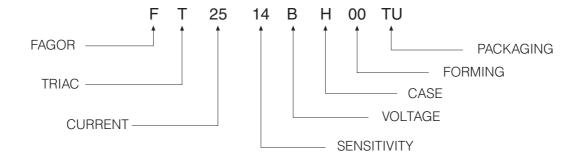


### Electrical Characteristics at Tamb = 25 °C

CVMPOL DADAMETED		CONDITIONS		Ou a dramt		SENSITIVITY		I India
SYMBOL	PARAMETER	CONDITIONS		Quadrant		14	16	Unit
I <sub>GT</sub> <sup>(1)</sup>	Gate Trigger Current	$V_D = 12 V_{DC}, R_L = 338$	$\Omega$ , $T_j = 25$ °C	Q1÷Q3	MAX	35	50	mA
V <sub>GT</sub>	Gate Trigger Voltage	$V_D = 12 V_{DC}, R_L = 33$	$\Omega$ , $T_j = 25$ °C	Q1÷Q3	MAX	1.3		V
$V_{GD}$	Gate Non Trigger Voltage	$V_D = V_{DRM}, R_L = 3.3 \text{ K}$	$\Omega$ , $T_j = 125  ^{\circ}C$	Q1÷Q3	MIN	0.2		V
I <sub>H</sub> <sup>(2)</sup>	Holding Current	I <sub>T</sub> =100 mA,Gate ope	en, $T_j = 25$ °C		MAX	35	50	mA
IL	Latching Current	$I_{G} = 1.2 I_{GT}, T_{j} = 25 °C$		Q1,Q3	MAX	50	70	mA
				Q2	MAX	60	80	mA
dV/dt (2)	Critical Rate of Voltage Rise	$V_D = 0.67 \times V_{DRM}$ , Ga	te open		MIN	500	1000	V/µs
		T <sub>j</sub> = 125 °C						
(dl/dt)c (2)	Critical Rate of Current Rise	$(dv/dt)c = 0.1 V/\mu s$	$T_j = 125  ^{\circ}C$		MIN	-	-	A/ms
		$(dv/dt)c = 10 V/\mu s$	$T_j = 125  ^{\circ}\text{C}$		MIN	-	-	
		without snubber	$T_j = 125  ^{\circ}\text{C}$		MIN	13	22	
V <sub>TM</sub> <sup>(2)</sup>	On-state Voltage	$I_T = 35 \text{ Amp, tp} = 380 \ \mu\text{s,} T_j = 25 \ ^{\circ}\text{C}$			MAX	1.55		V
V <sub>t (0)</sub> (2)	Threshold Voltage	T <sub>j</sub> = 125 °C			MAX	0.85		V
r <sub>d</sub> <sup>(2)</sup>	Dynamic resistance	T <sub>j</sub> = 125 °C			MAX	1	6	mΩ
I <sub>DRM</sub> /I <sub>RRM</sub>	Off-State Leakage Current	$V_D = V_{DRM}$ ,	T <sub>j</sub> = 125 °C		MAX	2	2	mA
		$V_R = V_{RRM}$ ,	$T_j = 25  ^{\circ}C$		MAX	Ę	5	μΑ
R <sub>th(j-c)</sub>	Thermal Resistance	for AC 360° conduction angle				1.0		°C/W
	Junction-Case							
R <sub>th(j-a)</sub>	Thermal Resistance					6	0	°C/W
	Junction-Ambient							

<sup>(1)</sup> Minimum  $I_{GT}$  is guaranted at 5% of  $I_{GT}$  max.

### **Part Number Information**



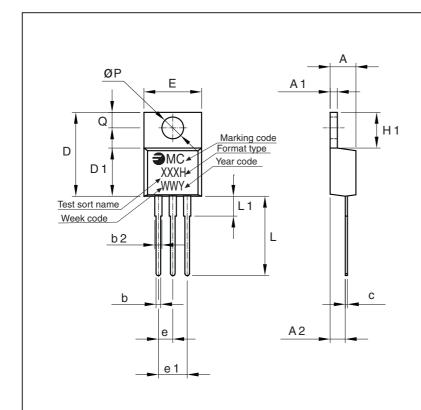
<sup>(2)</sup> For either polarity of electrode MT2 voltage with reference to electrode MT1.



## **Ordering information**

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
FT2516BH 00TU	TU	TUBE	1000	2.30

## Package Outline Dimensions: (mm) TO-220AB



	DIMENSIONS				
REF.	Milimeters				
	Min.	Max.			
А	4.47	4.67			
A1	1.17	1.37			
A2	2.52	2.82			
b	0.71	0.91			
b2	1.17	1.37			
С	0.31	0.53			
D	14.65	15.35			
D1	8.50	8.90			
Е	10.01	10.36			
е	2.51	2.57			
e1	4.98	5.18			
H1	6.15	6.45			
L	13.40	13.96			
L1	3.56	3.96			
Р	3.735	3.935			
Q	2.59	2.89			

Mounting Torque 0.5 N.m



## Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle)

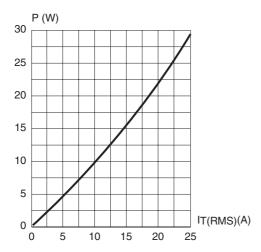


Fig. 3: Relative variation of thermal impedance versus pulse duration.

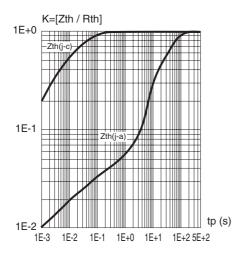


Fig. 5: Surge peak on-state current versus number of cycles

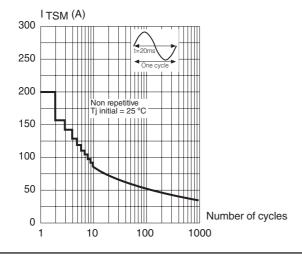


Fig. 2: RMS on-state current versus case temperature (full cycle).

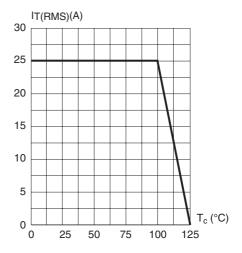


Fig. 4: On-state characteristics (maximum values)

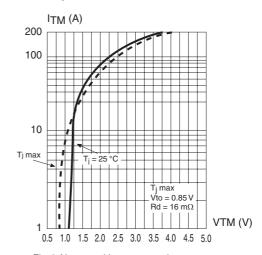
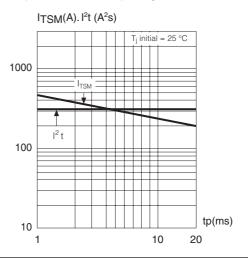


Fig. 6: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 20 ms, and corresponding value of I<sup>2</sup>t.





## Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 7: Relative variation of gate trigger current, holding current and latching versus junction temperature (typical values)

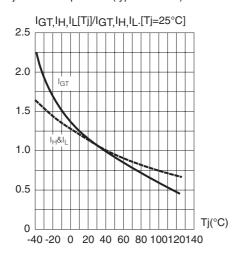
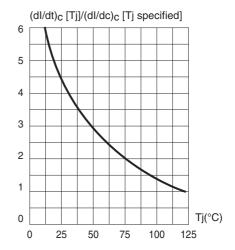


Fig. 8: Relative variation of critical rate of decrease of main current versus junction temperature





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