AEC-Q101 Qualified

4V Drive Pch MOS FET RSQ025P03FRA

Structure

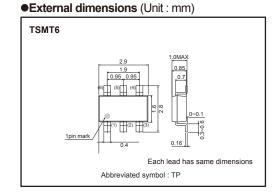
Silicon P-channel MOS FET

Features

- 1) Low On-resistance.(120m Ω at 4.5V)
- 2) High Power Package.(PD=1.25W)
- 3) High speed switching.
- 4) Low voltage drive. (4V)

Applications

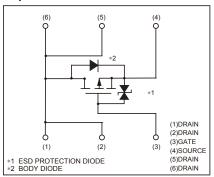
DC-DC converter



Packaging specifications

Туре	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ025P03F	0	

Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	-30	V
Gate-source voltage		Vgss	±20	V
Drain current	Continuous	ΙD	±2.5	A
Drain current	Pulsed	IDP *1	±10	A
Source current	Continuous	ls	-1	A
(Body diode)	Pulsed	Isp *1	-4	A
Total power dissipation		P _D *2	1.25	W
Channel temperature		Tch	150	°C
Range of Storage temperature		Tstg	-55 to +150	°C

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	100	°C / W

^{*} Mounted on a ceramic board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	-	_	±10	μΑ	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V _{(BR)DSS}	-30	_	_	V	I _D =-1mA, V _G S=0V	
Zero gate voltage drain current	IDSS	-	_	-1	μΑ	V _{DS} =-30V, V _{GS} =0V	
Gate threshold voltage	V _{GS(th)}	-1.0	_	-2.5	V	V _{DS} =-10V, I _D =-1mA	
		_	80	110	mΩ	I _D =-2.5A, V _G s=-10V	
Static drain-source on-state	RDS(on)*	-	120	165	mΩ	I _D =-2.5A, V _G S=-4.5V	
resistance		_	145	200	mΩ	I _D =-1.25A, V _G s=-4.0V	
Foward transfer admittance	Y _{fs} *	1.2	_	_	S	V _{DS} =-10V, I _D =-1.25A	
Input capacitance	Ciss	_	320	_	pF	V _{DS} =-10V,V _{GS} =0V f=1MHz	
Output capacitance	Coss	-	85	_	pF		
Reverse transfer capacitance	Crss	_	60	_	pF		
Turn-on delay time	td(on) *	-	8	_	ns	1 4054	
Rise time	tr *	_	11	_	ns	ID=-1.25A VDD≒-15V VGS=-10V RL=12Ω RG=10Ω	
Turn-off delay time	td(off) *	-	33	-	ns		
Fall time	t _f *	-	7	_	ns		
Total gate charge	Qg	_	4.4	_	nC	157	
Gate-source charge	Qgs	_	1.0	_	nC	VDD≒-15V VGS=-5V ID=-2.5A	
Gate-drain charge	Qgd	-	1.4	-	nC		

^{*}PULSED

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VsD	-	_	-1.2	V	Is=-0.9A, Vgs=0V

Fig.1 Typical Transfer Characteristics

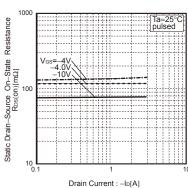


Fig.2 Static Drain–Source On–State Resistance vs. Drain Current

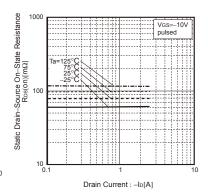


Fig.3 Static Drain-Source On-State Resistance vs.Drain Current

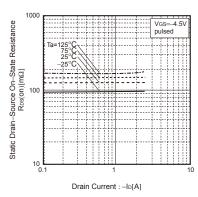


Fig.4 Static Drain–Source On–State Resistance vs. Drain–Current

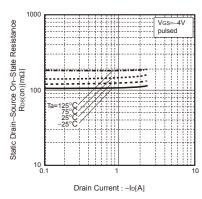


Fig.5 Static Drain–Source On–State Resistance vs.Drain–Current

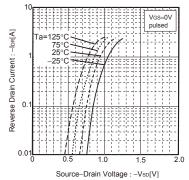


Fig.6 Reverse Drain Current Source-Drain Voltage

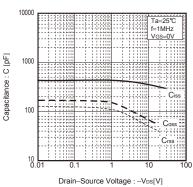


Fig.7 Typical Capactitance vs.Drain-Source Voltage

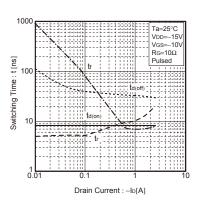
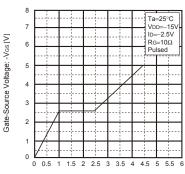


Fig.8 Switching Characteristics



Total Gate Charge : Qg[nC]
Fig.9 Dynamic Input Characteristics

Measurement circuits

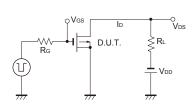


Fig.10 Switching Time Measurement Circuit

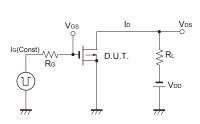


Fig.12 Gate Charge Measurement Circuit

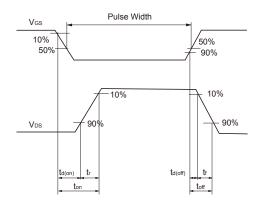


Fig.11 Switching Waveforms

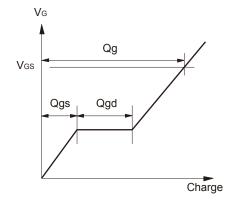


Fig.13 Gate Charge Waveforms

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JAPAN	USA	EU	CHINA	
CLASSⅢ	CLACCIII	CLASS II b	СГУССШ	
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ	

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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