AEC-Q101 Qualified

2.5V Drive Nch MOS FET

RTR020N05FRA

Structure

Silicon N-channel MOS FET

Features

- 1) Low On-resistance.
- 2) Space saving, small surface mount package (TSMT3).
- 3) Low voltage drive (2.5V drive).

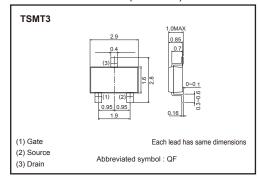
Applications

Switching

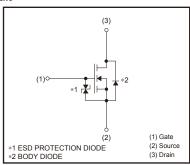
●Packaging specifications and hFE

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	3000
RTR020N05	0	

●External dimensions (Unit : mm)



•Inner circuit



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V _{DSS}	45	V	
Gate-source voltage	V _{GSS}	12	V	
Drain current	Continuous	I _D	±2.0	Α
Drain current	Pulsed	I _{DP} *1	±8	Α
Source current	Continuous	Is	0.8	Α
(Body diode)	Pulsed	I _{SP} *1	8	Α
Total power dissipation	P _D *2	1.0	W	
Channel temperature	Tch	150	°C	
Range of storage temperature	Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	125	°C/W

^{*} Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	I _{GSS}	-	-	10	μΑ	V _{GS} =12V, V _{DS} =0V	
Drain-source breakdown voltage	V _{(BR) DSS}	45	-	_	V	I _D = 1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	-	_	1	μΑ	V _{DS} = 45V, V _{GS} =0V V _{DS} = 10V, I _D = 1mA	
Gate threshold voltage	V _{GS (th)}	0.5	_	1.5	V		
Otalia dada a a a a a a a a a a a		-	130	180	mΩ	I _D = 2.0A, V _{GS} = 4.5V	
Static drain-source on-state resistance	RDS (on)*	_	135	190	mΩ	I _D = 2.0A, V _{GS} = 4V	
resistance		_	180	250	mΩ	ID= 2.0A, VGS= 2.5V	
Forward transfer admittance	Y _{fs} *	1.5	_	_	S	V _{DS} = 10V, I _D = 2.0A	
Input capacitance	Ciss	-	200	_	pF	V _{DS} = 10V	
Output capacitance	Coss	_	45	_	pF	V _{GS} =0V f=1MHz	
Reverse transfer capacitance	Crss	_	25	_	pF		
Turn-on delay time	t _{d (on)} *	-	11	_	ns	V _{DD} ≒ 25V	
Rise time	tr *	_	16	_	ns	I _D = 1.0A V _G = 4.5V R _I =25Ω	
Turn-off delay time	td (off) *	_	21	_	ns		
Fall time	t _f *	_	11	_	ns	R _G =10Ω	
Total gate charge	Qg *	_	2.9	4.1	nC	V _{DD} ≒25V V _{GS} =4.5V	
Gate-source charge	Qgs *	_	0.7	-	nC	I _D = 2.0A	
Gate-drain charge	Q _{gd} *	_	0.9	_	nC	RL=12.5 Ω R _G =10 Ω	

^{*}Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VsD	_	_	1.2	V	I _S = 0.8A, V _{GS} =0V

Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	CHINA		
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII	
CLASSIV	CLASSⅢ	CLASSIII	CLASSⅢ	

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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QR code printed on ROHM Products label is for ROHM's internal use only.

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