NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Datasheet

AEC-Q101 Qualified

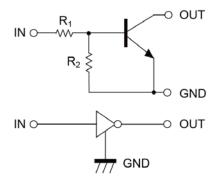
Parameter	Value
V _{CC}	50V
I _{C(MAX.)}	100mA
R ₁	1kΩ
R ₂	10kΩ

Outline UMT3 SMT3 OUT GND DTC113ZUAFRA SOT-323(SC-70) DTC113ZKAFRA SOT-346(SC-59)

Features

- 1) Built-In Biasing Resistors
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types: DTA113Z series
- 6) Lead Free/RoHS Compliant.

•Inner circuit



Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC113ZUAFRA	UMT3	2021	T106	180	8	3000	121
DTC113ZKAFRA	SMT3	2928	T146	180	8	3000	E21

● Absolute maximum ratings (T_a = 25°C)

Parameter			Values	Unit
Supply voltage			50	V
Input voltage			-5 to 10	V
Output current			100	mA
Collector current	I _{C(MAX)} *1	100	mA	
Daniel dia dia attau	DTC113ZUAFRA	D *2	200	\^/
Power dissipation	P_{D}^{*2}	200	mW	
Junction temperature	Tj	150	°C	
Range of storage tempera	T _{stg}	-55 to +150	°C	

● Electrical characteristics (T_a = 25°C)

Davamatar	Cymah ol	Canditions	Values			l limit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Innut valtage	$V_{l(off)}$	V _{CC} = 5V, I _O = 100μA	-	-	0.3	V	
Input voltage	V _{I(on)}	$V_O = 0.3V$, $I_O = 20mA$	3	_	-	V	
Output voltage	V _{O(on)}	$I_{O}/I_{I} = 10 \text{mA} / 0.5 \text{mA}$	-	0.1	0.3	V	
Input current	I ₁ V ₁ = 5V		-	-	7.2	mA	
Output current	$I_{O(off)}$ $V_{CC} = 50V, V_I = 0V$		-	-	0.5	μA	
DC current gain	G _I	G_{I} $V_{O} = 5V, I_{O} = 5mA$		_	-	-	
Input resistance	R ₁	-	0.7	1	1.3	kΩ	
Resistance ratio R ₂ /R ₁		-	8	10	12	-	
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

●Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

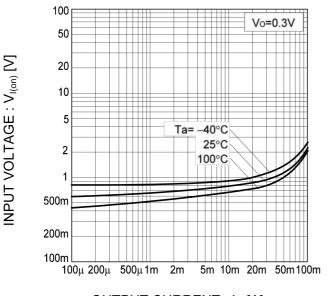
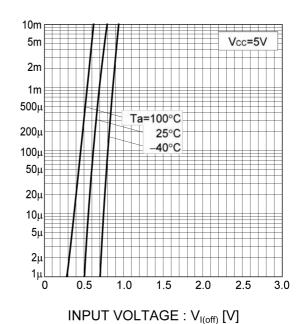


Fig.2 Output current vs. input voltage (OFF characteristics)



OUTPUT CURRENT : Io [A]

OUTPUT CURRENT: Io [A]

Fig.3 Output current vs. output voltage

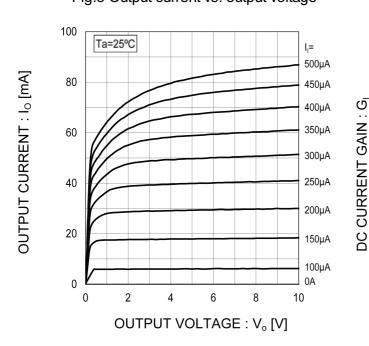
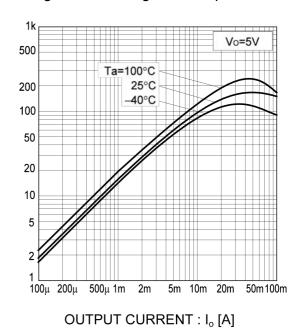
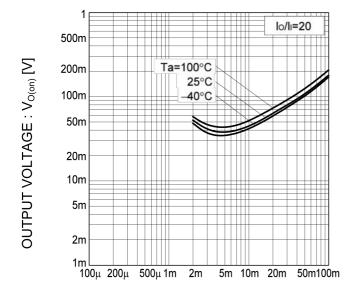


Fig.4 DC current gain vs. output current



● Electrical characteristic curves (T_a =25°C)

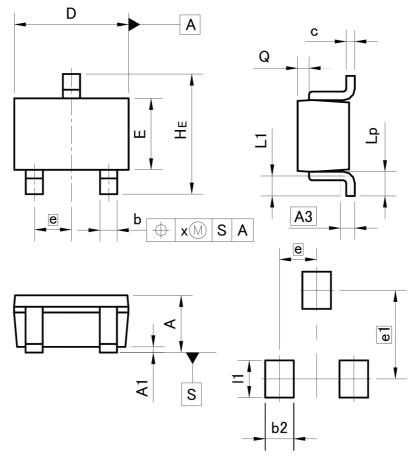
Fig.5 Output voltage vs. output current



OUTPUT CURRENT : I_o [A]

Dimensions

UMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.15	0.30	0.006	0.012	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.0	65	0.0	0.026	
HE	2.00	2.20	0.079	0.087	
L1	0.20	0.50	0.008	0.020	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
х	=	0.10	=	0.004	

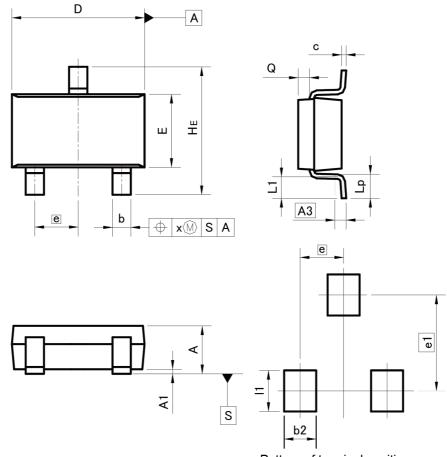
DIM	MILIMETERS		INC	INCHES	
DIM	MIN	MAX	MIN	MAX	
b2	_	0.50	_	0.020	
e1	1.55		0.0	061	
11	_	0.65	_	0.026	

Dimension in mm/inches



Dimensions

SMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.:	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.95		0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
X	E	0.10	722	0.004
У		0.10		0.004
	MILIMI	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	_	0.60	_	0.024
e1	2	10	0.083	

Dimension in mm/inches

11



0.035

0.90

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Ì	JÁPAN	USA	EU	CHINA
Γ	CLASSⅢ	CL ACCTI	CLASS II b	CI VCCIII
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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