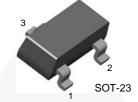


October 2014

# **KST2222A NPN Epitaxial Silicon Transistor**

#### **Features**

· General-Purpose Transistor



1. Base 2. Emitter 3. Collector

# **Ordering Information**

Part Number	Marking	Package	Packing Method
KST2222AMTF	1P	SOT-23 3L	Tape and Reel

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	75	V
$V_{CEO}$	Collector-Emitter Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
I <sub>C</sub>	Collector Current	600	mA
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

### Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
D	Power Dissipation	350	mW
$P_{D}$	Derate Above 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	357	°C/W

## Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

# **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10 \mu A, I_E = 0$	75		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 10 \text{ mA}, I_B = 0$	40		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10 \mu A, I_C = 0$	6		V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = 60 \text{ V}, I_{E} = 0$		0.01	μΑ
		$V_{CE} = 10 \text{ V}, I_{C} = 0.1 \text{ mA}$	35		
		$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$	50		
h <sub>FE</sub>	DC Current Gain <sup>(2)</sup>	$V_{CE} = 10 \text{ V}, I_{C} = 10 \text{ mA}$	75		
		$V_{CE} = 10 \text{ V}, I_{C} = 150 \text{ mA}$	100	300	
		$V_{CE} = 10 \text{ V}, I_{C} = 500 \text{ mA}$	40		
M ( 0	0-11-1-1-5	$I_C = 150 \text{ mA}, I_B = 15 \text{ mA}$		0.3	V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(2)</sup>	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		1.0	
V <sub>BE</sub> (sat)	Page Emitter Seturation Valtage(2)	$I_C = 150 \text{ mA}, I_B = 15 \text{ mA}$	0.6	1.2	V
	Base-Emitter Saturation Voltage <sup>(2)</sup>	$I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		2.0	
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = 20 mA, V <sub>CE</sub> = 20 V, f = 100 MHz	300		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz		8	pF
NF	Noise Figure	$I_C$ = 100 μA, $V_{CE}$ = 10 V, $R_S$ = 1 kΩ, $f$ = 1 MHz		4	dB
t <sub>ON</sub>	Turn-On Time	$V_{CC} = 30 \text{ V}, I_{C} = 150 \text{ mA},$ $V_{BE} = 0.5 \text{ V}, I_{B1} = 15 \text{ mA}$		35	ns
t <sub>OFF</sub>	Turn-Off Time	$V_{CC} = 30 \text{ V}, I_{C} = 150 \text{ mA},$ $I_{B1} = I_{B2} = 15 \text{ mA}$		285	ns

# Note:

2. Pulse test: Pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%

# **Typical Performance Characteristics**

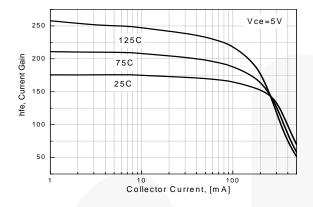


Figure 1. DC Current Gain

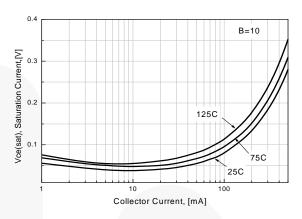


Figure 2. Collector-Emitter Saturation Voltage

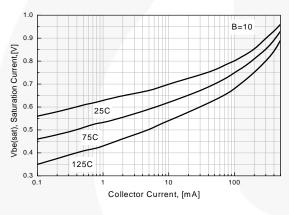


Figure 3. Base-Emitter Saturation Voltage

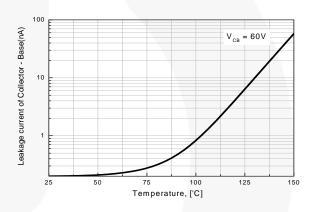


Figure 4. Collector-Base Leakage Current

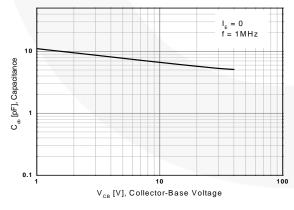


Figure 5. Output Capacitance

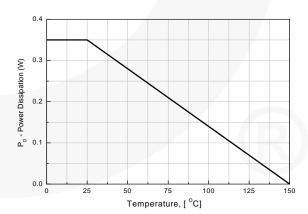
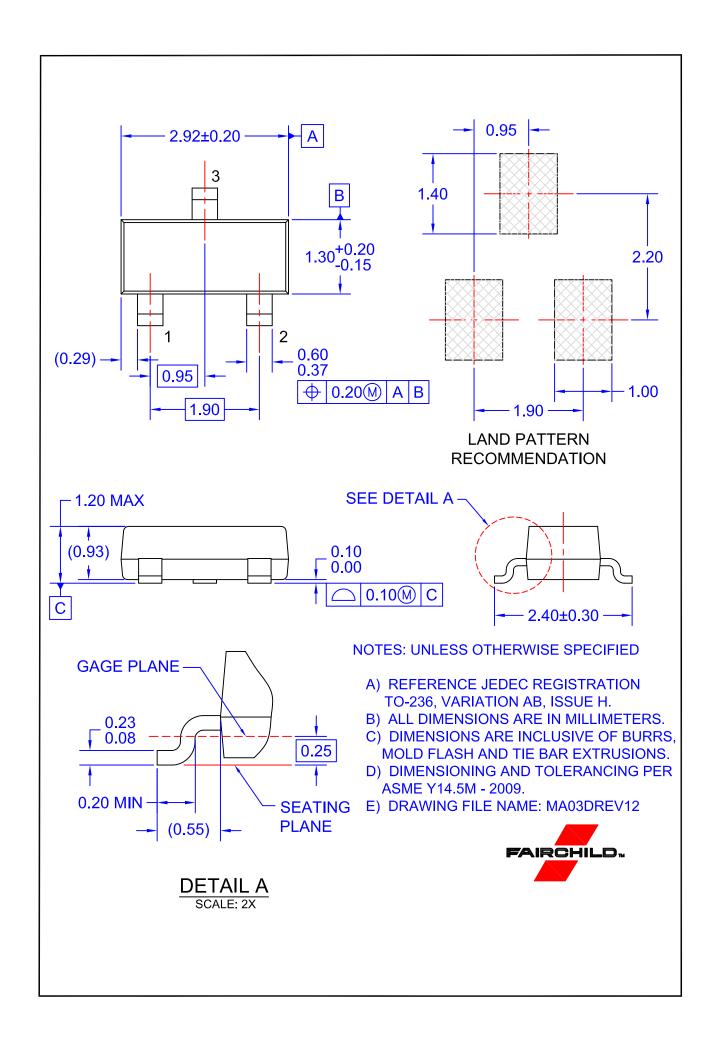


Figure 6. Power Dissipation vs. Ambient Temperature







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Definition of Terms			
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