

Multilayer Ceramic Capacitors for High Frequency Applications

UVK105CH2R4JW-F



■ Features

- Item Summary
2.4pF±5%, 50V,
C0H/CH, 0402/1005 (EIA/JIS)
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping paper 10000pcs

■ Products characteristics table

Capacitance	2.4 pF ± 5 %
Case Size (EIA/JIS)	0402/1005
Rated Voltage	50 V
Q (min)	170 at 1GHz
Temperature Characteristic (EIA)	C0H
Operating Temp. Range (EIA)	-55 to +125 °C
Temperature Characteristic (JIS)	CH
Operating Temp. Range (JIS)	-55 to +125 °C
Temperature Characteristic	0 ±60 ppm/°C
HALT (% Rated Voltage)	200 %
Insulation Resistance (min)	10 GΩ
RoHS2 Compliance	Yes
REACH Compliance	Yes
Soldering	Reflow

■ External Dimensions

Dimension L	1.0 ±0.05 mm
Dimension W	0.5 ±0.05 mm
Dimension T	0.5 ±0.05 mm
Dimension e	0.25 ±0.10 mm

2016.01.25

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor

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-Electrical Characteristics Data-

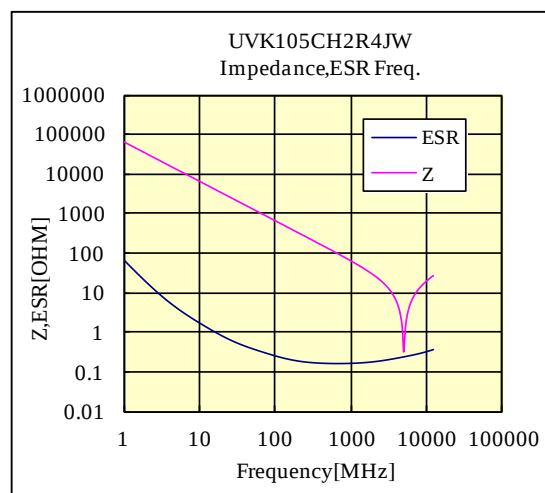
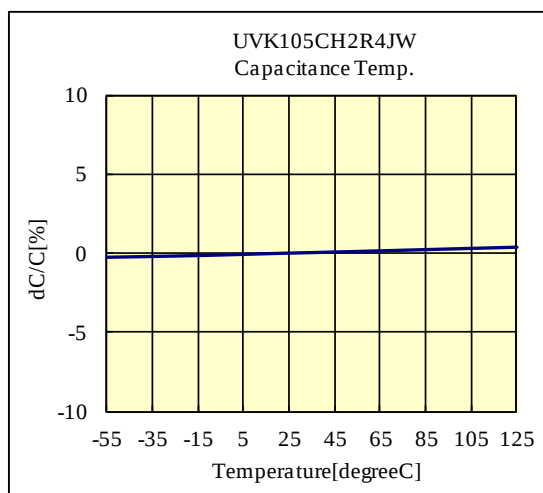
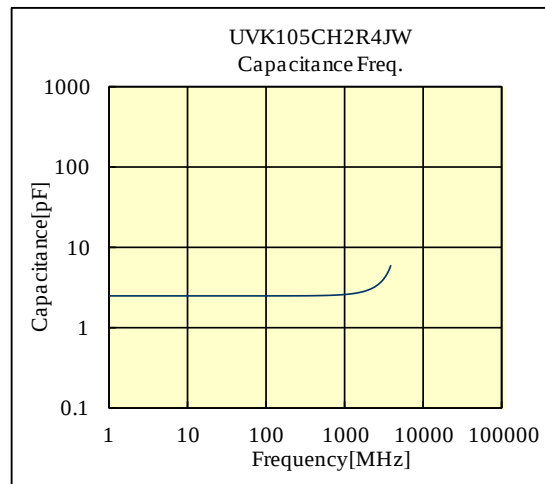
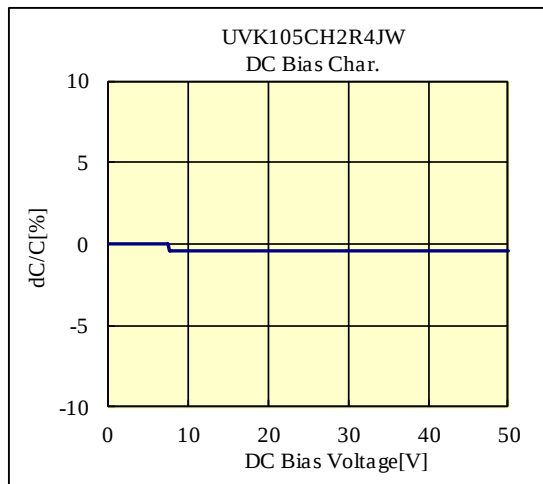
15RVD06

RoHS compliance product

○UVK105CH2R4JW

50V 1005 CH/C0H 2.4pF +/- 5% t=0.55mmMAX

50V 0402 CH/C0H 2.4pF +/- 5% t=0.022inchMAX



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