

# AM335x General-Purpose EVM (Revision 1.1)

## Errata



Literature Number: SPRUHD7  
January 2012



## ***AM335x General-Purpose EVM (Revision 1.1)***

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### **1 Introduction**

This document describes the known exceptions to the functional specifications for the AM335x general-purpose EVM (GP EVM). For additional information, see the *AM335x General-Purpose EVM User's Guide* (literature number TBD).

### **2 Revision 1.1 Known Design Exceptions to Functional Specifications**

[Table 1](#) lists known design exceptions to functional specifications for general-purpose EVM revision 1.1. Advisories are numbered in the order in which they were added to this document. If the design exceptions are still applicable, the advisories move up to the latest EVM revision section. If the design exceptions are no longer applicable or if the information has been documented elsewhere, those advisories are removed. Therefore, advisory numbering in this section may not be sequential.

**Table 1. Revision 1.1 Advisory List**

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**Advisory 1.1.1      *Keypad Capacitor Filter Issue***

**Revisions Affected:**      1.1A

**Details:**                      The keypad on the general-purpose daughterboard has three capacitors which are meant to filter key bounces. These capacitors may interfere with software builds that use alternate means to read the keypad switches and filter the mechanical bounce from the switches.

**Workaround:**                Remove C125-C127 from the general-purpose daughterboard.

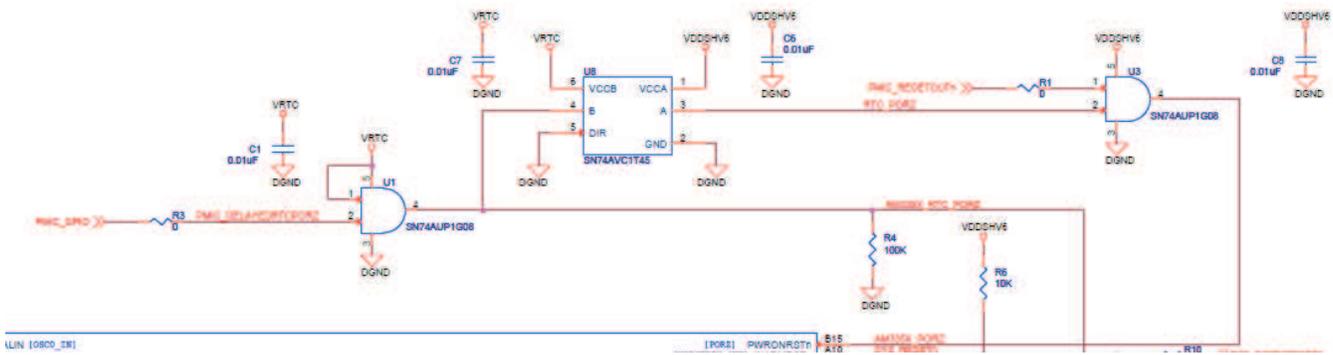
**Advisory 1.1.2      *PORz Reset Circuit***

**Revisions Affected:**      1.1A

**Details:**                      The logic driving the PWRONRSTn input on the AM335x device is at 1.8-V level instead of the VDDSHV6 level (3.3 V). Since this input generates a reset when active, this lower level may drive the board into reset when not intended.

**Workaround:**

- Remove U1.
- Add an SN74AUP1G08, an SN74AVC1T45, and an SN74AUP1G08 so that PWRONRSTn on the AM335x device is driven to the correct voltage level (see [Figure 1](#)).



**Figure 1. PORz Circuit**

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