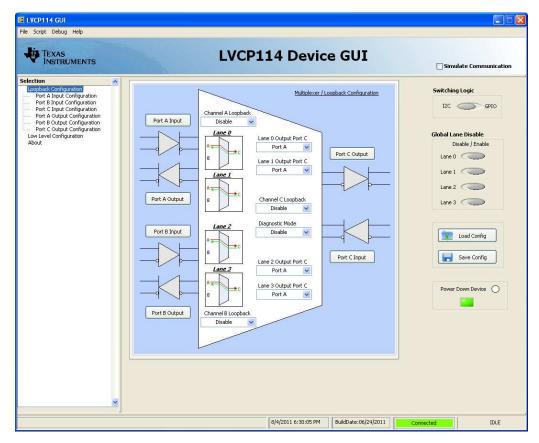


SN65LVCP114 Evaluation Module (EVM) Graphical User Interface

This User's Guide describes the usage and content of the SN65LVCP114 EVM graphical user interface (GUI). This document provides a basic overview of the different sections of the program.



WARNING

This equipment is intended for use in a laboratory test environment only. It generates, uses, and can radiate radio frequency energy and has not been tested for compliance with the limits of computing devices, pursuant to subpart J, part 15 of FCC rules. These rules are designed to provide reasonable protection against radio frequency interference. Operation of this equipment in other environments may cause interference with radio communications, in which case the user, at their own expense, must take whatever measures are necessary to correct this interference.



1	Introduction	3
	High-Level Operation of the GUI	
3	Low-Level Operation of the GUI	10

List of Figures

1	SN65LVCP114 EVM GUI About Window	3
2	SN65LVCP114 EVM GUI Loopback Configuration Window	4
3	SN65LVCP114 EVM GUI Port X Input Configuration	5
4	SN65LVCP114 EVM GUI Port X Output Configuration	6
5	SN65LVCP114 EVM GUI Loopback Configuration	7
6	SN65LVCP114 EVM GUI Diagnostic Mode	8
7	SN65LVCP114 EVM GUI Global Settings	9
8	SN65LVCP114 EVM GUI Low-Level Register Configuration Window	10



1 Introduction

The Texas Instruments (TI) SN65LVCP114 evaluation module (EVM) board is controlled and configured using a custom GUI.

High- and low-level control of the registers is possible using this GUI, as are a variety of built-in test modes.

Refer to the SN65LVCP114 EVM User's Guide for proper jumper settings on the EVM, to set the device in I2C mode, and to use the GUI.

2 High-Level Operation of the GUI

E LVCP114 GUI	
File Script Debug Help	
	LVCP114 Device GUI
Selection Voopback Configuration Port & Input Configuration Port & Input Configuration Port & Input Configuration Port & Output Configuration Port & Output Configuration Port & Output Configuration Low Level Configuration About	The GUI has two ways to test the device: Loopback Configuration and Low Level Configuration. Both of them achieve the same purpose. 1. Loopback Configuration: Uses the high level functions to configure the device during the test. 2. Low Level Configuration: Directly control the registers of the device during the test. 3. Each configuration at the Loopback Configuration also affect the Loopback Configuration area. If the user starts the test from Loopback Configuration, without the need to start over the test. 4. Status Log Window Double click on Status Bar to see the Status Log Window. Copyright (c) 2011 National Instruments Corporation. All Rights Reserved
	8/4/2011 6:30:54 PM BuildDate:06/24/2011 Connected IDLE

Figure 1. SN65LVCP114 EVM GUI About Window

When the GUI starts, the indicator at the bottom right corner of the window is red and displays *Working*. At this point, the GUI is establishing communication with the USB port of the EVM and setting the default configuration required for proper operation of both the board and GUI. When the indicator turns green, communication has been successfully established between the GUI and EVM board.

Introduction



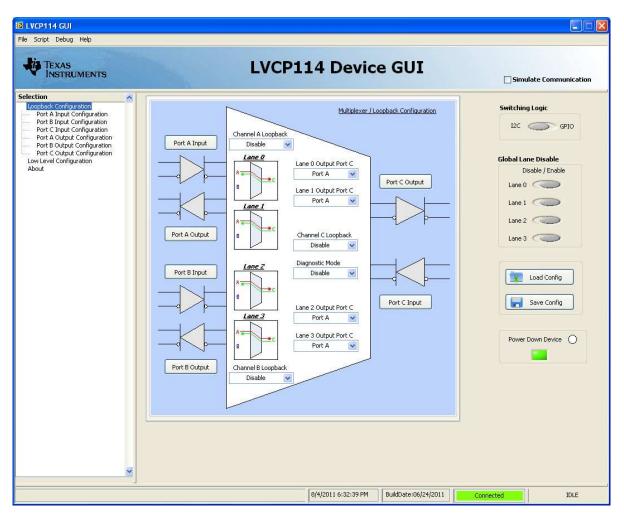


Figure 2. SN65LVCP114 EVM GUI Loopback Configuration Window

Register settings of the SN65LVCP114 can be modified from selections on this tab. Functions are grouped into individual windows.



TEXAS INSTRUMENTS	LVCP114 Device GUI	Simulate Communication
Selection Lopback Configuration Port A Input Configuration Port C Input Configuration Port B Output Configuration Port B Output Configuration Dent C Output Configuration Low Level Configuration About	Port A Input Configuration Input Stages Power Down Mode EQ Control (dB) Gain 1.3 I.ane 0 Polarity Lane 0 Polarity Lane 2 Polarity Lane 2 Polarity Lane 3 Polarity Normal Normal	Switching Logic I2C GPIO Clobal Lane Disable Disable / Enable Lane 0 Lane 1 Lane 2 Lane 3 Clobal Lane Origination Lane 3 Clobal Lane Disable Lane 1 Lane 2 Lane 3 Clobal Lane Disable Lane 1 Lane 2 Lane 3 Clobal Lane Disable Lane 2 Lane 3 Clobal Lane Disable Lane 2 Lane 3 Clobal Lane Disable Lane 2 Lane 3 Clobal Lane 2 Clobal Lane Disable Lane 2 Clobal Lane 1 Clobal Lane Disable Lane 2 Clobal Lane 3 Clobal
	8/5/2011 4:23:16 PM BuildDate:08/05/2011	Connected IDLE

Figure 3. SN65LVCP114 EVM GUI Port X Input Configuration

Selecting the *Port X, (A, B, C), Input* button in the main window or the Port X, (A, B, C), Input Configuration menu in the Selection tab opens the Port X Input parameters. To return to the main window, click the *Back* button or click the desired window on the Selection tab. All changes are instantly sent through I2C to the device after any change is made to the configuration.



🖻 LVCP114 GUI		
File Script Debug Help		Dimensional Association
TEXAS INSTRUMENTS	LVCP114 Device GUI	Simulate Communication
Selection	Port A Dutput Configuration Disable Lane 0 Disable Lane 1 Disable Lane 2 Swing (mVpp) Disable Lane 3 Disable AGC Loop Fast Switch Idle outputs are turned off (save power)	Switching Logic IZC GPIO Global Lane Disable Lane 0 Lane 1 Lane 2 Lane 3 Config Save Config Power Down Device O Back
	8/5/2011 4:26:28 PM BuildDate:08/05/2011	Connected IDLE

Figure 4. SN65LVCP114 EVM GUI Port X Output Configuration

Selecting the *Port X, (A, B, C), Output* button on the main window or the Port X, (A, B, C), Output Configuration menu on the Selection tab opens the Port X Output parameters. To return to the main window, click the *Back* button or click the desired window on the Selection tab. All changes are instantly sent through I2C to the device after any change is made to the configuration.



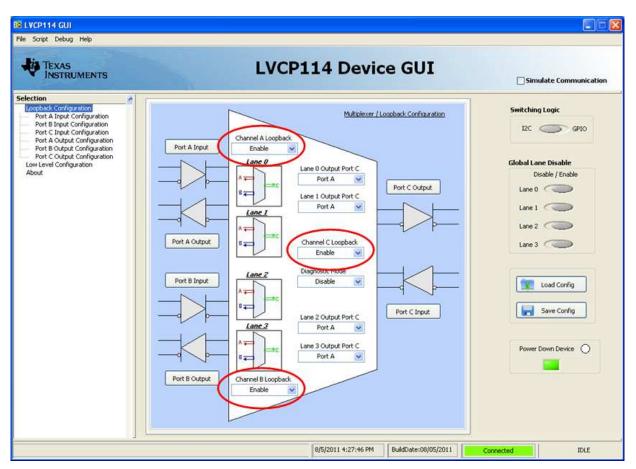


Figure 5. SN65LVCP114 EVM GUI Loopback Configuration

Channel A, B and C Loopback mode can be enabled or disabled on the main window using the pull-down menus. The Lane X displayed configurations update according to the selected configuration. All changes are instantly sent through I2C to the device after any change is made to the configuration.



High-Level Operation of the GUI

www.ti.com

S LVCP114 GUI File Script Debug Help		
TEXAS INSTRUMENTS	LVCP114 Device GUI	Simulate Communication
Jelection Port A Input Configuration Port B Input Configuration Port B Input Configuration Port Configuration Port A Output Configuration Port 8 Output Configuration Port 8 Output Configuration Low Level Configuration About	Port A Input Channel A Loopback Port A Input Disable Lane Ø Lane Ø Lane Ø Lane Ø Port A Output Port A Port A Output Eane Ø Port A Output Disable Port B Input Eane Ø Port B Input Lane 2 Output Port C Port A Port B Output Lane 2 Output Port C Port A Port B Output Lane 2 Port B Output Channel B Loopback Disable Insee 3 Output Port C Port B Output Disable	Switching Logic I2C GP10 Global Lane Disable Disable / Enable Lane 0 Lane 1 Lane 2 Lane 2 Lane 3 Config Save Config Power Down Device
-	8/5/2011 4:43:39 PM BuildDate:08/05/2011	Connected IDLE

Figure 6. SN65LVCP114 EVM GUI Diagnostic Mode

Diagnostic mode can be enabled or disabled on the main window using the pull-down menus. The Lane X drawings update according to the selected configuration. All changes are instantly sent through I2C to the device after any change is made to the configuration.



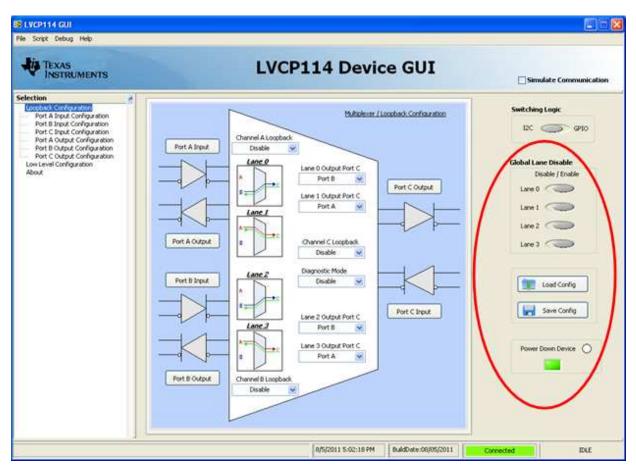


Figure 7. SN65LVCP114 EVM GUI Global Settings

Global registers are always available on the right side of the window. The *Power Down Device* button is set in register address 0x00 bit 6. For power down and I2C reset, jumper 5 must be toggled on the EVM. Refer to the EVM User's Guide for more details.

GUI settings can be saved and loaded for future use and convenience. The *Save Config* button saves all the register settings into a text file that can be loaded at any time using the *Load Config* button.

3 Low-Level Operation of the GUI

The SN65LVCP114 Device registers and settings can be controlled manually through a low-level register Read/Write portion of the GUI.

Script Debug Help	L	VC	P11	.4 C	Dev	/ic	e G	UI	Simulate Communication	
	Register Map									
Loopback Configuration Port A Input Configuration	Block / Register Name	Addres	s Default	Mode	Size	LW*	LR* 🔨	Write Data	Register Data	
Port B Input Configuration Port C Input Configuration Port A Output Configuration Port B Output Configuration Port B Output Configuration Low Level Configuration About	GENERAL_SETTINGS SWITCHING_LOGIC PORT_A_CONTROL_SETTINGS PORT_A_INPUT_SETTINGS PORT_A_RESERVED_SETTINGS PORT_B_IONTROL_SETTINGS PORT_B_IONTUT_SETTINGS PORT_B_OUTPUT_SETTINGS PORT_B_RESERVED_SETTINGS PORT_C_CONTROL_SETTINGS PORT_C_RESERVED_SETTINGS PORT_C_RESERVED_SETTINGS RESERVED_1 RESERVED_2	0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x06 0x07 0x08 0x09 0x0A 0x08 0x02 0x0D 0x0D 0x0E 0x0F	0x00 0x08 0x00 0x08 0x00 0x00 0x00 0x00	R/W R/W R/W R/W R/W R/W R/W R/W R/W R/W	8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	0×00 0×08 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00 0×00	0x00 0x00	O Write Register Read Data × 0 Read Register Current Address	0 SEL_0[0:0] 1 SEL_1[1:1] 2 SEL_2[2:2] 3 SEL_3[3:3] 4 LOOP_4[4:4] 5 LOOP_5[5:5] 6 LOOP_C[6:6] 7 DIAG[7:7]	
	POLARITY_CONTROL_A&B POLARITY_CONTROL_A&B POLARITY_CONTROL LANE_CONTROL RESERVED_SETTINGS_3 RESERVED_SETTINGS_6 RESERVED_SETTINGS_6 RESERVED_SETTINGS_8 RESERVED_SETTINGS_9	0x10 0x11 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19	0x00 0x00 0x00 0x00 0x00 0x00 0x00 0x0	R/W R/W R/W R/W R/W R/W R/W R/W R/W	8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	0x00 0x00 0x0F 0x00 0x00 0x00 0x00 0x00	0x00 0x00 0x0F 0x00 0x00 0x00 0x00 0x00	* 1	Transfer Read to Writ	
	Register Description DIAG[7:7] Enables data on Port C to be available on bo 0 = Disable 1 = Enable LOOP_[6:6] Enable Loop BackControl for Port C 0 = Disable 1 = Enable LooP_G[5:5]	th Ports A	and B						Default Device Address Custom Device Address 00	

Figure 8. SN65LVCP114 EVM GUI Low-Level Register Configuration Window

Selecting the Low-Level Configuration Tab brings up a complete register list for the SN65LVCP114. Selecting one of these registers loads the Register Description and Register Data fields with the proper values and displays the current value. The bits can be set by clicking in the check boxes next to the bit's name or typing the full HEX value for the register directly into the Write Data field. Clicking the *Write Register* button writes the register of the device. Reading the register is done by clicking the *Read Data* button. After a Read or Write operation the LW (Last Written) or LR (Last Read) fields in the register list are updated for future reference.

The GUI's mid-level array synchronizes the high-level control indicator values so navigation between the high- and low-level portions of the GUI is possible.

EVALUATION BOARD/KIT/MODULE (EVM) ADDITIONAL TERMS

Texas Instruments (TI) provides the enclosed Evaluation Board/Kit/Module (EVM) under the following conditions:

The user assumes all responsibility and liability for proper and safe handling of the goods. Further, the user indemnifies TI from all claims arising from the handling or use of the goods.

Should this evaluation board/kit not meet the specifications indicated in the User's Guide, the board/kit may be returned within 30 days from the date of delivery for a full refund. THE FOREGOING LIMITED WARRANTY IS THE EXCLUSIVE WARRANTY MADE BY SELLER TO BUYER AND IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED, IMPLIED, OR STATUTORY, INCLUDING ANY WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE. EXCEPT TO THE EXTENT OF THE INDEMNITY SET FORTH ABOVE, NEITHER PARTY SHALL BE LIABLE TO THE OTHER FOR ANY INDIRECT, SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES.

Please read the User's Guide and, specifically, the Warnings and Restrictions notice in the User's Guide prior to handling the product. This notice contains important safety information about temperatures and voltages. For additional information on TI's environmental and/or safety programs, please visit www.ti.com/esh or contact TI.

No license is granted under any patent right or other intellectual property right of TI covering or relating to any machine, process, or combination in which such TI products or services might be or are used. TI currently deals with a variety of customers for products, and therefore our arrangement with the user is not exclusive. TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein.

REGULATORY COMPLIANCE INFORMATION

As noted in the EVM User's Guide and/or EVM itself, this EVM and/or accompanying hardware may or may not be subject to the Federal Communications Commission (FCC) and Industry Canada (IC) rules.

For EVMs **not** subject to the above rules, this evaluation board/kit/module is intended for use for ENGINEERING DEVELOPMENT, DEMONSTRATION OR EVALUATION PURPOSES ONLY and is not considered by TI to be a finished end product fit for general consumer use. It generates, uses, and can radiate radio frequency energy and has not been tested for compliance with the limits of computing devices pursuant to part 15 of FCC or ICES-003 rules, which are designed to provide reasonable protection against radio frequency interference. Operation of the equipment may cause interference with radio communications, in which case the user at his own expense will be required to take whatever measures may be required to correct this interference.

General Statement for EVMs including a radio

User Power/Frequency Use Obligations: This radio is intended for development/professional use only in legally allocated frequency and power limits. Any use of radio frequencies and/or power availability of this EVM and its development application(s) must comply with local laws governing radio spectrum allocation and power limits for this evaluation module. It is the user's sole responsibility to only operate this radio in legally acceptable frequency space and within legally mandated power limitations. Any exceptions to this are strictly prohibited and unauthorized by Texas Instruments unless user has obtained appropriate experimental/development licenses from local regulatory authorities, which is responsibility of user including its acceptable authorization.

For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

For EVMs annotated as IC – INDUSTRY CANADA Compliant

This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Concerning EVMs including radio transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concerning EVMs including detachable antennas

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

[Important Notice for Users of this Product in Japan]

This development kit is NOT certified as Confirming to Technical Regulations of Radio Law of Japan

If you use this product in Japan, you are required by Radio Law of Japan to follow the instructions below with respect to this product:

- Use this product in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
- 3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

Texas Instruments Japan Limited (address) 24-1, Nishi-Shinjuku 6 chome, Shinjuku-ku, Tokyo, Japan

http://www.tij.co.jp

【ご使用にあたっての注】

本開発キットは技術基準適合証明を受けておりません。

本製品のご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

- 1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
- 2. 実験局の免許を取得後ご使用いただく。
- 3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。

日本テキサス・インスツルメンツ株式会社 東京都新宿区西新宿6丁目24番1号 西新宿三井ビル http://www.tij.co.jp

EVALUATION BOARD/KIT/MODULE (EVM) WARNINGS, RESTRICTIONS AND DISCLAIMERS

For Feasibility Evaluation Only, in Laboratory/Development Environments. Unless otherwise indicated, this EVM is not a finished electrical equipment and not intended for consumer use. It is intended solely for use for preliminary feasibility evaluation in laboratory/development environments by technically qualified electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems and subsystems. It should not be used as all or part of a finished end product.

Your Sole Responsibility and Risk. You acknowledge, represent and agree that:

- 1. You have unique knowledge concerning Federal, State and local regulatory requirements (including but not limited to Food and Drug Administration regulations, if applicable) which relate to your products and which relate to your use (and/or that of your employees, affiliates, contractors or designees) of the EVM for evaluation, testing and other purposes.
- 2. You have full and exclusive responsibility to assure the safety and compliance of your products with all such laws and other applicable regulatory requirements, and also to assure the safety of any activities to be conducted by you and/or your employees, affiliates, contractors or designees, using the EVM. Further, you are responsible to assure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard.
- 3. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.
- 4. You will take care of proper disposal and recycling of the EVM's electronic components and packing materials.

Certain Instructions. It is important to operate this EVM within TI's recommended specifications and environmental considerations per the user guidelines. Exceeding the specified EVM ratings (including but not limited to input and output voltage, current, power, and environmental ranges) may cause property damage, personal injury or death. If there are questions concerning these ratings please contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, some circuit components may have case temperatures greater than 60°C as long as the input and output are maintained at a normal ambient operating temperature. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors which can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during normal operation, please be aware that these devices may be very warm to the touch. As with all electronic evaluation tools, only qualified personnel knowledgeable in electronic measurement and diagnostics normally found in development environments should use these EVMs.

Agreement to Defend, Indemnify and Hold Harmless. You agree to defend, indemnify and hold TI, its licensors and their representatives harmless from and against any and all claims, damages, losses, expenses, costs and liabilities (collectively, "Claims") arising out of or in connection with any use of the EVM that is not in accordance with the terms of the agreement. This obligation shall apply whether Claims arise under law of tort or contract or any other legal theory, and even if the EVM fails to perform as described or expected.

Safety-Critical or Life-Critical Applications. If you intend to evaluate the components for possible use in safety critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, such as devices which are classified as FDA Class III or similar classification, then you must specifically notify TI of such intent and enter into a separate Assurance and Indemnity Agreement.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2012, Texas Instruments Incorporated

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use. Components which have not been so designated are neither designed nor intended for automotive use; and TI will not be responsible for any failure of such components to meet such requirements.

Products		Applications			
Audio	www.ti.com/audio	Automotive and Transportation	www.ti.com/automotive		
Amplifiers	amplifier.ti.com	Communications and Telecom	www.ti.com/communications		
Data Converters	dataconverter.ti.com	Computers and Peripherals	www.ti.com/computers		
DLP® Products	www.dlp.com	Consumer Electronics	www.ti.com/consumer-apps		
DSP	dsp.ti.com	Energy and Lighting	www.ti.com/energy		
Clocks and Timers	www.ti.com/clocks	Industrial	www.ti.com/industrial		
Interface	interface.ti.com	Medical	www.ti.com/medical		
Logic	logic.ti.com	Security	www.ti.com/security		
Power Mgmt	power.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense		
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video		
RFID	www.ti-rfid.com				
OMAP Applications Processors	www.ti.com/omap	TI E2E Community	e2e.ti.com		
Wireless Connectivity	www.ti.com/wirelessconnectivity				

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2012, Texas Instruments Incorporated