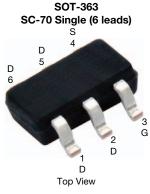
### SQ1421EDH



**Vishay Siliconix** 

### Automotive P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	-60				
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 V$	0.290				
$R_{DS(on)}(\Omega)$ at $V_{GS}$ = -4.5 V	0.395				
I <sub>D</sub> (A)	-1.6				
Configuration	Single				
Package	SC-70				

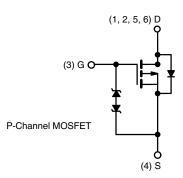


#### **FEATURES**

- TrenchFET<sup>®</sup> power MOSFET
- AEC-Q101 qualified
- 100 % R<sub>g</sub> and UIS tested
- Typical ESD protection: 800 V
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



ROHS COMPLIANT HALOGEN FREE



Marking Code: 9B

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25 \text{ °C}$ , unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V <sub>DS</sub>	-60	v	
Gate-Source Voltage	V <sub>GS</sub>	± 20	v		
Continuous Drain Current	$T_C = 25 \ ^\circ C \ ^a$	1	-1.6		
Continuous Drain Current	T <sub>C</sub> = 125 °C	Ι <sub>D</sub>	-1		
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	-1.6	А		
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	-6.7			
Single Pulse Avalanche Current	Single Pulse Avalanche Current		-8		
Single Pulse Avalanche Energy		E <sub>AS</sub>	3.2	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	Р	2.7	W	
Maximum Power Dissipation ~	T <sub>C</sub> = 125 °C	P <sub>D</sub>	0.5	vv	
Operating Junction and Storage Temperature Range	ge	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	125	°C/W	
Junction-to-Foot (Drain)		R <sub>thJF</sub>	45	0/10	

Notes

a. Package limited.

b. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	•	•					<b>I</b>
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	: 0 V, I <sub>D</sub> = -250 μA	-60	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{GS}, I_{D} = -250 \ \mu A$	-1.5	-2.0	-2.5	V
Gate-Source Leakage		V <sub>DS</sub> =	$V_{DS} = 0 V, V_{GS} = \pm 12 V$		-	± 5	μA
Gale-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	0 V, $V_{GS}$ = ± 20 V	-	-	± 5	mA
		$V_{GS} = 0 V$	V <sub>DS</sub> = -60 V	-	-	-1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = -60 V, T <sub>J</sub> = 125 °C	-	-	-50	
		$V_{GS} = 0 V$	$V_{DS} = -60 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$	-	-	-150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{GS} = -10 V$	$V_{DS} \le -5 V$	-5	-	-	Α
		V <sub>GS</sub> = -10 V	I <sub>D</sub> = -2 A	-	0.230	0.290	
Drain Source On State Desistance a	В	$V_{GS} = -10 V$	I <sub>D</sub> = -2 A, T <sub>J</sub> = 125 °C	-	-	0.470	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V	I <sub>D</sub> = -2 A, T <sub>J</sub> = 150 °C	-	-	0.566	Ω
		V <sub>GS</sub> = -4.5 V	I <sub>D</sub> = -1 A	-	0.305	0.395	
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> =	-10 V, I <sub>D</sub> = -1.5 A	-	3	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			-	284	355	pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = -25 V, f = 1 MHz	-	36	45	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	28	35	
Total Gate Charge <sup>c</sup>	Qg			-	3.6	5.4	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = -4.5 V	$V_{DS} = -30 \text{ V}, \text{ I}_{D} = -1 \text{ A}$	-	1.2	-	nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		Ē		1.7	-	
Gate Resistance	Rg	f = 1 MHz		3.1	6.05	9	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	44	66	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{\text{DD}} = -30 \text{ V}, \text{ R}_{\text{L}} = 30 \Omega$ $I_{\text{D}} \cong -1 \text{ A}, \text{ V}_{\text{GEN}} = -4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		-	25	38	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	13	20	- ns
Fall Time <sup>c</sup>	t <sub>f</sub>			-	9	14	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	-6.7	Α
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = -0.5 A, V <sub>GS</sub> = 0 V		-	-0.8	-1.2	V

Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

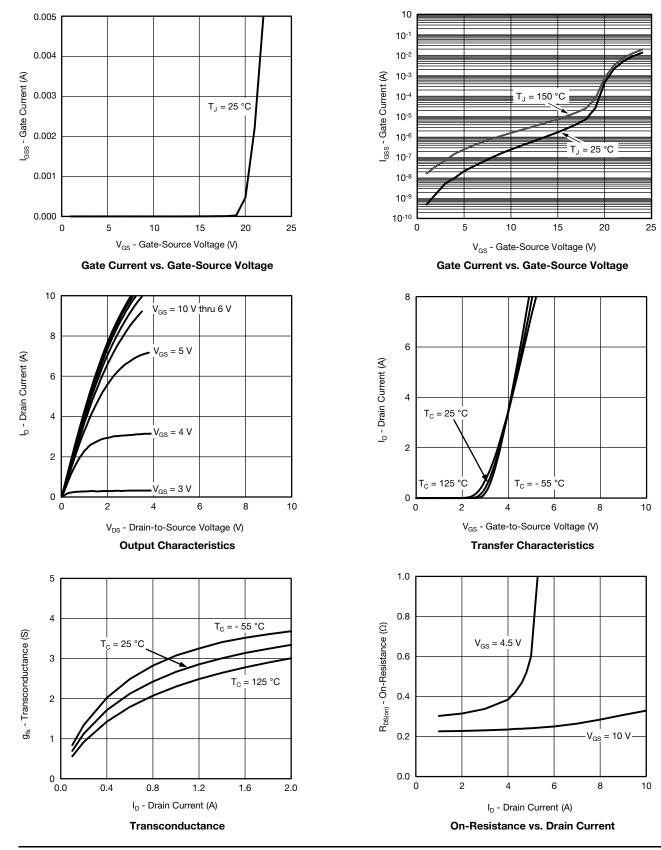
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## SQ1421EDH

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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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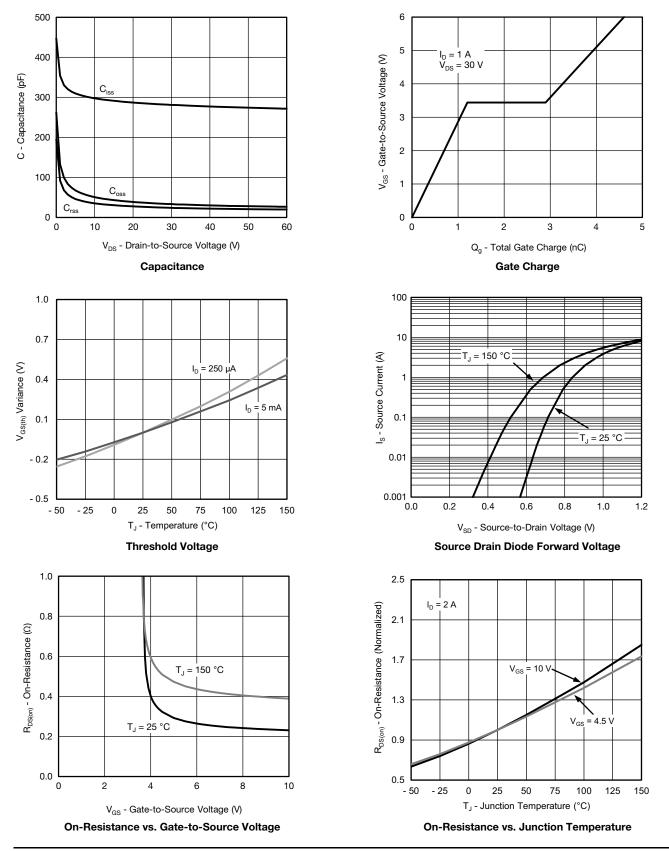
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SQ1421EDH

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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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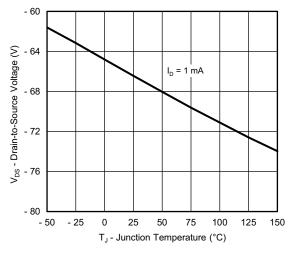
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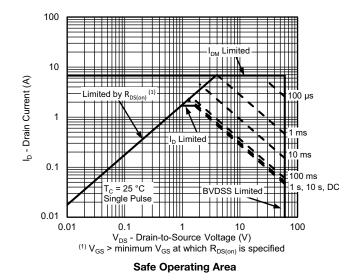


Vishay Siliconix

### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



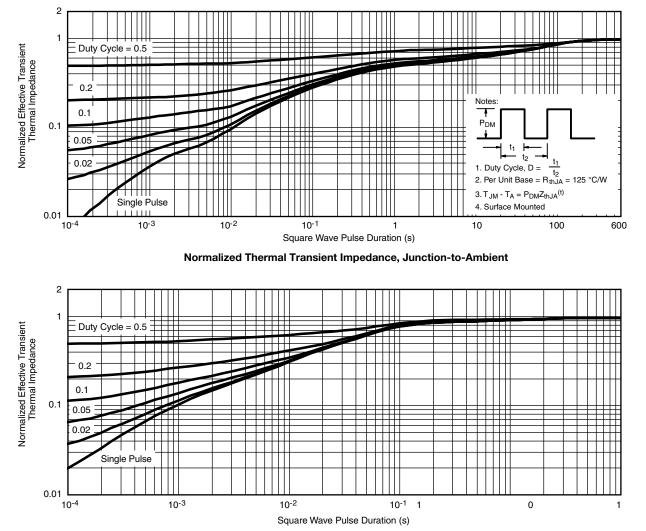
Drain Source Breakdown vs. Junction Temperature





Vishay Siliconix

#### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?68494">www.vishay.com/ppg?68494</a>.

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Vishay Siliconix

### SC-70

Ordering codes for the SQ rugged series power MOSFETs in the SC-70 package:

DATASHEET PART NUMBER	OLD ORDERING CODE <sup>a</sup>	NEW ORDERING CODE
SQ1421EDH	-	SQ1421EDH-T1_GE3
SQ1431EH	SQ1431EH-T1-GE3	SQ1431EH-T1_GE3
SQ1440EH	-	SQ1440EH-T1_GE3
SQ1470AEH	-	SQ1470AEH-T1_GE3
SQ1539EH	-	SQ1539EH-T1_GE3
SQ1563AEH	-	SQ1563AEH-T1_GE3
SQ1902AEL	-	SQ1902AEL-T1_GE3
SQ1912AEEH	-	SQ1912AEEH-T1_GE3
SQ1912EH	-	SQ1912EH-T1_GE3

Note

a. Old ordering code is obsolete and no longer valid for new orders



# Package Information Vishay Siliconix

#### SC-70: 6-LEADS





	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A <sub>1</sub>	-	-	0.10	-	-	0.004
A <sub>2</sub>	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.40	0.071	0.083	0.094
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC				0.026BSC	;
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
٩	7°Nom				7°Nom	
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						

## **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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