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Vishay Siliconix

# Automotive P-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	-60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.065			
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS}$ = -4.5 V	0.090			
I <sub>D</sub> (A)	-16			
Configuration	Single			
Package	PowerPAK 1212-8W			

### **FEATURES**

- TrenchFET® power MOSFET
- Low thermal resistance PowerPAK® 1212-8W package with 1.07 mm profile



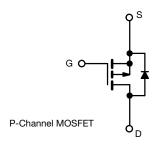
- Wettable flank terminals
- 100 % R<sub>g</sub> and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





**FREE** 





<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	$V_{DS}$	-60	V	
Gate-Source Voltage	$V_{GS}$	± 20		
Continuous Drain Current	T <sub>C</sub> = 25 °C <sup>a</sup>	L	-16	
Continuous Drain Current	T <sub>C</sub> = 125 °C	l <sub>D</sub>	-11	
Continuous Source Current (Diode Conduction) <sup>a</sup>	Is	-16	Α	
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	-64	ļ	
Single Pulse Avalanche Current	L = 0.1 mH		-23	
Single Pulse Avalanche Energy	L = 0.111111	E <sub>AS</sub>	26	mJ
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	В	53	W
	T <sub>C</sub> = 125 °C	$P_{D}$	17	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Soldering Recommendations (Peak Temperature) d		260	C	

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount c	$R_{thJA}$	81	°C/W	
Junction-to-Case (Drain)		$R_{thJC}$	2.8	C/VV	

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. When mounted on 1" square PCB (FR4 material).
- d. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.



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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT	
Static		-				l		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		-60	-	-	· v	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		-2.0	-2.5		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = -60 V	-	-	-1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = -60 V, T <sub>J</sub> = 125 °C	1	-	-50	μΑ	
		$V_{GS} = 0 V$	V <sub>DS</sub> = -60 V, T <sub>J</sub> = 175 °C	-	-	-150		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{GS} = -10 \text{ V}$	$V_{DS} \le -5 \text{ V}$	-15	-	-	Α	
		$V_{GS} = -10 \text{ V}$	I <sub>D</sub> = -5.7 A	-	0.050	0.065	Ω	
Drain-Source On-State Resistance a	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V	$I_D = -5.7 \text{ A}, T_J = 125 ^{\circ}\text{C}$	-	-	0.112		
Brain Godroe on Glate Hesistance		V <sub>GS</sub> = -10 V	$I_D = -5.7 \text{ A}, T_J = 175 \text{ °C}$	-	-	0.138		
		V <sub>GS</sub> = -4.5 V	I <sub>D</sub> = -4.4 A,	-	0.070	0.090		
Forward Transconductance b	9 <sub>fs</sub>	V <sub>DS</sub> =	-15 V, I <sub>D</sub> = -5.7 A	-	13	-	S	
Dynamic <sup>b</sup>		1				T	1	
Input Capacitance	C <sub>iss</sub>			-	1108	1385		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	$V_{DS} = -25 \text{ V}, f = 1 \text{ MHz}$	i	132	165	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	84	105		
Total Gate Charge <sup>c</sup>	Qg			-	25.5	38		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = -10 V	$V_{DS} = -30 \text{ V}, I_{D} = -5.7 \text{ A}$	-	3.6	-	nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$	1		-	6.7	-		
Gate Resistance	Rg	f = 1 MHz		3	6	9	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>				9	14		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = -30 V, $R_L$ = 30 $\Omega$ $I_D$ $\cong$ -1 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$		-	9	14	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	37	56		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	8	12		
Source-Drain Diode Ratings and Chara	acteristics b						1	
Pulsed Current a	I <sub>SM</sub>			-	-	-64	Α	
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = -6 A, V <sub>GS</sub> = 0 V		-	-0.85	-1.2	V	
	0.0	17 - 07 1, 1G5 - 0 1			l	l	L	

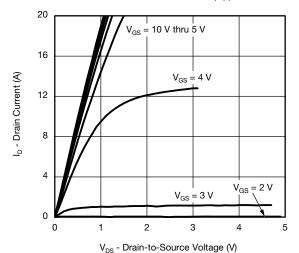
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

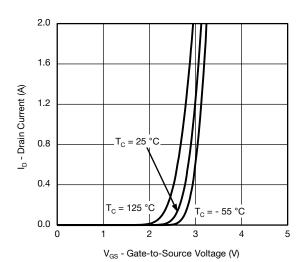
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



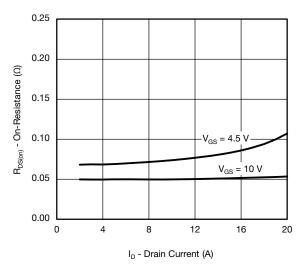
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



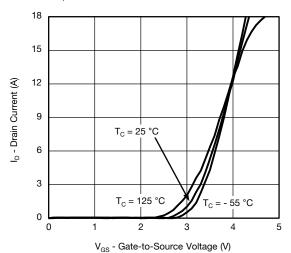
#### **Output Characteristics**



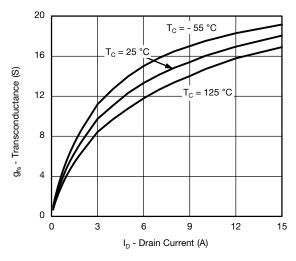
### Transfer Characteristics



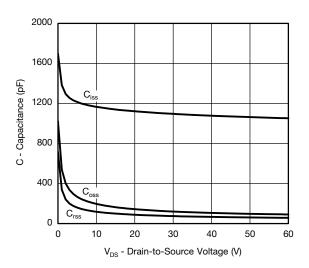
On-Resistance vs. Drain Current



**Transfer Characteristics** 



### Transconductance

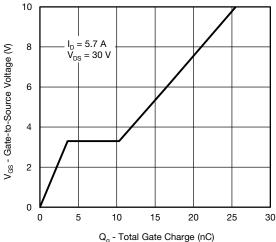


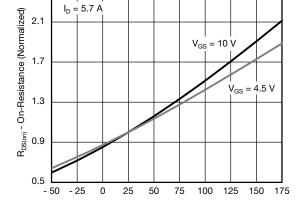
Capacitance

For technical questions, contact: automostechsur



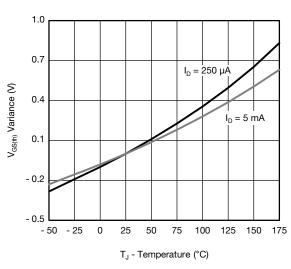
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

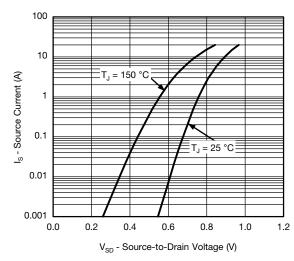




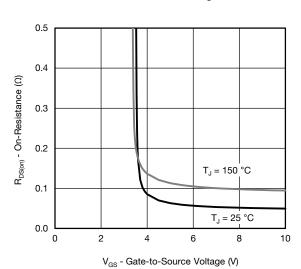
## $\label{eq:TJ-Junction} T_{J} \text{ - Junction Temperature (°C)}$ On-Resistance vs. Junction Temperature



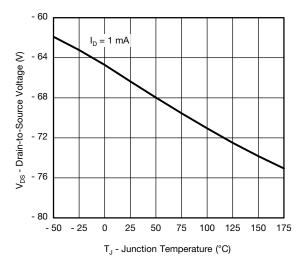




### **Threshold Voltage**



Source Drain Diode Forward Voltage

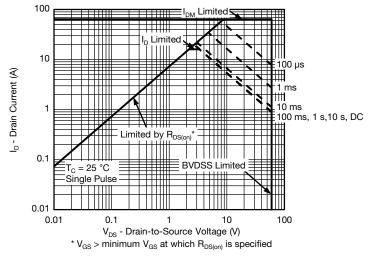


On-Resistance vs. Gate-to-Source Voltage

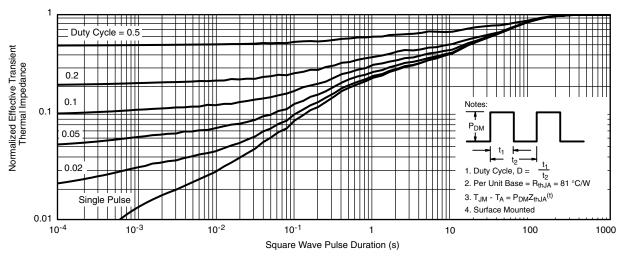
Drain Source Breakdown vs. Junction Temperature



## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



#### Safe Operating Area

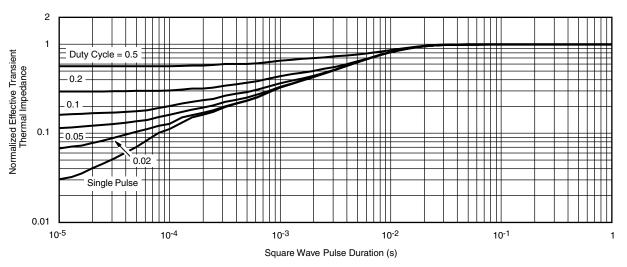


Normalized Thermal Transient Impedance, Junction-to-Ambient

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### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg276598">www.vishay.com/ppg276598</a>.



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# PowerPAK® 1212-8 and PowerPAK 1212-8W

Ordering codes for the SQ rugged series power MOSFETs in the PowerPAK 1212-8 and PowerPAK 1212-8W packages:

DATASHEET PART NUMBER	OLD ORDERING CODE <sup>a</sup>	NEW ORDERING CODE	
SQ7414AEN	SQ7414AEN-T1-GE3	SQ7414AEN-T1_GE3	
SQ7414AENW	-	SQ7414AENW-T1_GE3	
SQ7415AEN	SQ7415AEN-T1-GE3	SQ7415AEN-T1_GE3	
SQ7415AENW	-	SQ7415AENW-T1_GE3	
SQS401EN	SQS401EN-T1-GE3	SQS401EN-T1_GE3	
SQS401ENW	-	SQS401ENW-T1_GE3	
SQS405EN	SQS405EN-T1-GE3	SQS405EN-T1_GE3	
SQS405ENW	-	SQS405ENW-T1_GE3	
SQS420EN	SQS420EN-T1-GE3	SQS420EN-T1_GE3	
SQS423EN	SQS423EN-T1-GE3	SQS423EN-T1_GE3	
SQS460EN	SQS460EN-T1-GE3	SQS460EN-T1_GE3	
SQS462EN	SQS462EN-T1-GE3	SQS462EN-T1_GE3	
SQS482EN	SQS482EN-T1-GE3	SQS482EN-T1_GE3	
SQS484EN	SQS484EN-T1-GE3	SQS484EN-T1_GE3	
SQS490EN	SQS490EN-T1-GE3	SQS490EN-T1_GE3	
SQS840EN	SQS840EN-T1-GE3	SQS840EN-T1_GE3	
SQS850EN	SQS850EN-T1-GE3	SQS850EN-T1_GE3	

### Note

a. Old ordering code is obsolete and no longer valid for new orders



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