

#### **Overview**

KEMET's 900 Series encapsulated radial leaded ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution in situations where there is a need to suppress line disturbances at the source.

Safety Certified Capacitors are classified as either X and/or Y capacitors. Class X capacitors are primarily used in line-to-line (across-the-line) applications. In this application there is no danger of electric shock to humans should the capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock.

With a working voltage of 440 VAC in line-to-line (Class X) and 300 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y2 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 5 KV (Y2) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94V–0.



### **Ordering Information**

<b>C</b> 9	7	1	U	472	М	Z	w	D	Α	Α	7317
Ceramic Series	Body Diameter	Lead Spacing <sup>1,2,4</sup>	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/ Temp. Char.	Design	Lead Config. <sup>1,3,4</sup>	Failure Rate	Packaging (C-Spec)
C9 = Ceramic 900 Series	0 = 7.0 mm 1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 4 = 11.0 mm 6 = 13.0 mm 8 = 15.0 mm	7 = 7.5 mm 1 = 10.0 mm	U = Safety	2 significant digits + number of zeroes	J = ±5% K = ±10% M = ±20%	Z = X1 440 VAC /Y2 300 VAC	S = SL Y = Y5P W = Y5U V = Y5V	D = Disc	A = Straight B = Vertical Kink C = Outside Kink D = Inside Kink	A = N/A	See "Packaging C-Spec Ordering Options Table" below

<sup>1</sup> Due to a high risk of arcing, "Inside Kink" lead configuration cannot be combined with the 5 mm lead spacing option. The "Inside Kink" option is only available on capacitors with lead spacing of 7.5 mm or greater. A potential for arcing may exist when combining the "Inside Kink" lead configuration with a 7.5mm lead spacing option, especially in high humidity environments and/or when exposure to voltages and transients may impact creepage and clearance requirements.

<sup>2</sup> Capacitor body diameter will limit available lead spacing and packaging options. See "Dimensions" and "Product Ordering Codes and Ratings" sections of this document to determine availability.

<sup>3</sup> "Vertical Kink", "Outside Kink" and "Inside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

<sup>4</sup> Bulk packaging lead length availability is dependent upon "Lead Configuration" and "Lead Spacing." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.

One world. One KEMET



# Packaging C-Spec Ordering Options Table

Packaging Type	Lead Length (mm) <sup>2,3</sup>	Packaging Ordering Code (C-Spec)
Ammo Pack	20.0 +1.5/-1.0 (straight leads) 18.0 +2.0/-0 (preformed leads <sup>1</sup> )	7317
	3.0 ±1.0	WL30
	3.5 ±1.0	WL35
Bulk Pog	4.0 ±1.0	WL40
Bulk Bag	4.5 ±1.0	WL45
	5.0 ±1.0	WL50
	20.0 minimum⁴	WL20

<sup>1</sup> Preformed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>2</sup> "Vertical Kink", "Outside Kink" and "Inside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

<sup>3</sup> Bulk packaging lead length availability is dependent upon "Lead Configuration" and "Lead Spacing." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.

<sup>4</sup> Lead length of 20.0 mm minimum only available for straight leads.

#### **Benefits**

- · Safety Standard Recognized (IEC 60384-14)
- Reliable operation up to 125°C
- · Class X1/Y2
- 7.5 mm and 10 mm lead spacing
- · Lead (Pb)-free and RoHS Compliant
- Halogen Free
- Capacitance offerings ranging from 15 pF up to 4,700 pF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- · High reliability
- · Preformed (crimped) or straight lead configurations
- · Non-polar device, minimizing installation concerns
- Encapsulation meets flammability standard UL 94V–0

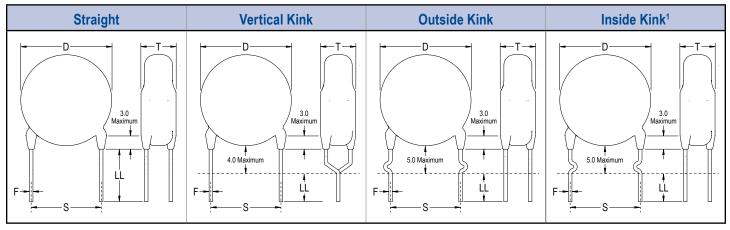
### **Applications**

Typical applications include:

- Line-to-line (Class X) filtering
- · Line-to-ground (Class Y) filtering
- Antenna coupling
- Primary and secondary coupling (switching power supplies)
- · Line disturbances suppression (motors and motor controls, relays, switching power supplies, and inverters)



### Lead Configurations



<sup>1</sup> Due to a high risk of arcing, the "Inside Kink" lead configuration option cannot be combined with 5 mm lead spacing ("F" dimension above). The "Inside Kink" option is only available on devices with lead spacing of 7.5 mm or 10 mm.

#### **Dimensions – Millimeters**

Lead Configuration	Lead Configuration Ordering Code <sup>1</sup>	F Lead Spacing <sup>2</sup>	Lead Spacing Tolerance	D Body Diameter <sup>2</sup>	T Body Thickness	e Lead Meniscus	Ød Lead Diameter
Straight	A	7.5	±1.0				
Straight	A	10.0	±1.0				
Vertical Kink	P	7.5	±1.0		See Table 1 - 3.0		
(Preformed)	В	10.0	±1.0				0.55 .0.4
Outside Kink	0	7.5	±1.0		atings"		0.55 ±0.1
(Preformed)	С	10.0	±1.0				
Inside Kink	D	7.5	±1.0				
(Preformed)	D	10.0	±1.0				

<sup>1</sup>Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

<sup>2</sup> Body diameter of capacitor will limit available lead spacing and packaging options. See "Product Ordering Codes and Ratings" sections of this document for further details.



## Approval Standard and Certification No.

Safety Standard	Standard No.	Subclass	Working Voltage	Certificate No.
VDE	IEC 60384–14	X1	440 VAC	40036415
(ENEC)	IEC 00304-14	Y2	300 VAC	40050415

These devices are VDE/ENEC recognized for antenna coupling and AC line-to-line (Class X) and line-to-ground (Class Y) applications per IEC60384–14.

#### **Environmental Compliance**

These devices are Halogen Free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.



#### **General Specifications/Performance Characteristics**

Dielectric/Temperature Characteristic	SL	Y5P	Y5U	Y5V		
Operating Temperature Range:		-40°C to +125°C				
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	−1,000 ~ +350 ppm/°C	±10%	+20%/-55%	~ +30%/-80%		
Dielectric Withstanding Voltage (7.5 mm and 10 mm Lead Spacing)		2,600 VAC (60 ±5 seconds at 25°C)				
Quality Factor (Q)	30 pF and above: ≥ 1,000 Below 30 pF: ≥ 400 +(20xC)*	See "Dissipation Factor"				
Dissipation Factor (tanδ) at +25°C1	See "Quality Factor"	2.50% 2.50% 5.0%				
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60 ±5 seconds @ 25°C)					

\*C = Nominal capacitance

<sup>2</sup> Capacitance and Dissipation Factor (DF) measured under the following conditions:

SL: 1 MHz  $\pm$  100 kHz and 1.0  $\pm$ 0.2 Vrms

X5P, Y5U and Y5V: 1 kHz ± 50 Hz and 1.0 ±0.2 Vrms

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 & Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."



### Table 1 – Product Ordering Codes and Ratings

Dielectric/				Din	nensions (mn	ו)	Lead S	pacing	
Temp. Char.	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging	
	C90(1)U100JZSD(2)A(3)	10 pF							
	C90(1)U120JZSD(2)A(3)	12 pF							
	C90(1)U150JZSD(2)A(3)	15 pF							
	C90(1)U180JZSD(2)A(3) C90(1)U200JZSD(2)A(3)	18 pF 20 pF							
	C90(1)U220JZSD(2)A(3)	20 pl 22 pF							
	C90(1)U240JZSD(2)A(3)	22 pr 24 pF							
	C90(1)U270JZSD(2)A(3)	27 pF	_	7.0					
	C90(1)U300JZSD(2)A(3)	30 pF		1.0					
	C90(1)U330JZSD(2)A(3)	33 pF					75	mm	
SL	C90(1)U360JZSD(2)A(3)	36 pF	±5%		5.0	0.55 ±0.1		r.	
	C90(1)U390JZSD(2)A(3)	39 pF						mm	
	C90(1)U470JZSD(2)A(3)	47 pF							
	C90(1)U500JZSD(2)A(3)	50 pF							
	C90(1)U510JZSD(2)A(3)	51 pF							
	C91(1)U560JZSD(2)A(3)	56 pF							
	C91(1)U620JZSD(2)A(3)	62 pF		80	8.0				
	C91(1)U680JZSD(2)A(3)	68 pF		0.0					
	C91(1)U750JZSD(2)A(3)	75 pF							
	C92(1)U820JZSD(2)A(3)	82 pF		9.0					
	C93(1)U101JZSD(2)A(3)	100 pF		10.0					
	000/4/1404/(7)/D(0) A(0)	100 pF							
	C90(1)U101KZYD(2)A(3) C90(1)U151KZYD(2)A(3)	150 pF							
	C90(1)U221KZYD(2)A(3)	220 pF		7.0				.5 mm or 10 mm	
	C90(1)U331KZYD(2)A(3)	330 pF		7.0			75		
Y5P	C90(1)U471KZYD(2)A(3)	470 pF	±10%		5.0	0.55 ±0.1			
101	C91(1)U561KZYD(2)A(3)	560 pF	21070		0.0	0.00 10.1			
	C91(1)U681KZYD(2)A(3)	680 pF		8.0					
	C92(1)U821KZYD(2)A(3)	820 pF		• •					
	C92(1)U102KZYD(2)A(3)	1,000 pF		9.0					
	, ., ., .						•		
	C90(1)U102MZWD(2)A(3)	1,000 pF		7.0					
	C92(1)U152MZWD(2)A(3)	1,500 pF		9.0				7.5 mm or 10 mm	
Y5U	C92(1)U222MZWD(2)A(3)	2,200 pF	±20%		5.0	0.55 ±0.1	7.5 mm or 10 mm		
100	C94(1)U332MZWD(2)A(3)	3,300 pF	22070	11.0	0.0	0.00 10.1			
	C96(1)U392MZWD(2)A(3)	3,900 pF		13.0				10 mm only	
	C96(1)U472MZWD(2)A(3)	4,700 pF			l	l		. ,	
	C90(1)U102MZVD(2)A(3)	1,000 pF				1			
	C90(1)U152MZVD(2)A(3)	1,500 pF		7.0					
	C90(1)U222MZVD(2)A(3)	2,200 pF		1.0					
	C92(1)U332MZVD(2)A(3)	3,300 pF		9.0				7.5 mm or 10 mm	
Y5V	C94(1)U392MZVD(2)A(3)	3,900 pF	±20%		5.0	0.55 ±0.1	7.5 mm or 10 mm		
	C94(1)U472MZVD(2)A(3)	4,700 pF		11.0					
	C96(1)U682MZVD(2)A(3)	6,800 pF		13.0				10	
	C98(1)U103MZVD(2)A(3)	10,000 pF		15.0				10 mm only	
	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Lead S	pacing	

(1) To properly complete ordering code, insert the one-digit numeric code to reflect required lead spacing: (Note that select capacitance values and packaging options may limit lead spacing availability. See table above to verify availability.)

7 = 7.5 mm
1 = 10.0 mm

(2) To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

A = Straight
B = Vertical Kink

B = Vertical Kink

C = Outside Kink

D = Inside Kink

(3) To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code." See "Dimensions" section of this document, page 2, for available options.



# Table 2 – Performance & Reliability: Test Methods and Conditions

lte	em	Specif	ication	Test Method				
Operating Tem	perature Range			-40°C to +125°C				
	Between lead wires	No fa	ilures	The capacitor shall between the lead w	The capacitor shall not be damaged when 2,600 VAC(rms) is applied between the lead wires for 60 seconds.			
Dielectric Strength	Body Insulation	No fa	ilures	The terminals (leads) of the capacitor shall be connected together. A metal foil is tightly wrapped around the body of the capacitor at a distance of about 3 to 4 mm from each terminal. The capacitor is then inserted into a container filled with metal balls approximately 1 mm in diameter. 2,600 VAC(rms) is applied for 60 seconds between the capacitor lead wires and metal balls.				
Insulation Re	sistance (IR)	10,000 MG	Ω minimum	The insulation resis after 60 ±5 second		with 500 ±50 VDC applied		
Сарас	itance	Within specif	fied tolerance					
		Temperature Characteristics	Specification					
		Y5P, Y5U	DF ≤ 2.5%		Capacitance is measured	l at 1 kHz ±20% and 5		
Dissination Fa	ctor (DF) or Q	Y5V	DF ≤ 5.0%	Vrms or less. (20 ±2°C) SL: Capacitance is measured at 1 MHz ±20% and 1.0 ±0.2 Vrms				
			$\geq 30 \text{ pF: } Q \geq 1000$ < 30 pF: Q ≥ 400 +(20 x C) C = Nominal capacitance	(25°C)				
					A capacitance measurement is made at each step specified:			
		Temperature Characteristics	Capacitance Change	Step	Temperature +20 ±2°C			
		Y5P	Within ±10%	2	-25 ±2°C			
Temperature (	Characteristics	Y5U	Within +20%/-55%	3	+20 ±2°C			
		Y5V	Within ~+30%/-80%	4	+85 ±2°C			
		SL	-1,000 ~+350 ppm°C	5	+20 ±2°C			
			(+20°C ~+85°C)	Pre-treatment: Capacitor is stored at 85 ±2°C for 1 hour and then placed at room condition <sup>1</sup> for 24 ±2 hours before measurement.				
	Tensile Terminal Strength Bending		r body shall not break.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.				
Terminal Strength			Lead wire or capacitor body shall not break.		With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by			
Solder	Solderability		e a uniform coating of ction and over 3/4 of its ference.	a second bend in the opposite direction. The lead wire of the capacitor is dipped into molten solder for $5 \pm 0.5$ seconds. The depth of immersion is up to 1.5 mm (+5/-0 mm) from the root of lead wires. Solder Temperature: Lead free solder (Sn-3Ag – 0.5Cu) 245°C ±5°C.				



# Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

lte	m	Specif	ication	Test N	lethod		
_	Appearance	No visu	al defect	As shown in the figure below, the le solder up to 1.5 mm (+5/-0 mm) fror			
	IR	1,000	) ΜΩ		:0.5 seconds/350°C ±10°C or 10 ±1		
	Dielectric Strength	Perit	tem 1	seconds/260°C ±5°C Thermal Capacitor			
Soldering Effect (Non-Preheat)	Capacitance	Y5P, Y5U and Y5V: Within ±10% SL: Within ±2.5% or ±0.25 pF, whichever is larger.		Screen Tito 2.0mm Solder Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition <sup>1</sup> for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition <sup>1</sup> .			
	Appearance	No visua	al defect	Capacitor is stored at 120°C +0/-5°	°C for 60 +0/−5 seconds. Then, as		
	IR	1,000	0 ΜΩ	up to 1.5 mm (+5/-0mm) from the er	wires are immersed in molten solder nd of the epoxy meniscus (root of		
-	Dielectric Strength	Peri	tem 1	lead wire). Duration/Solder Temperature: 7.5 +0/-1 seconds/260°C ±5°C			
Soldering Effect (Preheat)	Capacitance	Y5P, Y5U and Y5V: Within ±10% SL: Within ±2.5% or ±0.25 pF, whichever is larger.		Thermal Capacitor Screen 1.5 Molten Solder Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition <sup>1</sup> for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition <sup>1</sup> .			
	Appearance	No visu	al defect	Steady State Humidity:	Load Humidity:		
		Temperature Characteristics Y5P	Capacitance Change Within ±10%				
	Canaaitanaa	Y5U	Within ±20%				
	Capacitance	Y5V	Within ±30%				
Biased Humidity		SL	Within ±2.5% or ±0.25 pF, whichever is larger.	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours. Post Treatment:	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours with full rated voltage applied.		
_	DF		5.0% maximum maximum	Capacitor is stored for 1 to 2 hours at room condition <sup>1</sup> .	Post Treatment: Capacitor is stored for 1 to 2 hours at room condition <sup>1</sup> .		
	Q	SL: Less t Q ≥ 100 - More than 30	than 30 pF: + 10 × C/3 ) pF: Q ≥ 200   capacitance				
	IR	Y5P, Y5V and Y5U:	3,000 MΩ minimum IΩ minimum				
	Dielectric Strength	No fa	ilures				



# Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

Ite	m	Specification	Test Method			
High Temperature Life	Appearance     No visual defect       Capacitance Change     Y5P, Y5V and Y5U: Within ±20% SL: Within ±3 or ±0.3 pF, whichever is larger.       IR     3,000 MΩ minimum SL: 1,000 MΩ minimum       Dielectric Strength     No failures		Impulse Voltage: Each individual capacitor is subjected to three 5 kv impulses prior to life testing.			
Flame		The capacitor flame extinguishes as follows: Cycle     Time       1 ~ 4     30 seconds maximum       5     60 seconds maximum	Capacitors are placed in a circulating air oven for a period of 1,000 hours. The air in the oven is maintained at a temperature of 125°C ±2 throughout the test. The capacitors are subjected to AC 510 Vrms. Each hour the voltage is increased to 1,000 Vrms for 0.1 seconds. The capacitor is exposed to a flame for 15 seconds and then removed for 15 seconds. This test is repeated for 5 cycles.			
			The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge. $\int_{T_r}^{T_r} \int_{S_2}^{C_1} \int_{L_3}^{C_2} \int_{L_4}^{C_3} \int_{C_1}^{R_r} \int_{C_1}^{C_2} \int_{C_3}^{C_3} \int_{C_1}^{R_r} \int_{C_1}^{R_r} \int_{C_1}^{C_1} \int_{C_2}^{C_3} \int_{C_3}^{C_4} \int_{C_1}^{C_1} \int_{C_2}^{C_3} \int_{C_3}^{R_r} \int_{C_1}^{R_r} \int_{C_1}^{C_2} \int_{C_3}^{C_3} \int_{C_1}^{R_r} \int_{C_1}^{R_r} \int_{C_1}^{C_1} \int_{C_2}^{C_3} \int_{C_3}^{R_r} \int_{C_1}^{R_r} \int_{C_1}$			
Active Flammability		The cheesecloth should not ignite.	$ \begin{array}{ c c c c c } \hline C_{1,2} & 1 \ \mu F \pm 10\% & C_3 & 0.033 \ \mu F \pm 5\% \ 10 \ kV \\ \hline L_{1.4} & 1.5 \ Mh \pm 20\% \ 16A \ Rod \ core \ choke & Cx & Test \ capacitor \\ \hline R & 100 \pm 2\% & V_{AC} & VR \pm 5\% \\ \hline Ct & 3 \ \mu F \pm 5\% \ 10 \ kV & V_{R} & Rated \ Voltage \\ \hline F & Fuse, Rated \ 10A & Vt & Voltage \ applied \ to \ Ct \\ \hline V_x & \\ \hline \hline & \\ \hline & \\ \hline & \\ \hline & \\ \hline \hline & \\ \hline \hline & \\ \hline & \\ \hline \hline \hline \\ \hline \hline \\ \hline \hline \hline \\ \hline \hline \hline \hline$			



# Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

lte	em	Specifi	cation		Test Me	thod	
Passive Flammability		The burning time sh seco The tissue paper	nds.	best promot time.	or under test is held into a tes burning. Each specime	n is exposed to t	he flame one
	Appearance	No visua	al defect				
	Capacitance	Temperature Characteristics	Capacitance Change Within ±5%	The capacitor is subjected to 5 temperature cycles. Temperature Cycle			
	Cupuchanoc	Y5P Y5U, Y5V	Within ±10% Within ±20%	Step	Temperature (°C)	Dwell Time (minutes)	Transition Time (minutes)
Temperature		SL	≥ 30 pF: Q ≥ 350	1	-40 +0/-3	30	
Cycle			< 30 pF: Q ≥ 275 +5/2C	2	Room temperature	3	3
	DF/Q		C = Nominal	3	125 +3/-0	30	5
			capacitance	4	Room temperature	3	
		Y5P         DF ≤ 5%           Y5U, Y5V         DF ≤ 7.5%		<b>Pre-treatment:</b> Capacitor shall be stored at 85 $\pm$ 2 for 1 hour then placed at room condition <sup>1</sup> for 24 $\pm$ 2 hours.			
	IR	3,000 MΩ	minimum		nent: Capacitor is stored for		room condition <sup>1</sup> .
	Dielectric Strength	3,000 MΩ minimum No failures		_			



# **Soldering and Mounting Information**

#### Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

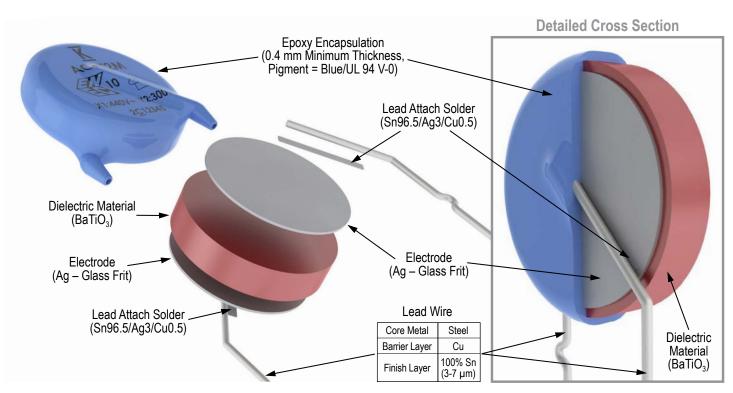
When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

- Temperature of iron-tip: 400°C maximum
- Soldering iron wattage: 50 W maximum
- · Soldering time: 3.5 seconds maximum

#### Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions:

- · Rinse bath capacity: Output of 20 watts per liter or less
- · Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- · Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

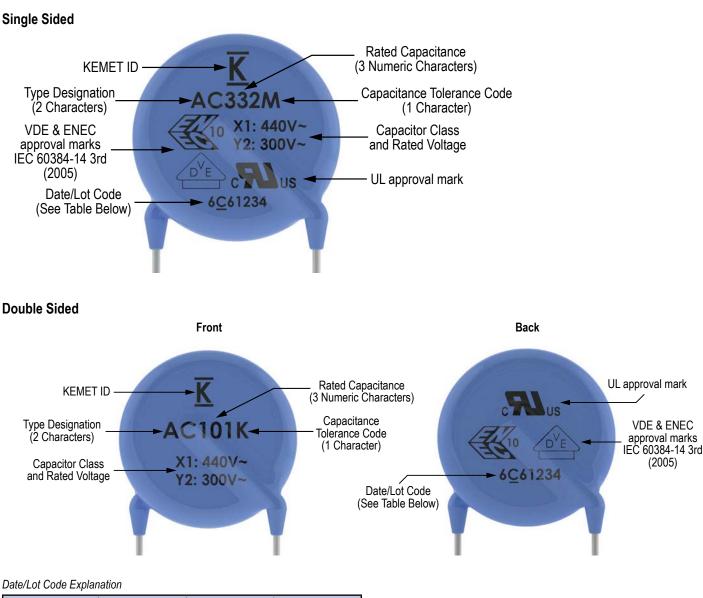


# Construction



## Marking

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two sided marking is limited to capacitors with body diameters  $\leq 8.0$  mm.)



6	<u>C</u>	6	1234
Last digit of year, e.g., 6 = 2016	Manufacturing Location Code	Manufacturing Month: 1-9 = Jan - Sept A = October N = November D = December	Last 4 digits of lot no.



# **Packaging Quantities**

Capacitor			Ammo Pack (Carrier Ta		pe)		
Body Diameter	Body Diameter Code <sup>1</sup>	Bulk Bag (Loose)	Component pitch on carrier tape <sup>2</sup>				
(mm)	oout	(20030)	12.7 mm	12.7 mm 15 mm			
7.0	0						
8.0	1						
9.0	2				1,000 pieces/box		
10.0	3						
11.0	4	500 pieces/bag	1,000 pie	eces/box			
12.0	5						
13.0	6						
14.0	7				500 pieces/box		
15.0	8						

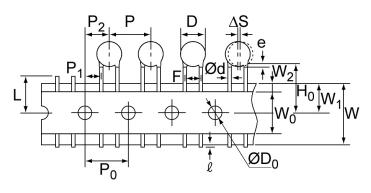
<sup>1</sup> The "Body Diameter Code" is located in the third character position of the ordering code. This code identifies the maximum diameter of the capacitor body in millimeters. For more information regarding the ordering code, see "Ordering Information" section of this document.

<sup>2</sup> For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.

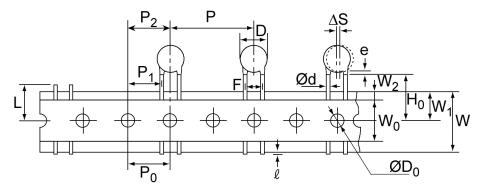


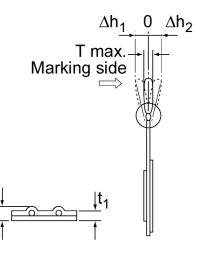
# Figure 1 - Ammo Pack Taping Format

#### 5 mm and 7.5 mm Lead Spacing:



#### 10 mm Lead Spacing:





# Table 3 – Ammo Pack Taping Specifications

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Preformed <sup>1</sup>	Straight	<b>Preformed</b> <sup>1</sup>	Straight	Preformed <sup>1</sup>
Item	Symbol	Dimensions (mm)					
Lead Spacing	F	5.0 +0.8/-0.2		7.5 ±1.0		10.0 ±1.0	
Component Pitch	Р	12.7		15.0		25.4 ±2	
Sprocket Hole Pitch	P <sub>0</sub>	12.7 ±0.3		15.0 ±0.3		12.7 ±0.3	
Sprocket Hole Center to Component Center	P <sub>2</sub>	6.35 ±1.5		7.5 ±1.5		12.7 ±1.5	
Sprocket Hole Center to Lead Center	P <sub>1</sub>	3.75 ±1.0		3.75 ±1.0		7.7 ±1.5	
Body Diameter	D	See "Product Ordering Codes and Ratings" section of this document.					
Component Alignment (side/side)	ΔS	0 ±2.0					
Carrier Tape Width	W	18.0 +1.0/-0.5					
Sprocket Hole Position	W <sub>1</sub>	9.0 ±0.5					

<sup>1</sup>Prefromed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>2</sup>Also referred to as "lead length" in this document.



## Table 3 – Ammo Pack Taping Specifications cont'd

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Preformed <sup>1</sup>	Straight	Preformed <sup>1</sup>	Straight	Preformed <sup>1</sup>
Item	Symbol	Dimensions (mm)					
Height to Seating Plane <sup>2</sup> (preformed leads <sup>1</sup> )	H <sub>o</sub>	N/A	18.0 +2.0/-0	N/A	18.0 +2.0/-0	N/A	18.0 +2.0/-0
Height to Seating Plane <sup>2</sup> (straight leads)	Н	20.0 +1.5/-1.0	N/A	20.0 +1.5/-1.0	N/A	20.0 +1.5/-1.0	N/A
Lead Protrusion	ł	2.0 maximum					
Diameter of Sprocket Hole	D <sub>0</sub>	4.0 ±0.2					
Lead Diameter	φd	0.55 ±0.1					
Carrier Tape Thickness	t,	0.6 ±0.3					
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	t <sub>2</sub>	1.5 maximum					
Component Alignment (front/back )	$\Delta h_1^{}$ $\Delta h_2^{}$	2.0 maximum					
Cut Out Length	L	11.0 maximum					
Hold-Down Tape Width	W <sub>0</sub>	11.0 minimum		11.5 minimum			
Hold-Down Tape Position	W <sub>2</sub>	3.0 maximum		1.5 ±1.5			

<sup>1</sup>Prefromed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>2</sup>Also referred to as "lead length" in this document.

### **Application Notes:**

#### **Storage and Operating Conditions:**

The insulating coating of these devices does not form an air and moisture-tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees Centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

#### Working Voltage:

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.



Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

#### **Operating Temperature and Self-Generating Heat:**

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

#### Handling - Vibration and Impact:

Do not expose these devices or their leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



### **KEMET Corporation** World Headquarters

2835 KEMET Way Simpsonville, SC 29681

Mailing Address: P.O. Box 5928 Greenville, SC 29606

www.kemet.com Tel: 864-963-6300 Fax: 864-963-6521

Corporate Offices Fort Lauderdale, FL Tel: 954-766-2800

### **North America**

Northeast Wilmington, MA Tel: 978-658-1663

Southeast Lake Mary, FL Tel: 407-855-8886

**Central** Novi, MI Tel: 248-994-1030

Irving, TX Tel: 972-915-6041

West Milpitas, CA Tel: 408-433-9950

Mexico Guadalajara, Jalisco Tel: 52-33-3123-2141

#### Europe

Southern Europe Sasso Marconi, Italy Tel: 39-051-939111

Skopje, Macedonia Tel: 389-2-55-14-623

**Central Europe** Landsberg, Germany Tel: 49-8191-3350800

Kamen, Germany Tel: 49-2307-438110

Northern Europe Wyboston, United Kingdom Tel: 44-1480-273082

Espoo, Finland Tel: 358-9-5406-5000

#### Asia

Northeast Asia Hong Kong Tel: 852-2305-1168

Shenzhen, China Tel: 86-755-2518-1306

Beijing, China Tel: 86-10-5877-1075

Shanghai, China Tel: 86-21-6447-0707

Seoul, South Korea Tel: 82-2-6294-0550

Taipei, Taiwan Tel: 886-2-27528585

Southeast Asia Singapore Tel: 65-6701-8033

Penang, Malaysia Tel: 60-4-6430200

Bangalore, India Tel: 91-806-53-76817

Note: KEMET reserves the right to modify minor details of internal and external construction at any time in the interest of product improvement. KEMET does not assume any responsibility for infringement that might result from the use of KEMET Capacitors in potential circuit designs. KEMET is a registered trademark of KEMET Electronics Corporation.



### Disclaimer

All product specifications, statements, information and data (collectively, the "Information") in this datasheet are subject to change. The customer is responsible for checking and verifying the extent to which the Information contained in this publication is applicable to an order at the time the order is placed.

All Information given herein is believed to be accurate and reliable, but it is presented without guarantee, warranty, or responsibility of any kind, expressed or implied.

Statements of suitability for certain applications are based on KEMET Electronics Corporation's ("KEMET") knowledge of typical operating conditions for such applications, but are not intended to constitute – and KEMET specifically disclaims – any warranty concerning suitability for a specific customer application or use. The Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by KEMET with reference to the use of KEMET's products is given gratis, and KEMET assumes no obligation or liability for the advice given or results obtained.

Although KEMET designs and manufactures its products to the most stringent quality and safety standards, given the current state of the art, isolated component failures may still occur. Accordingly, customer applications which require a high degree of reliability or safety should employ suitable designs or other safeguards (such as installation of protective circuitry or redundancies) in order to ensure that the failure of an electrical component does not result in a risk of personal injury or property damage.

Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicted or that other measures may not be required.