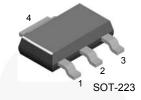
November 2014



# NZT651 NPN Current Driver Transistor

# Description

This device is designed for power amplifier, regulator and switching circuits where speed is important. Sourced from process 4P.



1. Base 2,4. Collector 3. Emitter

# **Ordering Information**

Part Number Marking		Package	Packing Method
NZT651 651		SOT-223 4L	Tape and Reel

# Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V
V <sub>CBO</sub>	Collector-Base Voltage	80	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
۱ <sub>C</sub>	Collector Current - Continuous	4.0	Α
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

# Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
Б	Total Power Dissipation	1.2	W
PD	Derate Above 25°C	9.7	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	103	°C/W

Note:

3. Device is mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead minimum 6 cm<sup>2</sup>.

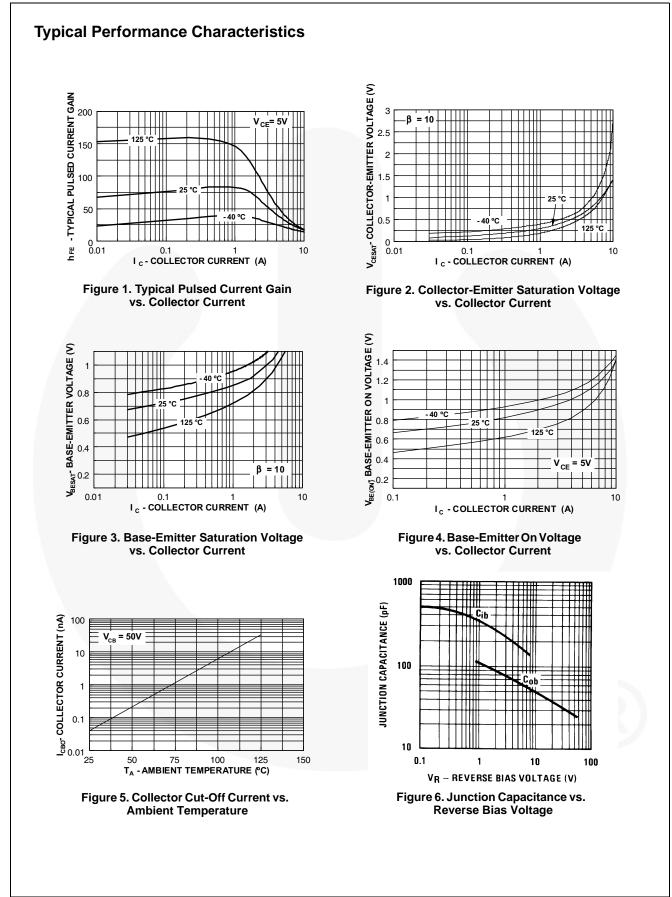
# **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

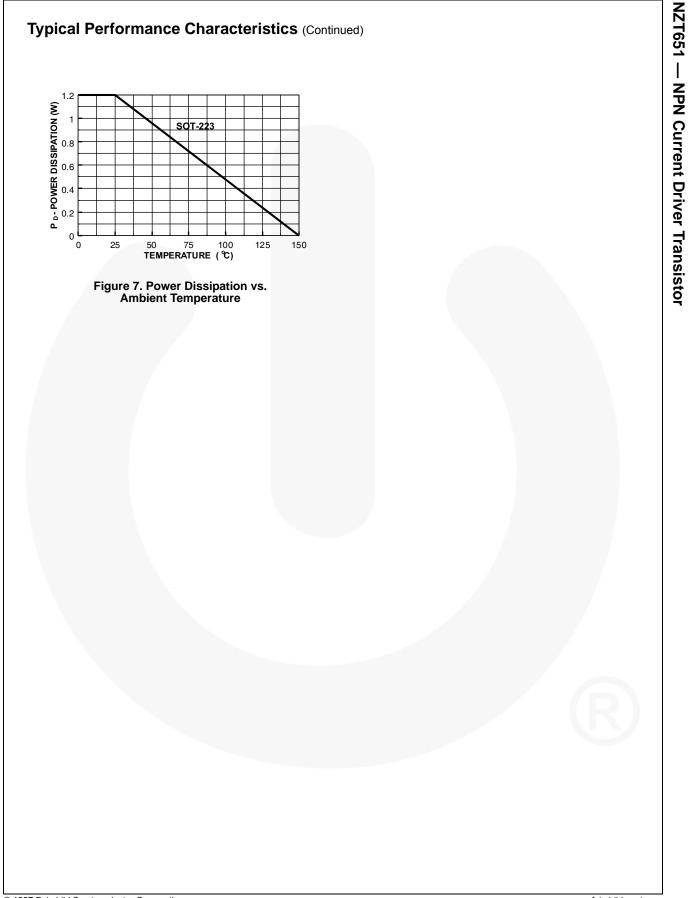
Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	60		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 100 μA, I <sub>E</sub> = 0	80		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 100 μA, I <sub>C</sub> = 0	5.0		V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = 80 V, I <sub>E</sub> = 0		100	nA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB}$ = 4.0 V, I <sub>C</sub> = 0		0.1	μΑ
	DC Current Gain <sup>(4)</sup>	$I_{C}$ = 50 mA, $V_{CE}$ = 2.0 V	75		
h		$I_{C}$ = 500 mA, $V_{CE}$ = 2.0 V	75		
h <sub>FE</sub>		I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 2.0 V	75		
		$I_{C}$ = 2.0 A, $V_{CE}$ = 2.0 V	40		
V (act)	Collector-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 100 mA		0.3	V
V <sub>CE</sub> (sat)		I <sub>C</sub> = 2.0 A, I <sub>B</sub> = 200 mA		0.5	
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 100 mA		1.2	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(4)</sup>	I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 2.0 V		1.0	V
f <sub>T</sub>	Current Gain - Bandwidth Product	I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 5.0 V, f = 100 MHz	75		MHz

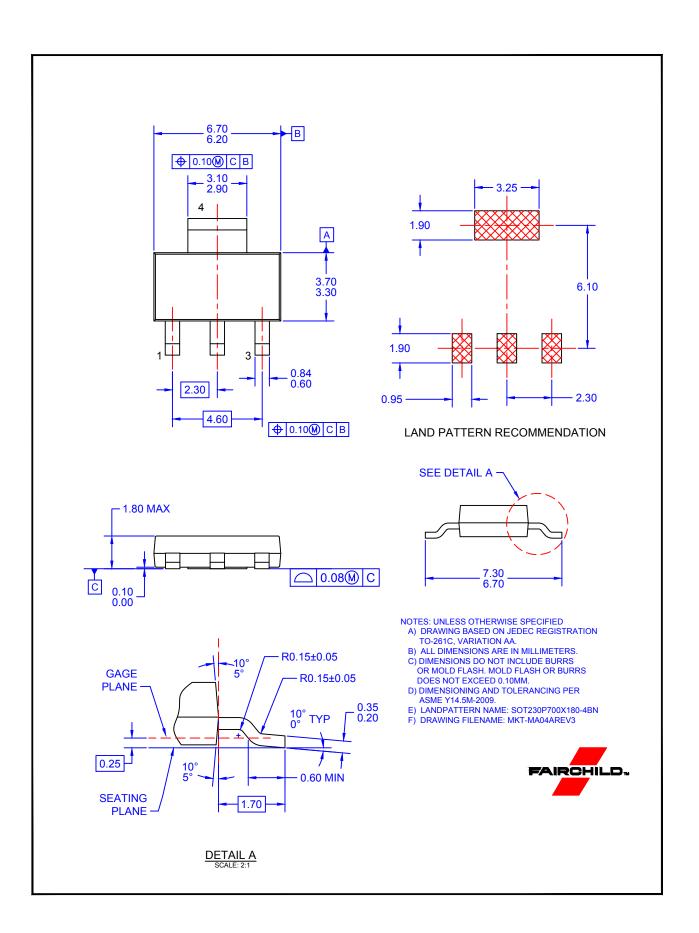
## Note:

4. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%



NZT651 — NPN Current Driver Transistor







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