Product data sheet

1. General description

PNP switching transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

NPN complement: PMST4401

2. Features and benefits

- General purpose switching transistor
- AEC-Q101 qualified

3. Applications

Switching and linear amplification.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-40	V
I _C	collector current		-	-	-600	mA
h _{FE}	DC current gain	V_{CE} = -2 V; I_{C} = -150 mA; pulsed; $t_{p} \le$ 300 µs; $\delta \le$ 0.02 ; T_{amb} = 25 °C	100	-	300	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	Е	emitter		В
3	С	collector	1 2 SC-70 (SOT323)	E sym132



40 V, 600 mA PNP switching transistor

6. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PMST4403	SC-70	plastic surface-mounted package; 3 leads	SOT323				

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMST4403	%2T

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

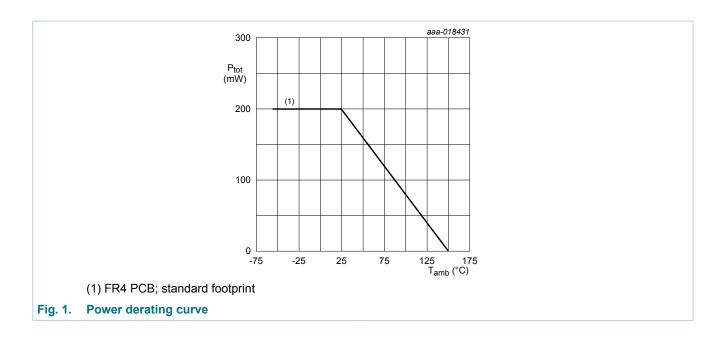
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-40	V
V_{CEO}	collector-emitter voltage	open base		-	-40	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

2/13

40 V, 600 mA PNP switching transistor

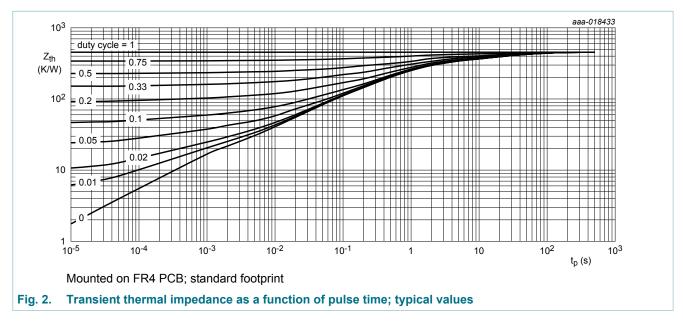


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



40 V, 600 mA PNP switching transistor

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Сво	collector-base cut-off	V_{CB} = -40 V; I_E = 0 A; T_{amb} = 25 °C	-	-	-50	nA
	current	V_{CB} = -40 V; I_{E} = 0 A; T_{j} = 125 °C	-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-50	nA
h _{FE}	DC current gain	V_{CE} = -1 V; I_{C} = -0.1 mA; T_{amb} = 25 °C	30	-	-	
		V_{CE} = -1 V; I_{C} = -1 mA; T_{amb} = 25 °C	60	-	-	
		V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	-	
		V_{CE} = -2 V; I_{C} = -150 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	100	-	300	
		V_{CE} = -2 V; I_{C} = -500 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C	20	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02 ; T_{amb} = 25 °C	-	-	-400	mV
		I_C = -500 mA; I_B = -50 mA; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02 ; T_{amb} = 25 °C	-	-	-750	mV
V _{BEsat}	base-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02 ; T_{amb} = 25 °C	-	-	-950	mV
		I_C = -500 mA; I_B = -50 mA; pulsed; $t_p \le$ 300 µs; $\delta \le$ 0.02 ; T_{amb} = 25 °C	-	-	-1.3	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;	-	-	15	ns
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	30	ns
t _{on}	turn-on time		-	-	40	ns
t _s	storage time		-	-	300	ns
t _f	fall time		-	-	50	ns
t _{off}	turn-off time		-	-	350	ns
C _C	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	8.5	pF
C _E	emitter capacitance	V_{EB} = -500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	35	pF
f _T	transition frequency	V_{CE} = -10 V; I_{C} = -20 mA; f = 100 MHz; T_{amb} = 25 °C	200	-	-	MHz

40 V, 600 mA PNP switching transistor

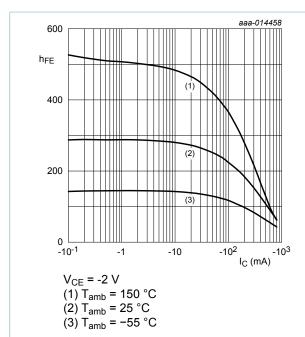


Fig. 3. DC current gain as a function of collector current; typical values

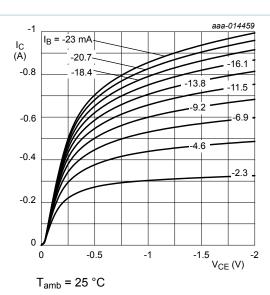


Fig. 4. Collector current as a function of collectoremitter voltage; typical values

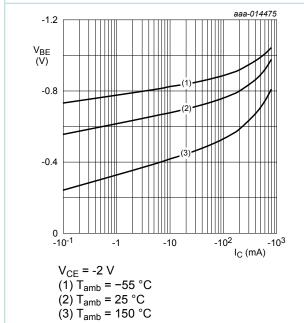
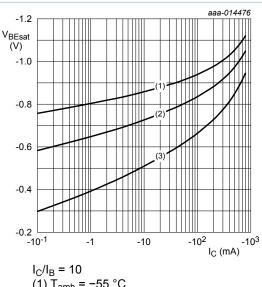


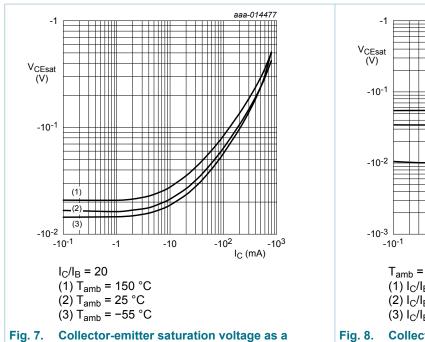
Fig. 5. Base-emitter voltage as a function of collector current; typical values



$$\begin{split} &I_{\text{C}}/I_{\text{B}} = 10\\ &(1)~T_{\text{amb}} = -55~^{\circ}\text{C}\\ &(2)~T_{\text{amb}} = 25~^{\circ}\text{C}\\ &(3)~T_{\text{amb}} = 150~^{\circ}\text{C} \end{split}$$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

40 V, 600 mA PNP switching transistor



function of collector current; typical values

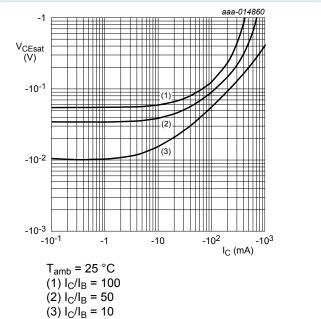
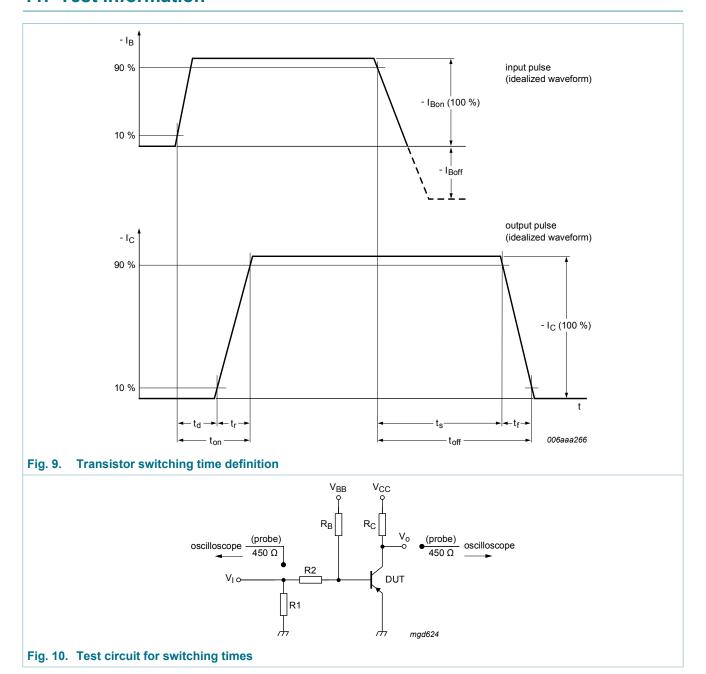


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

40 V, 600 mA PNP switching transistor

11. Test information

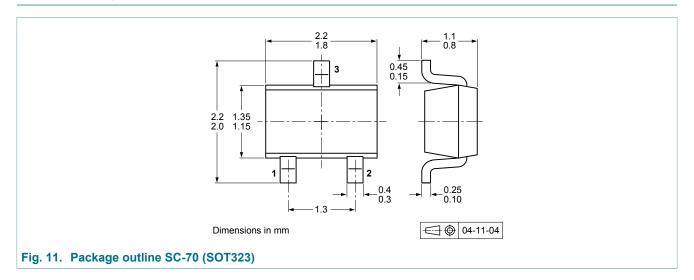


Quality information

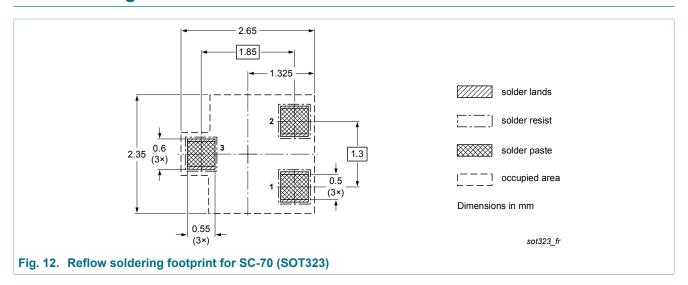
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

40 V, 600 mA PNP switching transistor

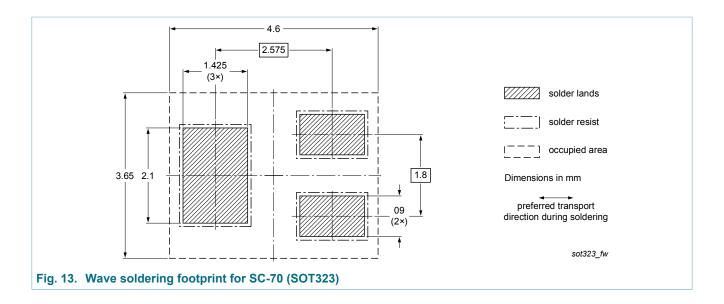
12. Package outline



13. Soldering



40 V, 600 mA PNP switching transistor



9 / 13

40 V, 600 mA PNP switching transistor

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMST4403 v.4	20160801	Product data sheet	-	PMST4403 v.3
Modifications:	of NXP Semico	his data sheet has been rede anductors. /e been adapted to the new o		, ,
PMST4403 v.3	19990422	Product data sheet	-	PMST4403 v.2
PMST4403 v.2	19970529	Product data sheet	-	PMST4403 v.1
PMST4403 v.1	199308xx	Product data sheet	-	-

40 V, 600 mA PNP switching transistor

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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PMST4403

40 V, 600 mA PNP switching transistor

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40 V, 600 mA PNP switching transistor

16. Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	2
9.	Thermal characteristics	3
10.	Characteristics	4
11.	Test information	7
12.	Package outline	8
13.	Soldering	8
14.	Revision history	10
15.	Legal information	. 11

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