

High Temperature Masking Tape



FEATURES:

- High temperature masking tape and dots are ideal for masking gold fingers of printed circuit boards during wave solder or solder dip process, as well as for solder wave masking and electrical insulation.
- This high temperature tape is a polyimide, pressure sensitive, adhesive tape with silicon resin, which shows excellent dielectric insulation properties, high heat resistance and excellent solvent resistance. These properties result in a tape with remarkable dimensional stability and excellent electrical and physical properties over a wide range of temperatures.
- This product is neither corrosive nor ozone depleting and is both flame and chemical resistant.



	DIN VALUE:	ASTM VALUE:
Structure	Silicone Adhesive / Polyimide Film	
Base Film Thickness	0.025mm	1.0MIL
Total Thickness	0.070mm	2.8.MIL
Peel Strength	2.7N/CM	25oz/In
Tensile Strength	40N/CM	22lbs/In
Elongation	50%	50%
Heat Resistance (Short Term)	290°C	554°F
Heat Resistance	230°C	446°F
Appearance	Amber	Amber

PRODUCT CODE:	DESCRIPTION:	SIZE (MM):	ADDITIONAL NOTES:
1503251	High Temperature Masking Tape	15	Per Roll (Ref: 051-0009)

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Moisture Barrier Bag




Standard Bag Artwork:


Our moisture barrier bags are produced with the following sample artwork as standard. For further information on bespoke/printed orders, please contact one of our sales team. Please note there is a MOQ of 20,000 bags on all printed bags.





Note: All of our moisture barrier bags are batch coded for QC traceability.

	CAUTION This bag contains MOISTURE SENSITIVE DEVICES	LEVEL <div style="border: 1px solid black; width: 30px; height: 20px; margin: 0 auto;"></div> <small>If blank, see adjacent label.</small>
<p>1. Calculated shelf life in sealed bag: 12 months at <math><40^{\circ}\text{C}</math> and <math><90\%</math> relative humidity (RH)</p> <p>2. Peak package body temperature: _____ °C</p> <p>3. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must:</p> <p>a) Mounted within _____ hours of factory conditions <math><30^{\circ}\text{C}</math> / 60% RH, OR <small>If blank, see adjacent label.</small></p> <p>b) Stored at <math><10\%</math> RH</p> <p>4. Devices require bake, before mounting, if:</p> <p>a) Humidity Indicator Card is >10% when read at <math>23 \pm 5^{\circ}\text{C}</math></p> <p>b) 3a or 3b not met</p> <p>5. If baking is required, devices may be baked for 48 hours at <math>125 \pm 5^{\circ}\text{C}</math></p> <p>Note: If device containers cannot be subjected to high temperature or shorter bake times are desired, reference IPC/JEDEC J-STD-033 for bake procedure.</p> <p>Bag Seal Date: _____ <small>If blank, see adjacent label.</small></p> <p>Note: Level and body temperature defined by IPC/JEDEC J-STD-020</p>		

LEVEL	FLOOR LIFE (OUT OF THE BAG) AT FACTORY AMBIENT 30°C / 60% RH OR AS STATED
1	Unlimited at 30°C / 85% RH
2	1 Year
2a	4 Weeks
3	168 Hours
4	72 Hours
5	48 Hours
5a	24 Hours
6	Mandatory bake before use. After bake must be reflowed within the time limit specified on the label.

MOISTURE BARRIER BAG
ANT018MBB
THIS BAG IS RoHS COMPLIANT 

ATTENTION
THIS BAG CONTAINS
MOISTURE & ELECTROSTATIC
SENSITIVE DEVICES 

    CONFORMS TO
IPC/JEDEC J-STD-033

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Moisture Barrier Bag



Test Conditions:

The following results were taken under the following environmental test conditions:

Temperature: 21.3°C / Humidity: 45.1%



Technical Parameters:

Item:	Test Standard:	Result:
Film Composition	N/A	PET-AL/NY/CPE
Metal Layer Resistance	ASTMD-257	<0.1 Ω
Inner and Outer Resistance	ASTMD-257	10 ⁸ - 10 ¹¹ Ω
Static Shielding - Capacitance Probe	EIA541 (Voltage Difference)	<10V
Moisture Vapour Transmission (at 90%RH, 23°C)	ASTMF1249-2005	0.02 gm/100sq.in/24hrs
Tensile Strength	ASTM D882	MD/TD >24lbs/in
Puncture Resistance	ASTM F1306-90(2002)	Inner to Outer: 54.7N Outer to Inner: 51.3N
Tear Strength	ASTM D1004	MD >3lbs/in TD >3.8lbs/in
Heat Seal Temperature	-	250-375 F
Heat Seal Time	-	0.5-3.5 sec
Heat Seal Pressure	-	30-70 PSI
Seal Strength	GB/96-04-10	>3kg/cm
Contact Corrosivity	FTMS 101C Method 3005	No visible spots detected
Static Decay Time	IEC61340-5-1 (±1000 - ±100V)	≤2S

Test Conclusion: (Date of Issue: 2009-08-16)

The anti-static moisture barrier bag is tested accordant with the relevant test standard and requirements.

Test Item:	Test Method:	Measured Equipment(s):	MDL:
Lead (Pb)	IEC 62321:2008 Ed.1 Sec.8	ICP-OES	2mg/kg
Cadmium (Cd)	IEC 62321:2008 Ed.1 Sec.8	ICP-OES	2mg/kg
Mercury (Hg)	IEC 62321:2008 Ed.1 Sec.7	ICP-OES	2mg/kg
Hexavalent Chromium (Cr(VI))	IEC 62321:2008 Ed.1 Annex C	UV-Vis	2mg/kg
Polybrominated Biphenyls (PBBs)	IEC 62321:2008 Ed.1 Annex A	GC-MS	5mg/kg
Polybrominated Diphenyl Ethers (PBDEs)	IEC 62321:2008 Ed.1 Annex A	GC-MS	5mg/kg

EMI Shielding: Meets required range of EN 61340-5-1 tested per IEC 61340-2-3 and ANSI/ESD STM11.31

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www.element14.com

www.farnell.com

www.newark.com



Moisture Barrier Bag



Product Code:	Description:	Size (inches):	Size (mm):	Additional Notes:
1503136	Moisture Barrier Bag 3.6Mil / 92 microns	6 x 28	152.4 x 711.2	Pack of 100

Note: Other sizes and thicknesses available upon request.

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