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MPSA64 / MMBTA64 / PZTA64 — PNP Darlington Transistor

November 2011



## MPSA64 / MMBTA64 / PZTA64 PNP Darlington Transistor

### Features

- This device is designed for applications requiring extremely high current gain at currents to 800 mA.
- Sourced from Process 61.



## **Absolute Maximum Ratings**<sup>\*</sup> $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CES</sub>	Collector-Emitter Voltage	-30	V
V <sub>CBO</sub>	Collector-Base Voltage	-30	V
V <sub>EBO</sub>	Emitter-Base Voltage	-10	V
Ι <sub>C</sub>	Collector Current - Continuous	-1.2	A
T <sub>J,</sub> T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. **NOTES:** 

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## Thermal Characteristics $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Max.			Units
Symbol		MPSA64	*MMBTA64	**PZTA64	Units
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	1,000 8.0	mW mW/°C
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

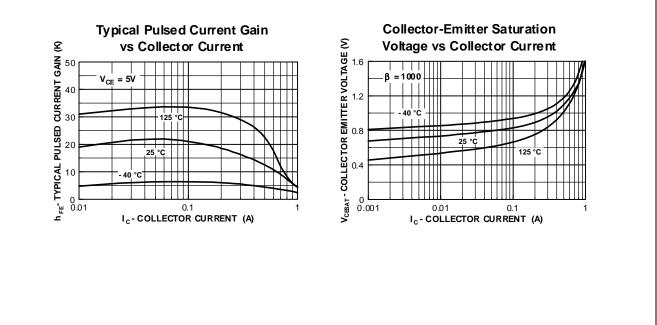
\* Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

\*\* Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm<sup>2</sup>.

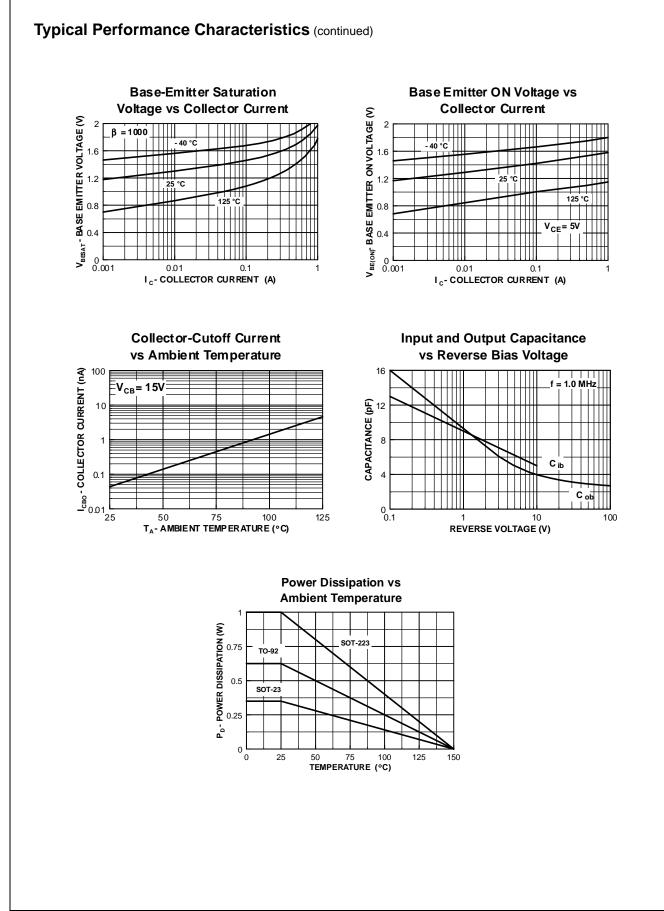
Symbol	Parameter	Test Condition	Min.	Max.	Units
OFF CHARAG	CTERISTICS				
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = -100 \mu A, I_{\rm B} = 0$	-30		V
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = -30V, I_E = 0$		-100	nA
I <sub>EBO</sub>	Emitter-Cutoff Current	V <sub>EB</sub> = -10V, I <sub>C</sub> = 0		-100	nA
ON CHARAC	TERISTICS*	·			
h <sub>FE</sub>	DC Current Gain	$I_{C} = -10$ mA, $V_{CE} = -5.0$ V $I_{C} = -100$ mA, $V_{CE} = -5.0$ V	10,000 20,000		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -100mA, I <sub>B</sub> = -0.1mA		-1.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = -100mA, V <sub>CE</sub> = -5.0V		-2.0	V
	AL CHARACTERISTICS				
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_{C} = -10$ mA, $V_{CE} = -5.0$ V, f = 100MHz	125		MHz

\* Pulse Test: Pulse Width  $\leq 300 \mu \text{s},$  Duty Cycle  $\leq 2.0\%$ 

## **Typical Performance Characteristics**



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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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