

PCN Number:

DEC 2015

Product/Process Change Notification (PCN)

Customer: Newark

Date: 12/18/15

Catalog / Customer Part # and/or Lot# affected: A4950ELJTR-T

Originator: J. Hurley **Phone:** 508.854.5431 **Email:** Jhurley@allegromicro.com

Duration of Change:

Permanent Temporary (explain)

Summary description of change: Part Change: Process Change: Other:

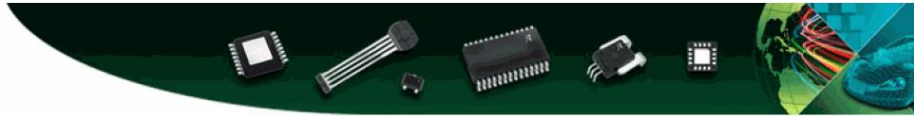
1. The A4950ELJTR-T device will change wafer fab from Polar Semiconductor LLC (PSL), Bloomington, MN, USA utilizing ABCD5-8 technology to United Microelectronics Corporation (UMC), Hsinshu, Taiwan utilizing the same ABCD5-8 technology.

2. The A4950ELJTR-T will change from final test at Allegro MicroSystems, Inc. Manila, Philippines (AMPI) to Final Test at Allegro MicroSystems Thailand, Company Ltd. located in Saraburi, Thailand (AMTC).

What is the part or process changing from (provide details)?

1. Allegro currently manufactures the A4950ELJTR-T on the 8" wafer line at wafer fab, Polar Semiconductor LLC (PSL), Bloomington, MN, USA, utilizing ABCD5-8 technology.

2. Allegro currently performs final test at Allegro MicroSystems, Inc. Manila, Philippines (AMPI)



What is the part or process changing to (describe the anticipated impact of this change on form, fit and/or function)?

1. The A4950ELJTR-T will be manufactured on the 8" wafer line at United Microelectronics Corporation (UMC), Hsinshu, Taiwan utilizing ABCD5-8 technology.
2. Allegro will perform final test at Allegro Microsystems Thailand Company Ltd. located in Saraburi, Thailand, a wholly-owned integrated circuit test facility. The same make and model test equipment will be utilized and test site transfer buy off data will be on file for each device before production begins.

Note: Validation of equivalence within a specific application is at the discretion of the Customer.

Is a PPAP update required?

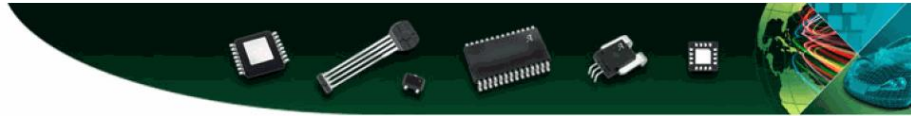
Yes

No

Is reliability testing required?
(If Yes, refer to results below)

Yes

No (explain)



Scope:

To qualify the UMC ABCD5 Fab process technology for commercial IC's.

Summary: The data below is the stresses performed for the UMC ABCD5 Fab process qualification. The UMC ABCD5 process qualification is considered to be passing all environmental stress evaluations per the Allegro MicroSystems, LLC. 900019 specification.

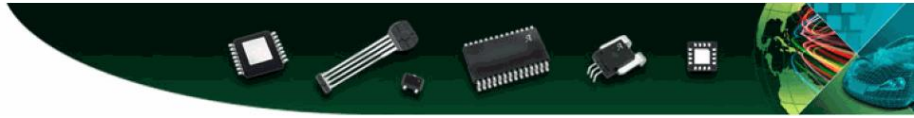
The UMC – ABCD5 Fab process technology meets and exceeds the qualification requirements required per JESD47 Stress Test Qualification for Integrated circuits.

Reference Qualification tracking numbers: 2982, 3030, and 3058

Detailed Data: *UMC-ABCD5*
Devices tested: *4950, 4942 and 4984*

Tests Summary

Stress	Test Method	Test Condition	Sample Size	# of Lots	Results
Preconditioning	J-STD-020	85°C/60% RH, (168 hrs)	231	3	0 Rejects
HAST	JESD22-A110	130°C/85% RH, (96 hrs)	77	2	0 Rejects
THB	JESD22-A101	85°C/85% RH, (1000 hrs)	77	3	0 Rejects
AC	JESD22-A102	121°C, 100% RH, (96 hrs)	77	3	0 Rejects
TC	JESD22-A104	-65°C to +150°C, (500, 1000 cycles)	77	2	0 Rejects
TC	JESD22-A104	-65°C to +175°C, (500, 1000 cycles)	77	1	0 Rejects
HTSL	JESD22-A103	150°C, (1000 hrs)	77	2	0 Rejects
HTOL	JESD22-A108	125°C, (1000 hrs)	77	3	0 Rejects
ELFR	AEC-Q100-008	125°C, (48 hrs)	800	3	0 Rejects
Wire Bond Pull	Mil-STD-883	Method 2011	5	3	Cpk > 1.67



Expected completion date for internal qualification: Wafer fab and Assembly Complete

The same make and model test equipment will be utilized and test site transfer buy off data will be on file for each device before production begins.

Expected PPAP availability date: N/A

Target implementation date: February 2016

Estimated date of first shipment: March 2016

Expected sample availability date: Available Upon Request

Customer Approval Required: Yes **Date Required:**
No **Notification Only**

Please note: It is our intention to inform our customer of changes as early as possible. Under Allegro’s procedure for product/process change notification, Allegro strives, based on its technical judgment, to provide notification of significant changes that may affect form, fit or function. However, as Allegro cannot ensure evaluation of product/process changes for each and every application; the customer retains responsibility to validate the impact of a change on its application suitability. If samples are needed for validation of a change, requests may be made via the contact information provided herein. Please contact your Account Manager or local Sales contact for any questions. We would kindly request your consideration so we can meet our target date for implementation. Unless both parties agree to extend the implementation date, this change will be implemented as scheduled.

Customer comments/Conditions of Acceptance:

Approved by: _____ Date: _____ Title: _____
cc: Allegro Sales/Marketing/Quality