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Microchip has released a new DeviceDoc for the PIC32MX1XX/2XX Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [PIC32MX1XX/2XX Family Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change: 1) Updated the Rev A3 Silicon DEVREV Values for Devices with 64/128 KB Flash in Table 2. 2) Updated the title of Table 3 to: Silicon DEVREV Values for Devices with 256 KB Flash and 64 KB RAM. 3) Added Silicon DEVREV Values for Devices with 256 KB Flash and 16 KB RAM (Table 4). 4) Updated the Affected Revisions for 256 KB Flash Memory devices in Table 5 and in silicon issues 22 (Flash Memory) and 23 (Flash Memory). 5) Updated issue 9 (I/O Ports). 6) Added Silicon Issues 25 (Power-Saving Modes) and 26 (CTMU). 7) Added Data Sheet Clarification 1 (Power-Down Current (I_{pd})).

Impacts to Data Sheet: None**Reason for Change:** To Improve Productivity**Change Implementation Status:** Complete**Date Document Changes Effective:** 31 Jul 2015**NOTE:** Please be advised that this is a change to the document only the product has not been changed..**Markings to Distinguish Revised from Unrevised Devices:** N/A**Attachment(s):** [PIC32MX1XX/2XX Family Silicon Errata and Data Sheet Clarification](#)Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.**Terms and Conditions:**

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