Product Change Notification - SYST-05CCUP460 - 09 Feb 2016 - ERRATA - dsPIC33FJ... Page 1 of 1

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Product Change Notification - SYST-05CCUP460 (Printer Friendly)								
Date:		09 Feb 2016	5					
Affected CPNs:								
Notification subject:		ERRATA - dsPIC33FJ32GS406/606/608/610 and dsPIC33FJ64GS406/606/608/610 Sil Err and DS Cla Errata Document Revision						
Notification text:		SYST-05CCUP460						
		of devices.	Microchip has released a new DeviceDoc for the dsPIC33FJ32GS406/606/608/610 and dsPIC33FJ64GS406/606/608/610 Sil Err and DS Cla of devices. If you are using one of these devices please read the document located at dsPIC33FJ32GS406/606/608/610 and dsPIC33FJ64GS406/606/608/610 Sil Err and DS Cla.					
Notification Status: Final								
Description of Change: 1) Added new silicon issue 35 (UART). 2) Added new data sheet clarifications 1 (50 MIPS Pow (IPD)), 2 (Table 1: dsPIC33FJ32GS406/ 606/608/610 and dsPIC33FJ64GS406/606/608/610 Controller Families) and 3 (dsPIC33F Configuration Bits Description).								
Pre Change: No UART TRMT bit errata No Ipd specs for 50MIPS operation.								
			Post Change: 1) Adding UART errata: In TX mode, TRMT bit is set before the Shift register is empty. 2) Datasheet Clarification section: Ipd spec for 50MIPS operation is added.					
Impacts to Data Sheet: None								
	Reason for Change: To Improve Productivity							
	Change Implementation Status: Complete							
Date Document Changes Effective: 09 Feb 2016								
		NOTE: Plea	ase be advised that this is a c	d that this is a change to the document only the product has not been changed.				
Markings to Distinguish Revised from Unrevised Devices: N/A								
Attachment(s):		dsPIC33FJ3	2GS406/606/608/610 and ds	PIC33FJ64GS406	/606/608/610 Sil Err and D	S Cla		
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