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## Product Change Notification - SYST-14YWOB973 [\(Printer Friendly\)](#)

**Date:** 15 Dec 2015

**Affected CPNs:**  

**Notification subject:** ERRATA - PIC18(L)F25/45K22 Rev. A2/A3/A4/A5 Silicon Errata and Data Sheet Clarification Errata Document Revision

**Notification text:** SYST-14YWOB973

Microchip has released a new DeviceDoc for the PIC18(L)F25/45K22 Rev. A2/A3/A4/A5 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [PIC18\(L\)F25/45K22 Rev. A2/A3/A4/A5 Silicon Errata and Data Sheet Clarification](#).

**Notification Status:** Final

**Description of Change:** Data Sheet Clarifications: Added Module 1: ECCP.

**Pre Change:** NA

**Post Change:** ECCPxASbits.CCPxAS options have been corrected 101 = FLT0 pin – low level will cause shutdown event or Comparator C1 (async\_C1OUT) – output high will cause shutdown event 110 = FLT0 pin – low level will cause shutdown event or Comparator C2 (async\_C2OUT) – output high will cause shutdown event 111 = FLT0 pin – low level will cause shutdown event or Comparators C1 or C2 – output high will cause shutdown event

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 15 Dec 2015

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

**Attachment(s):** [PIC18\(L\)F25/45K22 Rev. A2/A3/A4/A5 Silicon Errata and Data Sheet Clarification](#)

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