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Product Change Notification - SYST-30OLOT731 [\(Printer Friendly\)](#)

Date: 31 Oct 2014

Affected CPNs: 

Notification subject: Data Sheet - PIC32MX1XX/2XX/5XX 64/100-pin Family Data Sheet Data Sheet Document Revision

Notification text: SYST-30OLOT731

Microchip has released a new DeviceDoc for the PIC32MX1XX/2XX/5XX 64/100-pin Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32MX1XX/2XX/5XX 64/100-pin Family Data Sheet](#).

Notification Status: Final

Description of Change: This revision includes the following major changes, which are referenced by their respective chapter in Table A-2. In addition, minor updates to text and formatting were incorporated throughout the document. 1) Section 20.0 "Parallel Master Port (PMP)": Added the RDSTART bit to the Parallel Port Control Register (see Table 20-1 and Register 20-1). Section 31.0 "40 MHz Electrical Characteristics": Updated the Idd Operating Current DC Characteristics (see Table 31-5); Updated the Idle Idle Current DC Characteristics (see Table 31-6); Updated the Ipd Power Down Current DC Characteristics (see Table 31-7); Updated the Internal FRC Accuracy (see table 31-19). Section 32.0 "50 MHz Electrical Characteristics": Updated the Idd Operating Current DC Characteristics (see Table 32-2); Updated the Idle Idle Current DC Characteristics (see Table 32-3); Updated the Ipd Power Down Current DC Characteristics (see Table 32-4).

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 31 Oct 2014

NOTE: Please be advised that this is a change to the document only the product has not been changed..

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [PIC32MX1XX/2XX/5XX 64/100-pin Family Data Sheet](#)

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