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Product Change Notification - SYST-27FORI766 [\(Printer Friendly\)](#)

Date: 07 May 2015

Affected CPNs:  

Notification subject: ERRATA - USB333x Family Silicon Errata and Data Sheet Clarification Errata Document Revision

Notification text: SYST-27FORI766

Microchip has released a new DeviceDoc for the USB333x Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [USB333x Family Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change: Initial release.

Pre Change: Anomaly 1 Solution 1 The host SoC should command the USB334x to enable Full Speed Transceivers and enter Low Power Mode with a single command (TX CMD = 0x05h). Do not split these two steps into separate TX CMDs. The downstream USB device will always enter USB suspend when this work around is used.

Post Change: Anomaly 1 Solution 1 The host SoC should command the USB334x to enable Full Speed Transceivers and enter Low Power Mode with a single command (TX CMD = 0x05h). Do not split these two steps into separate TX CMDs. The downstream USB device will always enter USB suspend when this work around is used. If the Function Control Register (04h-06h) is written immediately after the Port Suspend bit is set and the XcvtSelect bits are set back to 00, the noise will be eliminated. Note: The squelch threshold may need to be adjusted through the Varisense bits in the HS Compensation Register (31h) to ensure that the correct status update still occurs.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 07 May 2015

NOTE: Please be advised that this is a change to the document only the product has not been changed..

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [USB333x Family Silicon Errata and Data Sheet Clarification](#)

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