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Bulletin Date: 7/12/2016	**Bulletin Effective Date:** 7/12/2016
Title: C8051F04x Datasheet Update	
Bulletin Details	

Description:
Silicon Labs is pleased to announce C8051F04x Data Sheet version 1.6. The change and description are found below:

Updated Port Input Output Chapter to indicate that P2.0 and P2.1 are not skipped on the crossbar when configured to Analog Input mode.

Reason:
See description above regarding Port Input Output Chapter.

Product Identification:
The C8051F04x family is as follows:
C8051F040-GQ
C8051F040-GQR
C8051F041-GQ
C8051F041-GQR
C8051F042-GQ
C8051F042-GQR
C8051F043-GQ
C8051F043-GQR
C8051F044-GQ
C8051F044-GQR
C8051F045-GQ
C8051F045-GQR
C8051F046-GQ
C8051F046-GQR
C8051F047-GQ
C8051F047-GQR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at www.silabs.com

Customer Actions Needed:
None