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June 2015

BAT54XV2 Schottky Barrier Diode

Features

- Low Forward Voltage Drop
- Flat Lead, Surface Mount Device at 0.60mm Height
- Extremely Small Outline Plastic Package SOD523F
- Moisture Level Sensitivity 1
- Pb-free Version and RoHS Compliant
- Matte Tin (Sn) Lead Finish
- Green Mold Compound



SOD-523F Band Indicates Cathode BAT54XV2 Marking: 5B

Ordering Information

Part Number	Top Mark	Package	Packing Method	
BAT54XV2	5B	SOD-523F 2L	Tape and Reel	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage	30	V
V _R	Maximum DC Blocking Voltage	30	V
I _{F(AV)}	Average Rectified Forward Current	200	mA
I _{FSM}	Peak Forward Surge Current (Square Wave at pw = 300 μsec)	4	Α
T _J	Operating Junction Temperature	+125	°C
T _{STG}	Storage Temperature Range	-65 to +125	°C

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P_{D}	Power Dissipation	200	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ⁽¹⁾	500	°C/W

Note

1. Device mounted on FR-4 PCB minimum land pad.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _R	Breakdown Voltage	I _R = 10 μA	30		V
I _R	Reverse Leakage Current	V _R = 25 V		2	μΑ
V _F	Forward Voltage	I _F = 0.1 mA		0.24	
		I _F = 1 mA		0.32	
		I _F = 10 mA		0.40	V
		I _F = 30 mA		0.50	
		I _F = 100 mA		0.80	
T _{RR}	Reverse Recovery Time	$I_F = I_R = 10 \text{ mA}, R_L = 100 \Omega,$ $I_{RR} = 1 \text{ mA}$		5	nS
С	Capacitance	V _R = 1 V, f = 1 MHz		10	pF

Typical Performance Characteristics

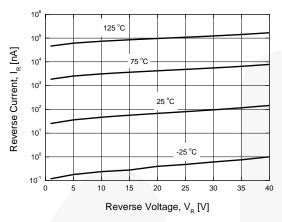
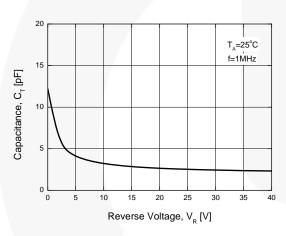


Figure 1. Reverse Current vs. Reverse Voltage

Figure 2. Forward Voltage vs. Forward Current



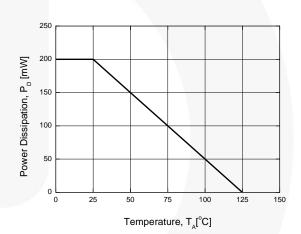
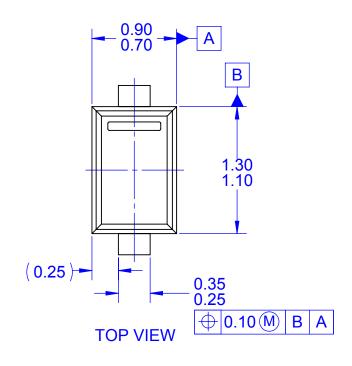
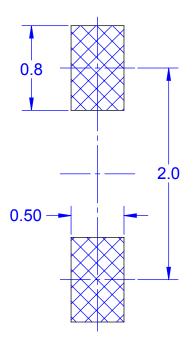


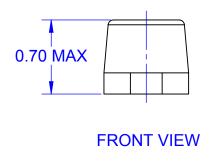
Figure 3. Total Capacitance

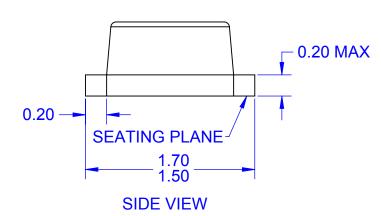
Figure 4. Power Derating





LAND PATTERN RECOMMENDATION





NOTES:

- A. CONFORMS TO JEITA SC-79
- B. ALL DIMENSIONS ARE IN MILLIMETERS
- C. DRAWING CONFORMS TO ASME Y14.5M-2009 D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS
- E. LAND PATTERN RECOMMENDATION IS BASED ON
- IPC7351A STANDARD SOD1609X65M F. DRAWING FILENAME: MKT-SOD523F1rev2







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Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.	
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.	

Rev 177

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