

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at <u>www.onsemi.com</u>

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor dates sheds, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor dates sheds and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use on similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor and its officers, employees, subsidiaries, affliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out or i, directly or indirectly, any lay bed ON Semiconductor and its officers, employees, ween if such claim alleges that ON Semiconductor was negligent regarding the d

March 2014



FSA4157, FSA4157A Low-Voltage, 1 Ω SPDT Analog Switch

Features

- FSA4157A Features Lower I_{CC} when the S Input is Lower Than V_{CC}
- Maximum 1.15 Ω On Resistance (R_{ON}) at 4.5 V V_{CC}
- 0.3 Ω Maximum R_{ON} Flatness at 4.5 V V_{CC}
- Space-Saving 6-lead, MicroPak[™] and SC70 6 Packages
- Broad V_{CC} Operating Range:
 FSA4157: 1.65 V to 5.5 V
 FSA4157A: 2.7 V to 5.5 V
- Fast Turn-On and Turn-Off Time
- Break-Before-Make Enable Circuitry
- Over-Voltage Tolerant TTL-Compatible Control Circuitry

Description

FSA4157 and FSA4157A are high performance Single Pole/Double Throw (SPDT) analog switches. Both devices feature ultra low R_{ON} of 1.15Ω maximum at $4.5 V V_{CC}$ and operates over the wide V_{CC} range of 1.65 V to 5.5 V for FSA4157, and 2.7 V to 5.5 V for FSA4157A. The device is fabricated with sub-micron CMOS technology to achieve fast switching speeds and is designed for break-before-make operation. The select input is TTL level compatible.

The FSA4157A features very low quiescent current even when the control voltage is lower than the V_{CC} supply. This feature services the mobile handset applications very well allowing for the direct interface with baseband processor general purpose I/Os.

Part Number	Top Mark	Package Description	Packing Method
FSA4157P6X	A57	6-Lead SC70, EIAJ SC88, 1.25 mm Wide	3000 Units Tape and Reel
FSA4157L6X	EG	6-Lead MicroPak,™ 1.0 mm Wide	5000 Units Tape and Reel
FSA4157AP6X	B57	6-Lead SC70, EIAJ SC88, 1.25 mm Wide	3000 Units Tape and Reel
FSA4157AL6X	EU	6-Lead MicroPak™, 1.0 mm Wide	5000 Units Tape and Reel

Ordering Information

FSA4157, FSA4157A — Low-Voltage, 1 Ω SPDT Analog Switch

Pin Configurations

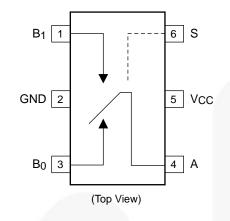


Figure 1. SC70 Pin Assignments

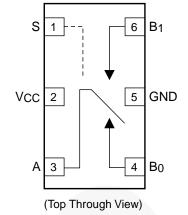


Figure 2. MicroPak[™] Pin Assignments

Pin Definitions

Pin# SC70	Pin# MicroPak™	Name	Description
1	6	B1	Data Ports
2	5	GND	Ground
3	4	B0	Data Ports
4	3	А	Data Ports
5	2	V _{cc}	Supply Voltage
6	1	S	Control Input

Truth Table

Control Input (S)	Function
Low	B0 connected to A
High	B1 connected to A

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Paramete	er	Min.	Max.	Unit
V _{cc}	Supply Voltage		-0.5	6.0	V
Vs	DC Switch Voltage ⁽¹⁾		-0.5	V _{CC} + 0.5	V
V _{IN}	DC Input Voltage ⁽¹⁾		-0.5	6.0	V
I _{IK}	DC Input Diode Current		-50		mA
I _{SW}	Switch Current			200	mA
I _{SWPEAK}	Peak Switch Current (Pulse at 1 ms du	uration, <10% Duty Cycle)		400	mA
Б	Dower Dissinction of 95%	SC70	100		m\//
P _D	Power Dissipation at 85°C	MicroPak™		180	mW
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Maximum Junction Temperature			+150	°C
TL	Lead Temperature (Soldering, 10 seco		+260	°C	
ESD	Electrostatic Discharge Capability		7500	V	

Note:

1. Input and output negative ratings may be exceeded if input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit		
N/		FSA4157	1.65	5.50	V	
V _{cc}	Supply Voltage	FSA4157A	2.7	5.5	V	
V _{CNTRL}	Control Input Voltage ⁽²⁾	0	V _{CC}	V		
V _{SW}	Switch Input Voltage	0	V _{cc}	V		
T _A	Operating Temperature		-40	+85	°C	
0	Thermal Resistance in Still Air	SC70		350	°C/W	
θ_{JA}		MicroPak™ (Estimated)		330	C/VV	

Note:

2. Control input must be held HIGH or LOW and it must not float.

DC Electrical Characteristics

Typical values are at 25°C unless otherwise specified.

					Ambier	nt Temp	erature			
Symbol	Parameter	Conditions	V _{cc} (V)		-25°			+85°C	Unit	
				Min.	Тур.	Max.	Min.	Max.		
		FSA4157 Only	1.8 to 2.7				1.0			
V _{IH}	Input Voltage High		2.7 to 3.6				2.0		V	
			4.5 to 5.5				2.4			
		FSA4157 Only	1.8 to 2.7					0.4		
V	Input Voltage Low	FSA4157A Only	2.7 to 3.6					0.4	V	
V _{IL}	input voltage Low		2.7 to 3.6					0.6	v	
			4.5 to 5.5					0.8		
1	Control Input	V _{IN} =0 V to V _{CC}	2.7 to 3.6				-1.0	1.0	μA	
I _{IN}	Leakage	V _{IN} =0 V to V _{CC}	4.5 to 5.5				-1.0	1.0	μΑ	
I _{NO(OFF)} , I _{NC(OFF)}	Off Leakage Current of Port B0 and B1	A=1 V, 4.5 V, B ₀ or B ₁ =4.5, 1 V	5.5		±2		-20	20	nA	
I _{A(ON)}	On Leakage Current of Port A	A=1 V, 4.5v, B_0 or B ₁ =4.5, 1 V,4.5 V or Floating	5.5		±4		-40	40	nA	
R _{ON}	Switch On	I _{OUT} =100 mA, B ₀ or B ₁ =1.5 V	2.7		2.6	4.0		4.3	Ω	
NON	Resistance	I _{OUT} =100mA, B ₀ or B ₁ =3.5V	4.5		0.95	1.15		1.30	52	
ΔR_{ON}	On Resistance Matching Between Channels ⁽⁴⁾	I_{OUT} =100 mA, B ₀ or B ₁ =1.5 V	4.5		0.06	0.12		0.15	Ω	
		I _{OUT} =100 mA, B ₀ or B _I =0 V, 0.75 V,1.5 V	2.7		1.4					
R _{FLAT(ON)} On Resistance Flatness ⁽⁴⁾		I _{OUT} =100 mA, B ₀ or B ₁ =0 V, 1 V, 2 V	4.5		0.2	0.3		0.4	Ω	
	Quiescent Supply	V _{IN} =0 V or V _{CC} ,	3.6		0.1	0.5		1.0		
I _{CC}	Current I _{OUT} =0 V		5.5		0.1	0.5		1.0	μA	
ΔI_{CC}	Increase in I _{CC} per Input	One Input at 2.7 V, others at V _{CC} or GND (FSA4157A Only)	4.3		0.2			10.0	μA	

FSA4157, FSA4157A — Low-Voltage, 1 Ω SPDT Analog Switch

Notes:

Measured by the voltage drop between the A and B pins at the indicated current through the switch. On 3. resistance is determined by the lower of the voltage on the two (A or B ports).

4.

 $\Delta R_{ON} = R_{ON max} - R_{ON min}$ measured at identical V_{CC}, temperature, and voltage. Flatness is defined as the difference between the maximum and minimum value of on resistance over the 5. specified range of conditions.

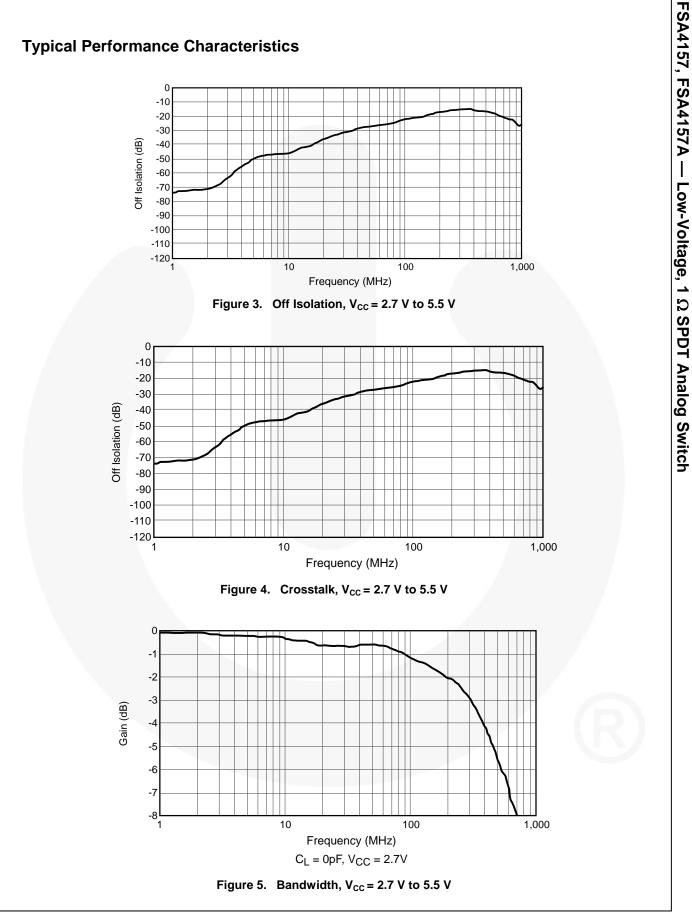
AC Electrical Characteristics

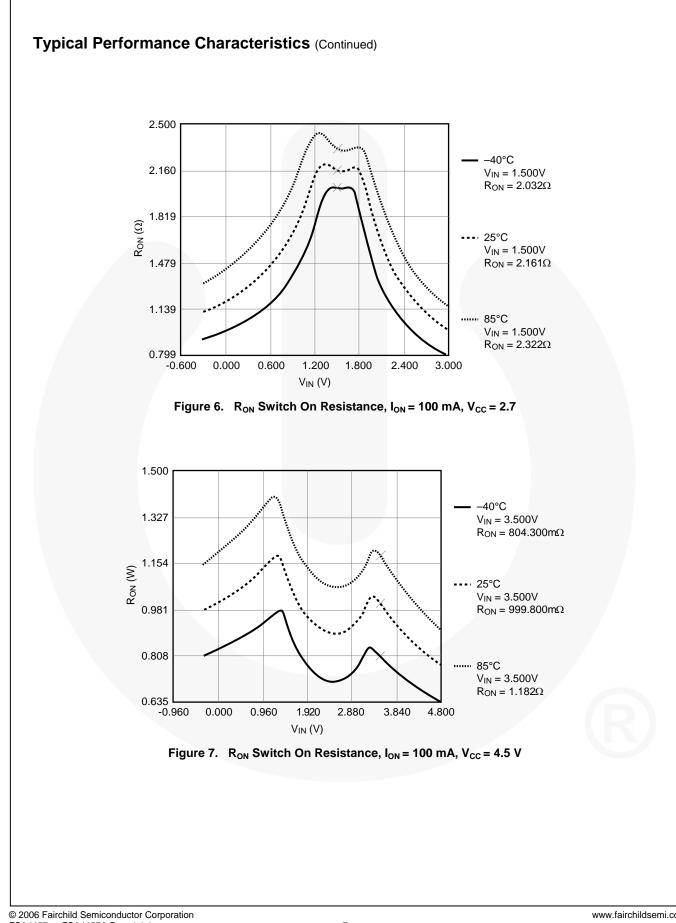
Typical values are at 25°C unless otherwise specified.

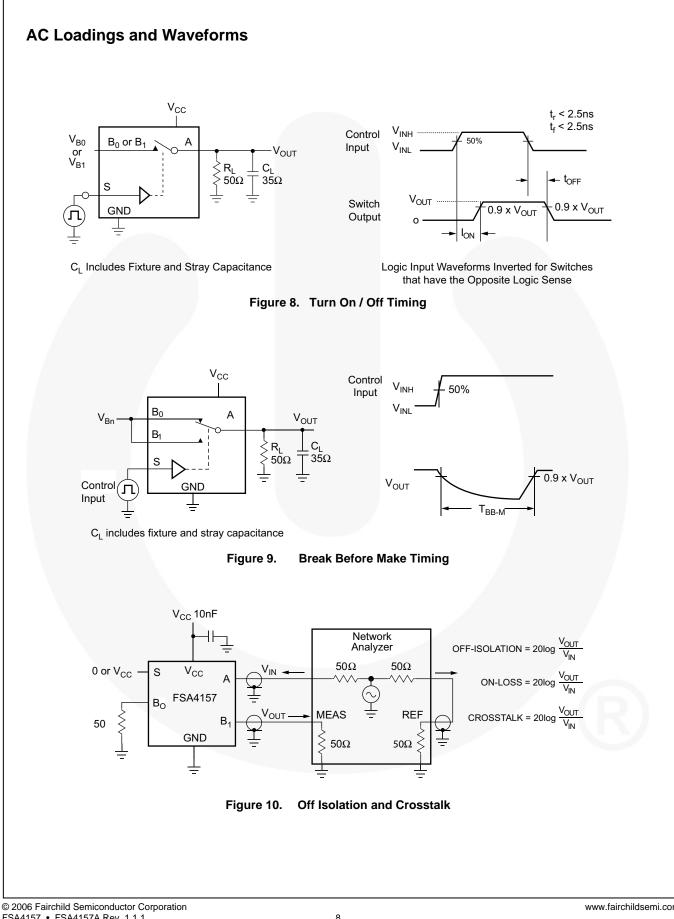
					Ambie	nt Temp	peratur	e	Unit	F igure	
Symbol	Parameter	Conditions	V _{cc} (V)	-25°			-40 to	+85°C	Unit	Figure	
				Min.	Тур.	Max.	Min.	Max.			
		$\begin{array}{l} B_0 \text{ or } B_1 {=} 1.5 \text{ V}, \\ R_L {=} 50 \ \Omega, \ C_L {=} 35 \text{ pF} \\ (FSA4157A Only) \end{array}$	2.7 to 3.6			60		65			
t _{ON}	t _{on} Turn-On Time	B_0 or $B_1=1.5V$, $R_L=50\Omega$, $C_L=35pF$	2.7 to 3.6			50		60	ns	Figure 8	
		B_0 or B_1 =1.5 V, R _L =50 Ω, C _L =35pF	4.5 to 5.5			35		40			
4	Turn-Off	B ₀ or B ₁ =1.5 V, R _L =50 Ω, C _L =35 pF	2.7 to 3.6			20		30		Figure 8	
LOFF	t _{OFF} Time	B_0 or B_1 =1.5 V, R _L =50 Ω, C _L =35 pF	4.5 to 5.5			15		20	ns		
	Break-	FSA4157	2.7 to 3.6								
t _{BBM}	Before-	Before-	4.5 to 5.5		20				ns	Figure 9	
	Make Time	FSA4157A Only	4.5 to 5.5		25						
Q	Charge	C _L =1.0 nF,	2.7 to 3.6		10				nC	Figure 1	
Q	Injection	V_{GE} =0 V, R _{GEN} =0 Ω	4.5 to 5.5		20				pC	Figure 11	
OIRR	Off Isolation	f=1 MHz, R _L =50 Ω	2.7 to 3.6		-70				dB	Figure 10	
OINN	On isolation	1=1 WI12, IXL=30 32	4.5 to 5.5		-70				uD.	Tigure it	
N/2 11			2.7 to 3.6		-70					_	
Xtalk Crosstalk	sstalk f=1 MHz, R_L =50 Ω	4.5 to 5.5		-70				dB	Figure 10		
	., -3db	D 50 0	2.7 to 3.6			300					
BW	Bandwidth	R _L =50 Ω	4.5 to 5.5			300			MHz	Figure 13	
THD	Total $R_{L}=600 \Omega, V_{IN}=1$		2.7 to 3.6		0.002				%	Figure 14	
Distortion	паппоп	f=20 Hz to 20 kHz	4.5 to 5.5		0.002				,0	, iguio i	

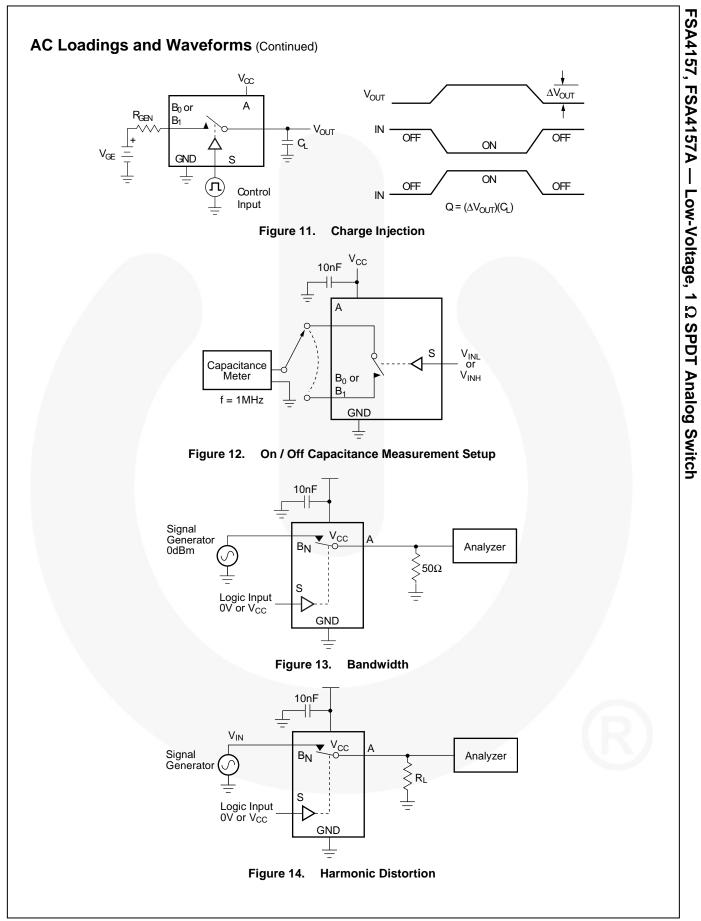
Capacitance

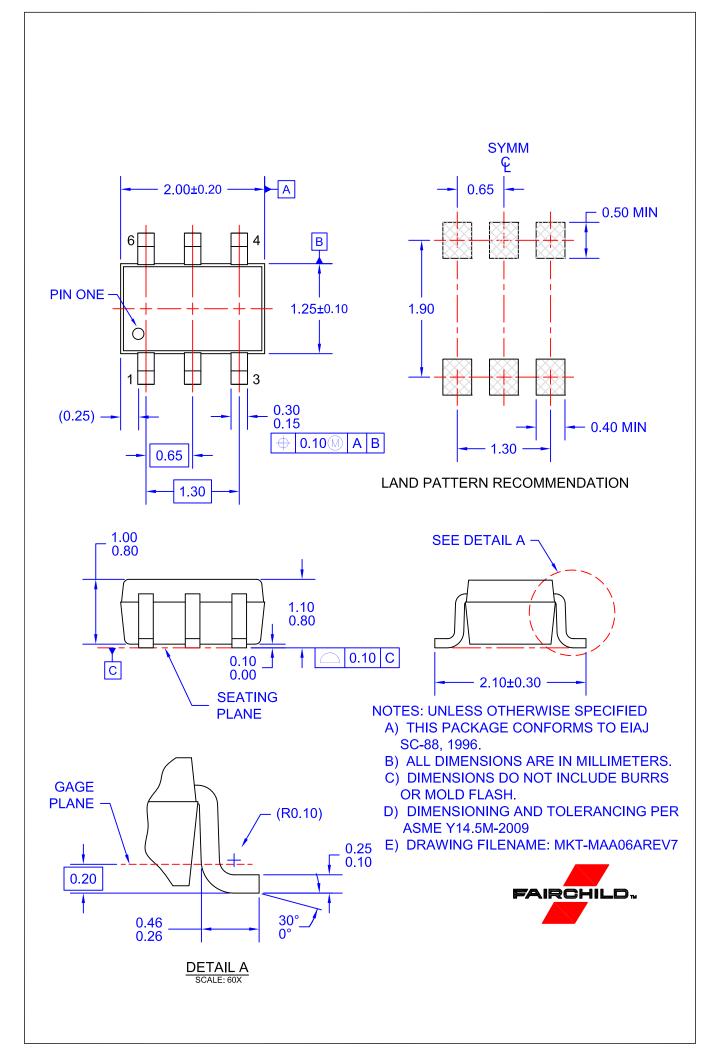
Symbol	Symbol Parameter Conditions		arameter Conditions V _{cc} (V)		Ambient Temperature -25°			Figure	
				Min.	Тур.	Max.			
C _{IN}	Control Pin Input Capacitance	f=1 MHz			3.5		pF	Figure 12	
C_{OFF}	B Port Off Capacitance	f=1 MHz	4.5		12.0		pF	Figure 12	
C _{ON}	On Capacitance	f=1 MHz	4.5		40.0		pF	Figure 12	

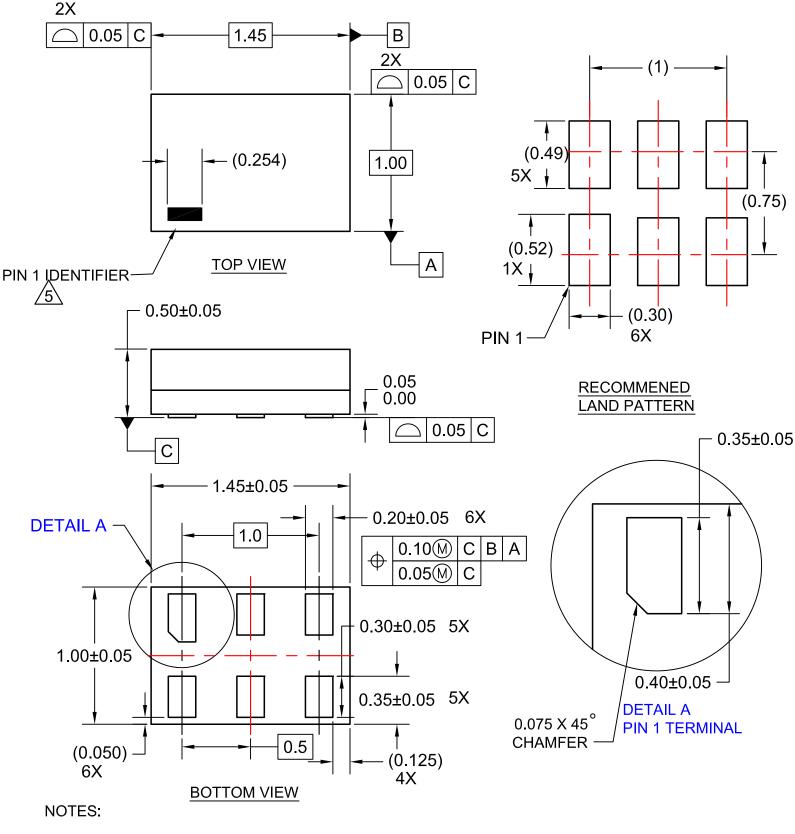












- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
- 4. LANDPATTERN RECOMMENDATION PER FSC
- 5. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY
- OTHER LINE IN THE MARK CODE LAYOUT.
- 6. FILENAME AND REVISION: MAC06AREV6





* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. TO OBTAIN THE LATEST, MOST UP-TO-DATE DATASHEET AND PRODUCT INFORMATION, VISIT OUR WEBSITE AT <u>HTTP://WWW.FAIRCHILDSEMI.COM</u>, FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

AUTHORIZED USE

Unless otherwise specified in this data sheet, this product is a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability. This product may not be used in the following applications, unless specifically approved in writing by a Fairchild officer: (1) automotive or other transportation, (2) military/aerospace, (3) any safety critical application – including life critical medical equipment – where the failure of the Fairchild product reasonably would be expected to result in personal injury, death or property damage. Customer's use of this product is subject to agreement of this Authorized Use policy. In the event of an unauthorized use of Fairchild's product, Fairchild accepts no liability in the event of product failure. In other respects, this product shall be subject to Fairchild's Worldwide Terms and Conditions of Sale, unless a separate agreement has been signed by both Parties.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Terms of Use

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Definition of Terms		
Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 177