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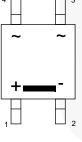
April 2014

MB1S - MB8S 0.5 A Bridge Rectifiers

Features

- Low-Leakage
- Surge Overload Rating: 35 A peak
- Ideal for Printed Circuit Board
- UL Certified: UL #E258596





SOIC-4 Polarity symbols molded or mark on body

Ordering Informations

Part Number	Marking	Package	Packing Method
MB1S	MB1S		
MB2S	MB2S		
MB4S	MB4S	SOIC-4	Tape and Reel
MB6S	MB6S		
MB8S	MB8S		

Description

The MB family of bridge rectifiers is a 0.5 A rectifier family that achieves high surge current absorption within a very small foot print. Within its small 35 mm² form factor, the MB family shines in its surge capability. In order to absorb high surge currents, the design supports a 35 A I_{FSM} rating and a 5.0 A²Sec I²T rating. Devices in the family are also rated to breakdown voltages of up to 1000 V. These features make the MB family ideal for small power supplies that need a little extra surge capability.

For higher I_{FAV} current ratings, lower profile packaging, or lower V_F values, explore the Fairchild MDB family of bridge rectifiers. For improved V_F and efficiency values in the MB package or even higher surge capability, ask about Fairchild's pending MBxSV family.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value					Unit
Symbol	F al allielei		MB2S	MB4S	MB6S	MB8S	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage		200	400	600	800	V
V _{RMS}	Maximum RMS Bridge Input Voltage		140	280	420	560	V
V _R	DC Reverse Voltage (Rated V _R)		200	400	600	800	V
I _{F(AV)}	Average Rectified Forward Current at $T_A = 50^{\circ}C$		0.5				А
I _{FSM}	Non-Repetitive Peak Forward Surge Current:358.3 ms Single Half-Sine-Wave35		A				
T _{STG}	Storage Temperature Range		-55 to +150				°C
TJ	Operating Junction Temperature Range		-55 to +150				°C

Thermal Characteristics

Symbol	Parameter	Value	Unit
PD	Power Dissipation	1.4	W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, per Leg ⁽¹⁾	85	°C/W
$R_{ extsf{ heta}JL}$	Thermal Resistance, Junction to Lead, per Leg ⁽¹⁾	20	°C/W

Note:

1. Device mounted on PCB with 0.5 x 0.5 inch (13 x 13 mm) lead length.

Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Conditions	Value	Unit
V _F	Forward Voltage, per Bridge	I _F = 0.5 A	1.0	V
I _R	Reverse Current, per Leg at Rated V_R	$T_A = 25^{\circ}C$	5.0	μΑ
		T _A = 125°C	0.5	mA
l ² t	I ² t Rating for Fusing	t < 8.3 ms	5.0	A ² s
C _T	Total Capacitance, per Leg	V _R = 4.0 V, f = 1.0 MHz	13	pF

Typical Performance Characteristics

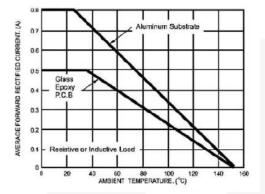
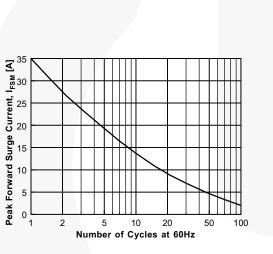


Figure 1. Derating Curve for Output Rectified Current





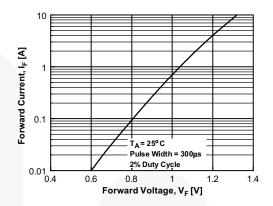


Figure 2. Forward Voltage Characteristics

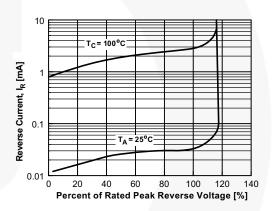
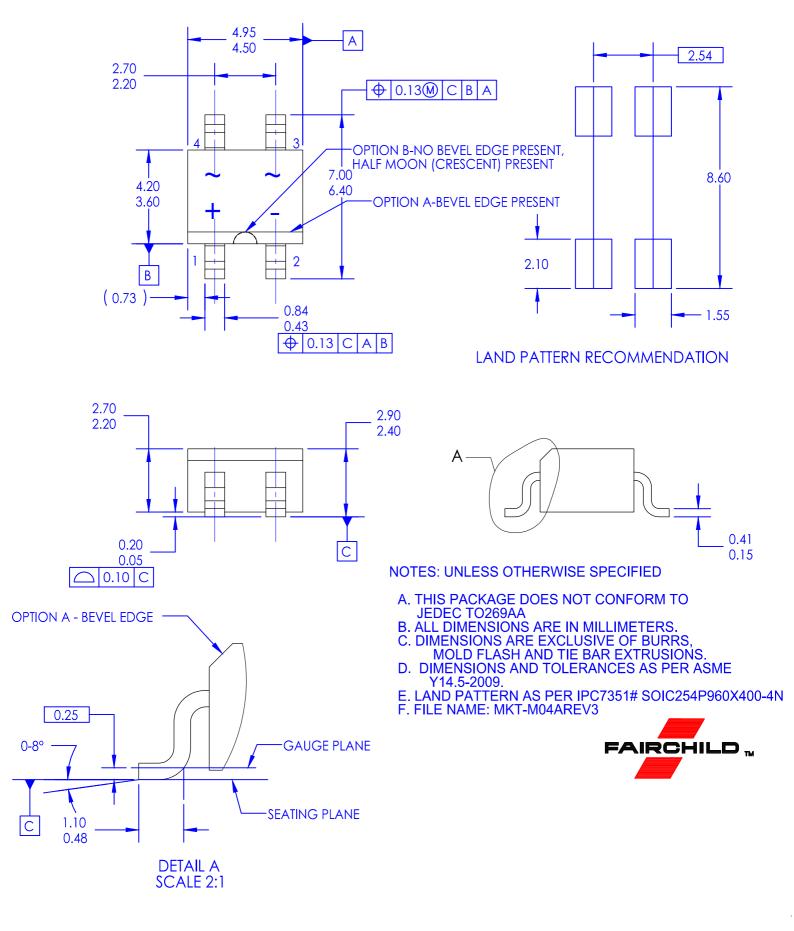


Figure 4. Reverse Current vs. Reverse Voltage





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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