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NC7SV125

TinyLogic® ULP-A Buffer with 3-STATE Output

General Description

The NC7SV125 is a single buffer with 3-STATE output from Fairchild's Ultra Low Power-A (ULP-A) Series of TinyLogic®. ULP-A is ideal for applications that require extreme high speed, high drive and low power. This product is designed for wide low voltage operating range (0.9V to 3.6V $\rm V_{CC}$) and applications that require more drive and speed than the TinyLogic ULP series, but still offer best in class low power operation.

The NC7SV125 is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve high-speed operation while maintaining low CMOS power dissipation.

Features

- 0.9V to 3.6V V_{CC} supply operation
- \blacksquare 3.6V overvoltage tolerant I/O's at $\rm V_{CC}$ from 0.9V to 3.6V
- Extremely High Speed tpD

1.0 ns typ for 2.7V to 3.6V $V_{\rm CC}$

2.0 ns typ for 2.3V to 2.7V $\rm V_{\rm CC}$

3.0 ns typ for 1.65V to 1.95V $\ensuremath{\text{V}_{\text{CC}}}$

3.5 ns typ for 1.4V to 1.6V $V_{\mbox{CC}}$

6.0 ns typ for 1.1V to 1.3V $\rm V_{CC}$ 13 ns typ for 0.9V $\rm V_{CC}$

- Power-Off high impedance inputs and outputs
- High Static Drive (I_{OH}/I_{OL})

±24 mA @ 3.00V V_{CC}

±18 mA @ 2.30V V_{CC}

±6 mA @ 1.65V V_{CC}

 ± 4 mA @ 1.4V V_{CC}

 ± 2 mA @ 1.1V V_{CC}

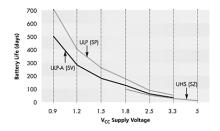
 ± 0.1 mA @ 0.9V V_{CC}

- Uses patented Quiet Series[™] noise/EMI reduction circuitry
- Ultra small MicroPak™ leadfree package
- Ultra low dynamic power

Ordering Code:

| Order Number | Package Number | Product Code Top Mark | Package Description | Supplied As |
|--------------|-------------------|--------------------------|---------------------------------------|---------------------------|
| NC7SV125P5X | MAA05A | V25 | 5-Lead SC70, EIAJ SC-88a, 1.25mm Wide | 3k Units on Tape and Reel |
| NC7SV125L6X | MAC06A | H6 | 6-Lead MicroPak, 1.0mm Wide | 5k Units on Tape and Reel |

Battery Life vs. V_{CC} Supply Voltage



TinyLogic ULP and ULP-A with up to 50% less power consumption can extend your battery life significantly.

Battery Life = $(V_{battery} *I_{battery} *.9)/(P_{device})/24hrs/day$

Where, $P_{\text{device}} = (I_{CC} * V_{CC}) + (C_{PD} + C_{L}) * V_{CC}^{2} * f$

Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with $C_L=15\,\mathrm{pF}$ load

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MicroPak™, and Quiet Series™ are trademarks of Fairchild Semiconductor Corporation

Logic Symbol

IEEE/IEC



Pin Descriptions

| Pin Names | Description |
|------------------|-------------|
| A, OE | Input |
| Y | Output |
| NC | No Connect |

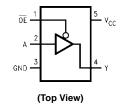
Function Table

| Inj | out | Output |
|-----|------|--------|
| OE | In A | Out Y |
| L | L | L |
| L | Н | Н |
| Н | Х | Z |

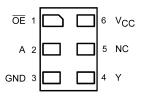
- H = HIGH Logic Level
 L = LOW Logic Level
 X = HIGH or LOW Logic Level
 Z = HIGH Impedance State

Connection Diagrams

Pin Assignments for SC70



Pad Assignments for MicroPak



(Top Thru View)

±24 mA

Absolute Maximum Ratings(Note 1)

$\begin{array}{lll} \mbox{Supply Voltage (V$_{CC}$)} & -0.5 \mbox{V to } +4.6 \mbox{V} \\ \mbox{DC Input Voltage (V$_{IN}$)} & -0.5 \mbox{V to } +4.6 \mbox{V} \\ \end{array}$

DC Output Voltage (V_{OUT})

 $\label{eq:local_$

DC Output Diode Current (I_{OK})

 $\begin{array}{lll} \rm V_{OUT} < 0V & -50~mA \\ & \rm V_{OUT} > V_{CC} & \pm 50~mA \\ DC~Output~Source/Sink~Current~(I_{OH}/I_{OL}) & \pm 50~mA \\ \end{array}$

 $\operatorname{DC}\operatorname{V}_{\operatorname{CC}}$ or Ground Current per

Supply Pin (I_{CC} or Ground) \pm 50 mA Storage Temperature Range (T_{STG}) -65° C to +150 $^{\circ}$ C

Recommended Operating Conditions (Note 3)

Supply Voltage 0.9V to 3.6VInput Voltage (V_{IN}) 0V to 3.6V

Output Voltage (V_{OUT})

 $V_{CC} = 0.0V$ 0V to 3.6V HIGH or LOW State 0V to V_{CC}

Output Current in I_{OH}/I_{OL} $V_{CC} = 3.0 \text{V to } 3.6 \text{V}$

Free Air Operating Temperature (T_A) -40° C to +85°C

Minimum Input Edge Rate (Δt/ΔV)

 $V_{IN} = 0.8V$ to 2.0V, $V_{CC} = 3.0V$ 10 ns/V

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: IO Absolute Maximum Rating must be observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

| Symbol | Parameter | V _{CC} | T _A = +25°C | | $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ | | Units | Conditions |
|-----------------|----------------|------------------------------|------------------------|--------------------------|---|--------------------------|-------|---------------------------|
| Symbol | Farameter | (V) | Min | Max | Min | Max | Units | Conditions |
| V _{IH} | HIGH Level | 0.90 | 0.65 x V _{CC} | | 0.65 x V _{CC} | | | |
| | Input Voltage | $1.10 \le V_{CC} \le 1.30$ | 0.65 x V _{CC} | | 0.65 x V _{CC} | | | |
| | | $1.40 \le V_{CC} \le 1.60$ | 0.65 x V _{CC} | | 0.65 x V _{CC} | | V | |
| | | $1.65 \leq V_{CC} \leq 1.95$ | 0.65 x V _{CC} | | 0.65 x V _{CC} | | · · | |
| | | $2.30 \leq V_{CC} < 2.70$ | 1.6 | | 1.6 | | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | 2.0 | | 2.0 | | | |
| V _{IL} | LOW Level | 0.90 | | 0.35 x V _{CC} | | 0.35 x V _{CC} | | |
| | Input Voltage | $1.10 \le V_{CC} \le 1.30$ | | $0.35 \times V_{\rm CC}$ | | $0.35 \times V_{\rm CC}$ | | |
| | | $1.40 \le V_{CC} \le 1.60$ | | $0.35 \times V_{\rm CC}$ | | $0.35 \times V_{\rm CC}$ | V | |
| | | $1.65 \le V_{CC} \le 1.95$ | | $0.35 \times V_{\rm CC}$ | | $0.35 \times V_{\rm CC}$ | · | |
| | | $2.30 \leq V_{CC} < 2.70$ | | 0.7 | | 0.7 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | 0.8 | | 8.0 | | |
| V _{OH} | HIGH Level | 0.90 | V _{CC} - 0.1 | | V _{CC} - 0.1 | | | |
| | Output Voltage | $1.10 \le V_{CC} \le 1.30$ | V _{CC} - 0.1 | | V _{CC} - 0.1 | | | |
| | | $1.40 \le V_{CC} \le 1.60$ | V _{CC} – 0.2 | | V _{CC} - 0.2 | | | I _{OH} = -100 μA |
| | | $1.65 \le V_{CC} \le 1.95$ | V _{CC} - 0.2 | | V _{CC} - 0.2 | | | ΙΟΗ = -100 μΑ |
| | | $2.30 \le V_{CC} < 2.70$ | V _{CC} – 0.2 | | V _{CC} - 0.2 | | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | V _{CC} - 0.2 | | V _{CC} – 0.2 | | | |
| | | $1.10 \le V_{CC} \le 1.30$ | 0.75 x V _{CC} | | 0.75 x V _{CC} | | | $I_{OH} = -2 \text{ mA}$ |
| | | $1.40 \le V_{CC} \le 1.60$ | 0.75 x V _{CC} | | 0.75 x V _{CC} | | V | $I_{OH} = -4 \text{ mA}$ |
| | | $1.65 \le V_{CC} \le 1.95$ | 1.25 | | 1.25 | | | I _{OH} = -6 mA |
| | | $2.30 \le V_{CC} < 2.70$ | 2.0 | | 2.0 | | | IOH - O IIIV |
| | | $2.30 \le V_{CC} < 2.70$ | 1.8 | | 1.8 | | | I _{OH} = -12 mA |
| | | $2.70 \le V_{CC} \le 3.60$ | 2.2 | | 2.2 | | | .Он |
| | | $2.30 \le V_{CC} < 2.70$ | 1.7 | | 1.7 | | | I _{OH} = -18 mA |
| | | $2.70 \leq V_{CC} \leq 3.60$ | 2.4 | | 2.4 | | | |
| i | | $2.70 \le V_{CC} \le 3.60$ | 2.2 | | 2.2 | | | $I_{OH} = -24 \text{ mA}$ |

DC Electrical Characteristics (Continued)

| Symbol | Parameter | V _{CC} | T _A = | +25°C | T _A = -40° | C to +85°C | Units | Conditions |
|------------------|---------------------------|------------------------------|------------------|------------------------|-----------------------|------------------------|-------|-----------------------------|
| Syllibol | Farameter | (V) | Min | Max | Min | Max | Units | Conditions |
| V _{OL} | LOW Level | 0.90 | | 0.1 | | 0.1 | | |
| | Output Voltage | $1.10 \le V_{CC} \le 1.30$ | | 0.1 | | 0.1 | | |
| | | $1.40 \le V_{CC} \le 1.60$ | | 0.2 | | 0.2 | | I _{OL} = 100 μA |
| | | $1.65 \leq V_{CC} \leq 1.95$ | | 0.2 | | 0.2 | | I _{OL} = 100 μA |
| | | $2.30 \le V_{CC} < 2.70$ | | 0.2 | | 0.2 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | 0.2 | | 0.2 | | |
| | | $1.10 \le V_{CC} \le 1.30$ | | 0.25 x V _{CC} | | 0.25 x V _{CC} | V | I _{OL} = 2 mA |
| | | $1.40 \le V_{CC} \le 1.60$ | | 0.25 x V _{CC} | | 0.25 x V _{CC} | · · | I _{OL} = 4 mA |
| | | $1.65 \le V_{CC} \le 1.95$ | | 0.3 | | 0.3 | | I _{OL} = 6 mA |
| | | $2.30 \le V_{CC} < 2.70$ | | 0.4 | | 0.4 | | I _{OL} = 12 mA |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | 0.4 | | 0.4 | | 10L - 12 IIIA |
| | | $2.30 \le V_{CC} < 2.70$ | | 0.6 | | 0.6 | | I _{OL} = 18 mA |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | 0.4 | | 0.4 | | IOL - 10 IIIA |
| | | $2.70 \le V_{CC} \le 3.60$ | | 0.55 | | 0.55 | | I _{OL} = 24 mA |
| I _{IN} | Input Leakage Current | 0.90 to 3.60 | | ±0.1 | | ±0.5 | μΑ | $0 \le V_I \le 3.6V$ |
| I _{OZ} | 3-STATE Output Leakage | 0.90 to 3.60 | | ±0.5 | | ±0.5 | μΑ | $V_I = V_{IH}$ or V_{IL} |
| | | | | | | | | $0 \le V_O \le 3.6V$ |
| I _{OFF} | Power Off Leakage Current | 0 | | 0.5 | | 0.5 | μΑ | $0 \le (V_I, V_O) \le 3.6V$ |
| I _{CC} | Quiescent Supply Current | 0.90 to 3.60 | | 0.9 | | 0.9 | μА | $V_I = V_{CC}$ or GND |
| l | | 0.90 to 3.60 | | | | ±0.9 | μΛ | $V_{CC} \le V_I \le 3.6V$ |

AC Electrical Characteristics

| Symbol | Parameter | V _{CC} | $T_A = +25^{\circ}C$ | | $T_A = -40^{\circ}C$ to $+85^{\circ}C$ | | Units | Conditions | Figure | |
|------------------|----------------------------------|-------------------------------|----------------------|-----|--|-----|-------|------------|--|---------|
| Syllibol | Farameter | (V) | Min | Тур | Max | Min | Max | Units | Conditions | Number |
| t _{PHL} | Propagation Delay | 0.90 | | 13 | | | | | $C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$ | |
| t _{PLH} | | $1.10 \le V_{CC} \le 1.30$ | 3.0 | 6.0 | 9.8 | 1.9 | 14.9 | | $C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$ | |
| | | $1.40 \le V_{CC} \le 1.60$ | 1.0 | 3.5 | 5.3 | 0.8 | 5.7 | ns | | Figures |
| | | 1.65 ≤ V _{CC} ≤ 1.95 | 0.9 | 3.0 | 4.3 | 0.8 | 4.6 | 115 | C _L = 30 pF | 1, 2 |
| | | $2.30 \le V_{CC} < 2.70$ | 8.0 | 2.0 | 2.8 | 0.7 | 3.0 | | $R_L = 500\Omega$ | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | 0.5 | 1.0 | 2.6 | 0.5 | 2.8 | | | |
| t _{PZH} | Output | 0.90 | | 14 | | | | | C _L = 30 pF | |
| t_{PZL} | Enable Time | $1.10 \le V_{CC} \le 1.30$ | 3.0 | 6.0 | 9.7 | 2.0 | 16.4 | | $R_U = 1k\Omega$ | |
| | | $1.40 \leq V_{CC} \leq 1.60$ | 1.2 | 4.0 | 6.0 | 1.0 | 7.5 | ns | $R_D = 1k\Omega$ | Figures |
| | | 1.65 ≤ V _{CC} ≤ 1.95 | 1.0 | 3.0 | 4.5 | 0.9 | 5.0 | 115 | $S_1 = GND \text{ for } t_{PZH}$ | 1, 2 |
| | | $2.30 \le V_{CC} < 2.70$ | 8.0 | 2.0 | 3.0 | 0.7 | 3.4 | | $S_1 = V_I \text{ for } t_{PZL}$ | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | 0.5 | 1.2 | 2.6 | 0.4 | 2.9 | | $V_I = 2 \times V_{CC}$ | |
| t _{PHZ} | Output | 0.90 | | 14 | | | | | C _L = 30 pF | |
| t_{PLZ} | Disable Time | $1.10 \le V_{CC} \le 1.30$ | 2.0 | 5.0 | 9.5 | 2.0 | 14.0 | | $R_U = 1k\Omega$ | |
| | | $1.40 \le V_{CC} \le 1.60$ | 1.2 | 3.0 | 5.5 | 1.1 | 7.0 | ns | $R_D = 1k\Omega$ | Figures |
| | | $1.65 \le V_{CC} \le 1.95$ | 1.0 | 2.0 | 5.6 | 0.8 | 5.8 | 113 | $S_1 = GND \text{ for } t_{PHZ}$ | 1, 2 |
| | | $2.30 \le V_{CC} < 2.70$ | 8.0 | 1.5 | 4.2 | 0.5 | 5.0 | | $S_1 = V_I \text{ for } t_{PLZ}$ | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | 0.5 | 1.0 | 3.9 | 0.4 | 4.2 | | $V_I = 2 \times V_{CC}$ | |
| C _{IN} | Input Capacitance | 0 | | 2.0 | | | | pF | | |
| C _{OUT} | Output Capacitance | 0 | | 4.5 | | | | pF | | |
| C _{PD} | Power Dissipation Capacitance | 0.90 to 3.60 | | 10 | | | | pF | $V_I = 0V \text{ or } V_{CC}$ f = 10 MHz | |

AC Loading and Waveforms

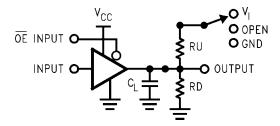
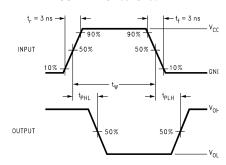


FIGURE 1. AC Test Circuit



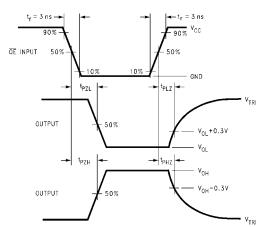


FIGURE 2. AC Waveforms

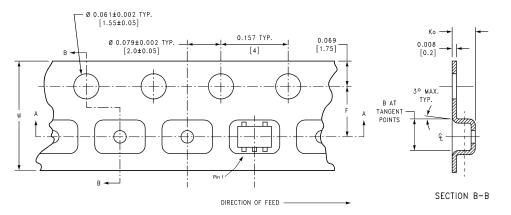
| Symbol | V _{CC} | | | | | | |
|-----------------|-----------------|-----------------------------------|------------------------------------|--------------------|--------------------|--------------------|--|
| 5,20. | $3.3V \pm 0.3V$ | $\textbf{2.5V} \pm \textbf{0.2V}$ | $\textbf{1.8V} \pm \textbf{0.15V}$ | $1.5V \pm 0.10V$ | $1.2V \pm 0.10V$ | 0.9V | |
| V _{mi} | 1.5V | V _{CC} /2 | V _{CC} /2 | V _{CC} /2 | V _{CC} /2 | V _{CC} /2 | |
| V _{mo} | 1.5V | V _{CC} /2 | V _{CC} /2 | V _{CC} /2 | V _{CC} /2 | V _{CC} /2 | |

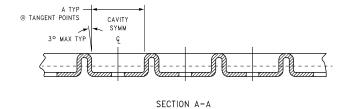
Tape and Reel Specification

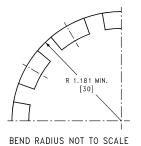
TAPE FORMAT for SC70

| ., = . • | | | | |
|------------|--------------------|-----------|--------|------------|
| Package | Tape | Number | Cavity | Cover Tape |
| Designator | Section | Cavities | Status | Status |
| | Leader (Start End) | 125 (typ) | Empty | Sealed |
| P5X | Carrier | 3000 | Filled | Sealed |
| | Trailer (Hub End) | 75 (typ) | Empty | Sealed |

TAPE DIMENSIONS inches (millimeters)







| | MicroPak | 1 | Marine | 0 " | 0 |
|--|------------------------|--------------------------|---------------------------------------|--------|------------------------------------|
| Package | Tape | | Number | Cavity | Cover Tape |
| Designator | Section | | Cavities | Status | Status |
| | Leader (Start | End) | 125 (typ) | Empty | Sealed |
| L6X | Carrier | | 5000 | Filled | Sealed |
| | Trailer (Hub | End) | 75 (typ) | Empty | Sealed |
| APE DIMENSIONS | inches (millimeters) | /- ÿ1.50 ^{+0.1} | 1 | | |
| 8.00 ^{+0.30} A | 4.00 | Ø 0.50 ±0.05 | B B B B B B B B B B B B B B B B B B B | | AX. 1.15±0.05 CTION B-B SCALE:10X |
| <u>{</u> | 5° MAX | -1.60±0.05 | 0.254±0.020 0.70±0.05 | | |
| EEL DIMENSIONS | s inches (millimeters) | _ | | | → → W ₁ |
| | | $\langle \chi \rangle$ | TAPE SLOT | | <u> </u> |
| | | | - | B C | |
| A - | | DETAIL X | | 1 | W ₃ |
| A - Canal A - Ca | B | | DET SCA | AIL X | → W ₂ |
| | B C | D N | DET | AIL X | |
| Fape A Size 7.0 | | D N | DET SCA | AIL X | → W ₂ |

NOTES:

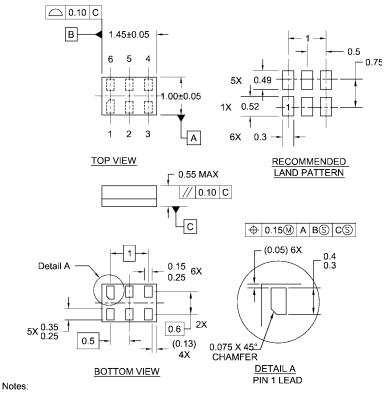
- A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A.
- B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
- C. DIMENSIONS ARE IN MILLIMETERS.

MAA05ARevC

DETAIL A

5-Lead SC70, EIAJ SC-88a, 1.25mm Wide Package Number MAA05A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



- 1. JEDEC PACKAGE REGISTRATION IS ANTICIPATED
- 2. DIMENSIONS ARE IN MILLIMETERS
 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06ARevB

6-Lead MicroPak, 1.0mm Wide Package Number MAC06A

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- 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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