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March 2015



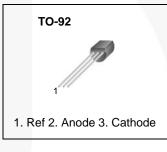
## FAN431A Programmable Shunt Regulator

### Features

- Programmable Output Voltage to 36 V
- Low Dynamic Output Impedance: 0.2  $\Omega$  (Typical)
- Sink Current Capability: 1.0 to 100 mA
- Equivalent Full-Range Temperature Coefficient of 50 ppm/°C (Typical)
- Temperature Compensated for Operation Over Full Rated Operating Temperature Range
- Low Output Noise Voltage
- Fast Turn-on Response

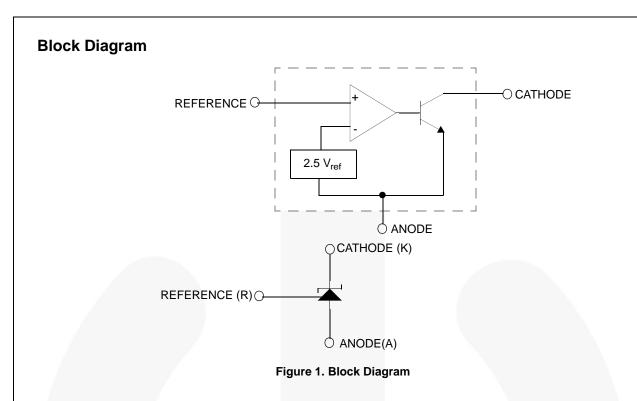
## Description

The FAN431A is a three-terminal output adjustable regulator with thermal stability over the full operating temperature range. The output voltage can be set to any value between V<sub>REF</sub> (approximately 2.5 V) and 36 V with two external resistors. This device has a typical dynamic output impedance of 0.2  $\Omega$ . Active output circuit provides a sharp turn-on characteristic, making this device excellent replacements for zener diodes in many applications.



### **Ordering Information**

Part Number	Operating Temperature Range	Output Voltage Tolerance	Top Mark	Package	Packing Method
FAN431AZXA	-25 to +85°C	1%	FAN431A	TO-92 3L	Ammo



## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value		
V <sub>KA</sub>	Cathode Voltage	37	V	
I <sub>KA</sub>	Cathode current Range (Continuous)	-100 to +150	mA	
I <sub>REF</sub>	Reference Input Current Range	-0.05 to +10.00	mA	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient <sup>(1,2)</sup> Z Suffix Package	132	°C/W	
PD	Power Dissipation <sup>(3,4)</sup> Z Suffix Package	940	mW	
TJ	Junction Temperature	150	°C	
T <sub>OPR</sub>	Operating Temperature Range	-25 to +85	°C	
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C	

#### Notes:

- 1. Thermal resistance test board
  - Size: 1.6 mm x 76.2 mm x 114.3 mm (1S0P)
  - JEDEC Standard: JESD51-3, JESD51-7.
- 2. Assume no ambient airflow.
- 3.  $T_{JMAX} = 150^{\circ}C$ , Ratings apply to ambient temperature at 25°C. 4. Power dissipation calculation:  $P_D = (T_J T_A) / R_{\theta JA}$ .

### **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter		Max.	Unit
V <sub>KA</sub>	Cathode Voltage	V <sub>REF</sub>	36	V
I <sub>KA</sub>	Cathode Current	1.0	100	mA

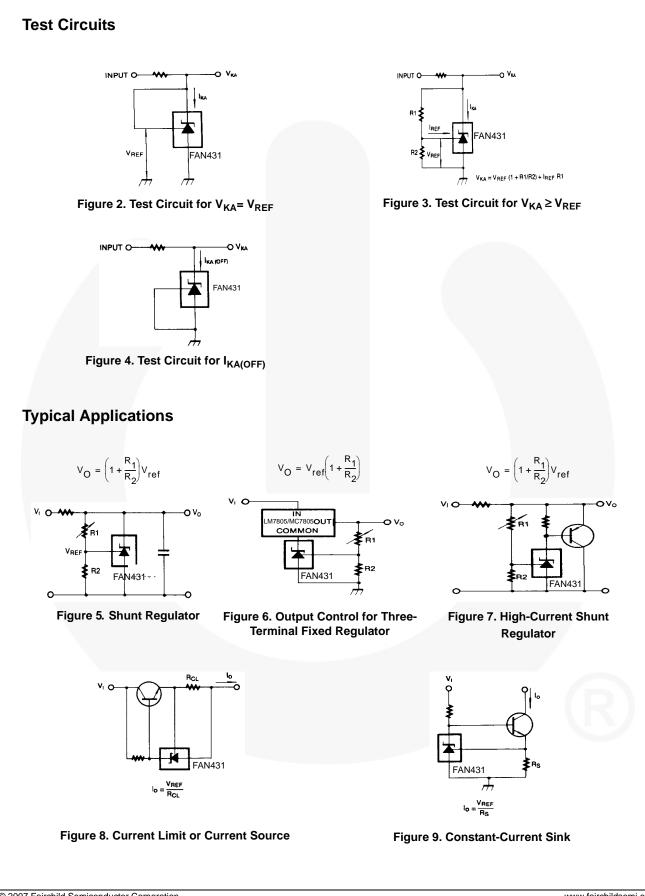
### Electrical Characteristics<sup>(5)</sup>

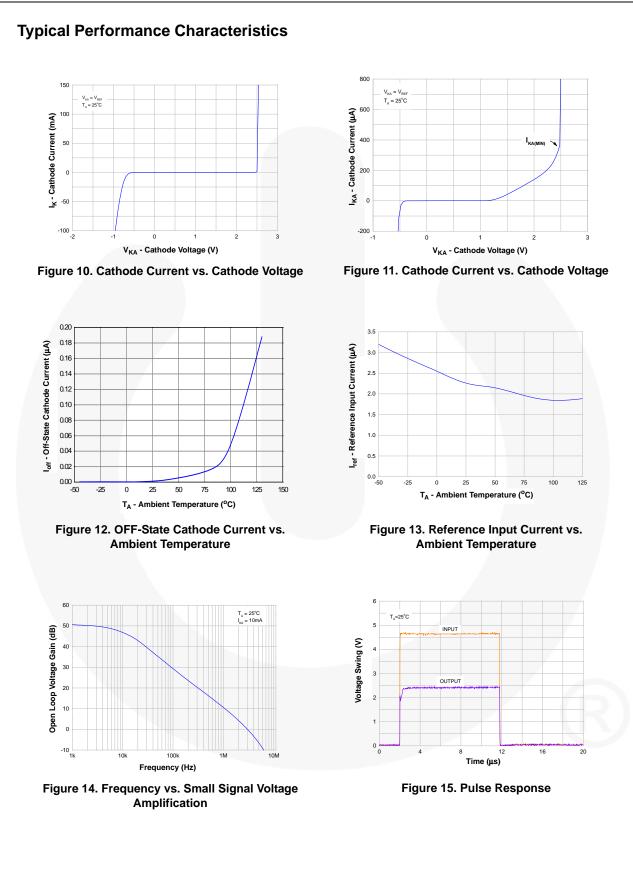
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Devementer	Conditions		FAN431A			L Incit
Symbol	Parameter			Min.	Тур.	Max.	Unit
V <sub>REF</sub>	Reference Input Voltage	$V_{KA} = V_{REF}, I_{KA} = 1$	10 mA	2.470	2.495	2.520	V
$\Delta V_{REF} / \Delta T$	Deviation of Reference Input Voltage Over- Temperature	$\label{eq:VKA} \begin{split} V_{KA} &= V_{REF}, \ I_{KA} = 10 \ \text{mA}, \\ T_{MIN} &\leq T_A \leq T_{MAX} \end{split}$			4.5	17.0	mV
		I <sub>KA</sub> = 10 mA	$\Delta V_{KA} = 10V - V_{REF}$		-1.0	-2.7	mV/V
ΔV <sub>REF</sub> / ΔV <sub>KA</sub>			$\Delta V_{KA} = 36 \text{ V} - 10 \text{ V}$		-0.5	-2.0	
I <sub>REF</sub>	Reference Input Current	$I_{KA} = 10 \text{ mA}, R_1 = 10 \text{ k}\Omega, R_2 = \infty$			1.5	4.0	μA
$\Delta I_{REF} / \Delta T$	Deviation of Reference Input Current Over Full Temperature Range	$I_{KA} = 10 \text{ mA}, R_1 = T_A = Full Range$	10 kΩ, R <sub>2</sub> = ∞,		0.4	1.2	μΑ
I <sub>KA(MIN)</sub>	Minimum Cathode Current for Regulation	V <sub>KA</sub> = V <sub>REF</sub>			0.45	1.00	mA
I <sub>KA(OFF)</sub>	Off-Stage Cathode Current	V <sub>KA</sub> = 36 V, V <sub>REF</sub> =	= 0		0.05	1.00	μΑ
Z <sub>KA</sub>	Dynamic Impedance	$V_{KA} = V_{REF},$ $I_{KA} = 1 \text{ to } 100 \text{ mA},$	f ≥ 1.0 kHz		0.15	0.50	Ω

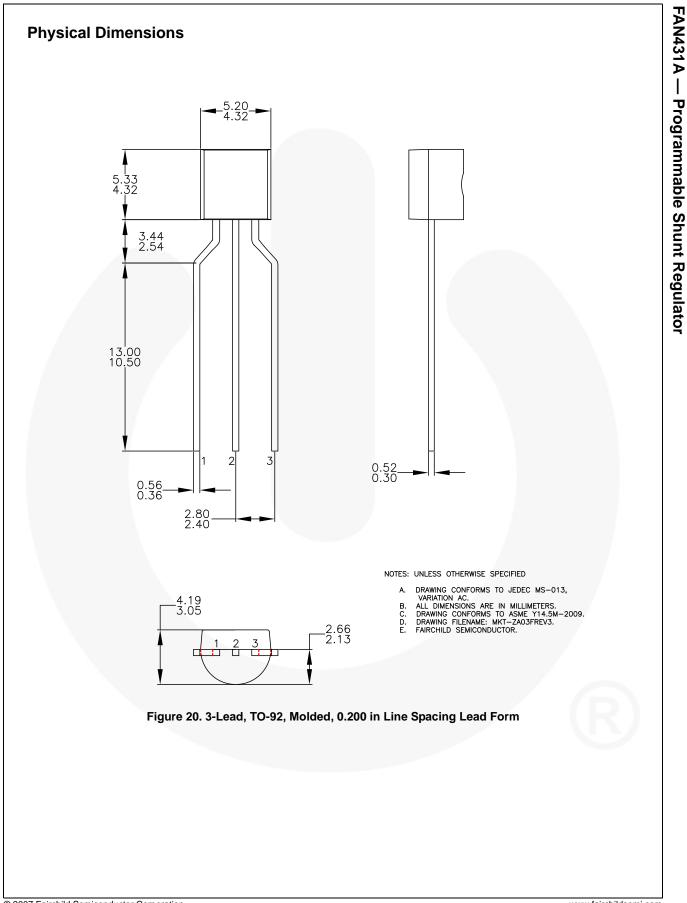
Note:

5.  $T_{MIN} = -25^{\circ}C$ ,  $T_{MAX} = +85^{\circ}C$ .





### Typical Performance Characteristics (Continued) 140 $\begin{array}{l} A \hspace{0.1cm} V_{_{KA}} = Vref \\ B \hspace{0.1cm} V_{_{KA}} = 5.0 \hspace{0.1cm} V \hspace{0.1cm} @ \\ T_{_{A}} = 25^{\circ}C \end{array}$ 120 I<sub>K</sub> - Cathode Current (mA) 2 Current (mA) 20 0 100p 0.0 10n 100r 0.2 0.4 0.8 1.0 1.2 1.8 1.4 1.6 2.0 0.6 C<sub>L</sub> - Load Capacitance Anode-Ref. Voltage (V) Figure 16. Stability Boundary Conditions Figure 17. Anode-Reference Diode Curve 5 2.51 2.50 2.49 2.49 2.49 2.49 2.49 2.48 3 Current (mA) 2 ُلْظُ<sup>2.47</sup> 2.46 └─ -50 0.0 0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 2.0 0 25 50 75 T<sub>A</sub>, Ambient Temperature (°C) 100 125 -25 Ref.-Cathode Voltage (V) Figure 18. Reference-Cathode Diode Curve Figure 19. Reference Input Voltage vs. **Ambient Temperature**



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