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January 2011

Single-Channel: 6N137, HCPL2601, HCPL2611
Dual-Channel: HCPL2630, HCPL2631
High Speed 10MBit/s Logic Gate Optocouplers

Features

■ Very high speed – 10 MBit/s
■ Superior CMR – 10 kV/μs
■ Double working voltage-480V
■ Fan-out of 8 over -40°C to +85°C
■ Logic gate output
■ Strobable output
■ Wired OR-open collector
■ U.L. recognized (File # E90700)

Applications

■ Ground loop elimination
■ LSTTL to TTL, LSTTL or 5-volt CMOS
■ Line receiver, data transmission
■ Data multiplexing
■ Switching power supplies
■ Pulse transformer replacement
■ Computer-peripheral interface

Description

The 6N137, HCPL2601, HCPL2611 single-channel and HCPL2630, HCPL2631 dual-channel optocouplers consist of a 850 nm AlGaAS LED, optically coupled to a very high speed integrated photo-detector logic gate with a strobable output. This output features an open collector, thereby permitting wired OR outputs. The coupled parameters are guaranteed over the temperature range of -40°C to +85°C. A maximum input signal of 5mA will provide a minimum output sink current of 13mA (fan out of 8).

An internal noise shield provides superior common mode rejection of typically 10kV/μs. The HCPL2601 and HCPL2631 has a minimum CMR of 5kV/μs. The HCPL2611 has a minimum CMR of 10kV/μs.

Schematics

6N137
HCPL2601
HCPL2611

HCPL2630
HCPL2631
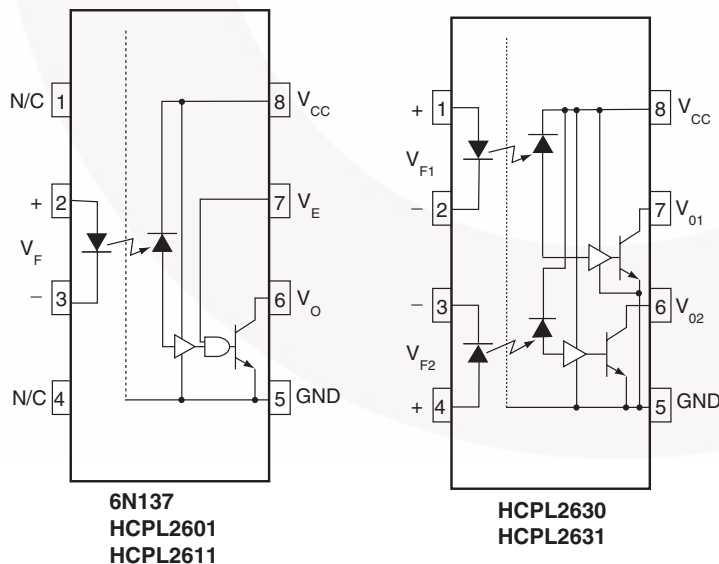
A 0.1μF bypass capacitor must be connected between pins 8 and 5[1].

Package Outlines
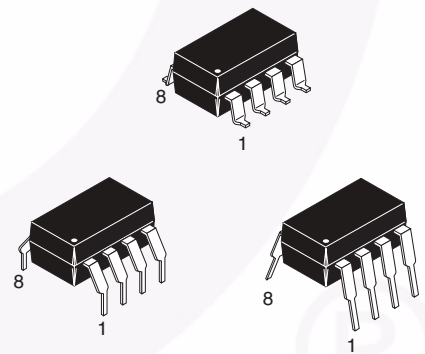
Truth Table (Positive Logic)

Input	Enable	Output
H	H	L
L	H	H
H	L	H
L	L	H
H	NC	L
L	NC	H

Absolute Maximum Ratings (T_A = 25°C unless otherwise specified)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Value	Units
T_{STG}	Storage Temperature		-55 to +125	°C
T_{OPR}	Operating Temperature		-40 to +85	°C
T_{SOL}	Lead Solder Temperature (for wave soldering only)*		260 for 10 sec	°C
EMITTER				
I_F	DC/Average Forward	Single Channel	50	mA
	Input Current	Dual Channel (Each Channel)	30	
V_E	Enable Input Voltage Not to Exceed V_{CC} by more than 500mV	Single Channel	5.5	V
V_R	Reverse Input Voltage	Each Channel	5.0	V
P_I	Power Dissipation	Single Channel	100	mW
		Dual Channel (Each Channel)	45	
DETECTOR				
V_{CC} (1 minute max)	Supply Voltage		7.0	V
I_O	Output Current	Single Channel	50	mA
		Dual Channel (Each Channel)	50	
V_O	Output Voltage	Each Channel	7.0	V
P_O	Collector Output	Single Channel	85	mW
	Power Dissipation	Dual Channel (Each Channel)	60	

*For peak soldering reflow, please refer to the Reflow Profile on page 11.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Units
I_{FL}	Input Current, Low Level	0	250	µA
I_{FH}	Input Current, High Level	*6.3	15	mA
V_{CC}	Supply Voltage, Output	4.5	5.5	V
V_{EL}	Enable Voltage, Low Level	0	0.8	V
V_{EH}	Enable Voltage, High Level	2.0	V_{CC}	V
T_A	Low Level Supply Current	-40	+85	°C
N	Fan Out (TTL load)		8	

*6.3mA is a guard banded value which allows for at least 20% CTR degradation. Initial input current threshold value is 5.0mA or less.

Electrical Characteristics (T_A = 0 to 70°C unless otherwise specified)

Individual Component Characteristics

Symbol	Parameter	Test Conditions		Min.	Typ.*	Max.	Unit
EMITTER							
V_F	Input Forward Voltage	I_F = 10mA				1.8	V
			T_A = 25°C		1.4	1.75	
B_{VR}	Input Reverse Breakdown Voltage	I_R = 10µA		5.0			V
C_{IN}	Input Capacitance	V_F = 0, f = 1MHz			60		pF
$\Delta V_F / \Delta T_A$	Input Diode Temperature Coefficient	I_F = 10mA			-1.4		mV/°C
DETECTOR							
I_{CCH}	High Level Supply Current	V_{CC} = 5.5V, I_F = 0mA, V_E = 0.5V	Single Channel		7	10	mA
			Dual Channel		10	15	
I_{CCL}	Low Level Supply Current	Single Channel	V_{CC} = 5.5V, I_F = 10mA		9	13	mA
		Dual Channel	V_E = 0.5V		14	21	
I_{EL}	Low Level Enable Current	V_{CC} = 5.5V, V_E = 0.5V			-0.8	-1.6	mA
I_{EH}	High Level Enable Current	V_{CC} = 5.5V, V_E = 2.0V			-0.6	-1.6	mA
V_{EH}	High Level Enable Voltage	V_{CC} = 5.5V, I_F = 10mA		2.0			V
V_{EL}	Low Level Enable Voltage	V_{CC} = 5.5V, I_F = 10mA[3]				0.8	V

Switching Characteristics (T_A = -40°C to +85°C, V_{CC} = 5V, I_F = 7.5mA unless otherwise specified)

Symbol	AC Characteristics	Test Conditions		Min.	Typ.*	Max.	Unit
T_{PLH}	Propagation Delay Time to Output HIGH Level	R_L = 350Ω, C_L = 15pF[4] (Fig. 12)	T_A = 25°C	20	45	75	ns
						100	
T_{PHL}	Propagation Delay Time to Output LOW Level	T_A = 25°C[5]		25	45	75	ns
		R_L = 350Ω, C_L = 15pF (Fig. 12)				100	
$|T_{PHL} - T_{PLH}|$	Pulse Width Distortion	(R_L = 350Ω, C_L = 15pF (Fig. 12)			3	35	ns
t_r	Output Rise Time (10–90%)	R_L = 350Ω, C_L = 15pF[6] (Fig. 12)			50		ns
t_f	Output Rise Time (90–10%)	R_L = 350Ω, C_L = 15pF[7] (Fig. 12)			12		ns
t_{ELH}	Enable Propagation Delay Time to Output HIGH Level	I_F = 7.5mA, V_{EH} = 3.5V, R_L = 350Ω, C_L = 15pF[8] (Fig. 13)			20		ns
t_{EHL}	Enable Propagation Delay Time to Output LOW Level	I_F = 7.5mA, V_{EH} = 3.5V, R_L = 350Ω, C_L = 15pF[9] (Fig. 13)			20		ns
$|CM_H|$	Common Mode Transient Immunity (at Output HIGH Level)	T_A = 25°C, $|V_{CM}|$ = 50V (Peak), I_F = 0mA, V_{OH} (Min.) = 2.0V, R_L = 350Ω[10] (Fig. 14)	6N137, HCPL2630		10,000		V/µs
			HCPL2601, HCPL2631	5000	10,000		
		$|V_{CM}|$ = 400V	HCPL2611	10,000	15,000		V/µs
$|CM_L|$	Common Mode Transient Immunity (at Output LOW Level)	R_L = 350Ω, I_F = 7.5mA, V_{OL} (Max.) = 0.8V, T_A = 25°C[11] (Fig. 14)	6N137, HCPL2630		10,000		V/µs
			HCPL2601, HCPL2631	5000	10,000		
		$|V_{CM}|$ = 400V	HCPL2611	10,000	15,000		

Electrical Characteristics (Continued)

Transfer Characteristics (T_A = -40 to +85°C unless otherwise specified)

Symbol	DC Characteristics	Test Conditions	Min.	Typ.*	Max.	Unit
I_{OH}	HIGH Level Output Current	V_{CC} = 5.5V, V_O = 5.5V, I_F = 250µA, V_E = 2.0V[2]			100	µA
V_{OL}	LOW Level Output Current	V_{CC} = 5.5V, I_F = 5mA, V_E = 2.0V, I_{CL} = 13mA[2]		.35	0.6	V
I_{FT}	Input Threshold Current	V_{CC} = 5.5V, V_O = 0.6V, V_E = 2.0V, I_{OL} = 13mA		3	5	mA

Isolation Characteristics (T_A = -40°C to +85°C unless otherwise specified.)

Symbol	Characteristics	Test Conditions	Min.	Typ.*	Max.	Unit
I_{I-O}	Input-Output Insulation Leakage Current	Relative humidity = 45%, T_A = 25°C, t = 5s, V_{I-O} = 3000 VDC[12]			1.0*	µA
V_{ISO}	Withstand Insulation Test Voltage	RH < 50%, T_A = 25°C, $I_{I-O} \leq$ 2µA, t = 1 min.[12]	2500			V_{RMS}
R_{I-O}	Resistance (Input to Output)	V_{I-O} = 500V[12]		10^{12}		Ω
C_{I-O}	Capacitance (Input to Output)	f = 1MHz[12]		0.6		pF

*All Typicals at V_{CC} = 5V, T_A = 25°C

Notes:

1. The V_{CC} supply to each optoisolator must be bypassed by a 0.1µF capacitor or larger. This can be either a ceramic or solid tantalum capacitor with good high frequency characteristic and should be connected as close as possible to the package V_{CC} and GND pins of each device.

2. Each channel.

3. Enable Input – No pull up resistor required as the device has an internal pull up resistor.

4. t_{PLH} – Propagation delay is measured from the 3.75mA level on the HIGH to LOW transition of the input current pulse to the 1.5 V level on the LOW to HIGH transition of the output voltage pulse.

5. t_{PHL} – Propagation delay is measured from the 3.75mA level on the LOW to HIGH transition of the input current pulse to the 1.5 V level on the HIGH to LOW transition of the output voltage pulse.

6. t_r – Rise time is measured from the 90% to the 10% levels on the LOW to HIGH transition of the output pulse.

7. t_f – Fall time is measured from the 10% to the 90% levels on the HIGH to LOW transition of the output pulse.

8. t_{ELH} – Enable input propagation delay is measured from the 1.5V level on the HIGH to LOW transition of the input voltage pulse to the 1.5V level on the LOW to HIGH transition of the output voltage pulse.

9. t_{EHL} – Enable input propagation delay is measured from the 1.5V level on the LOW to HIGH transition of the input voltage pulse to the 1.5V level on the HIGH to LOW transition of the output voltage pulse.

10. CM_H – The maximum tolerable rate of rise of the common mode voltage to ensure the output will remain in the HIGH state (i.e., V_{OUT} > 2.0V). Measured in volts per microsecond (V/µs).

11. CM_L – The maximum tolerable rate of rise of the common mode voltage to ensure the output will remain in the LOW output state (i.e., V_{OUT} < 0.8V). Measured in volts per microsecond (V/µs).

12. Device considered a two-terminal device: Pins 1, 2, 3 and 4 shorted together, and Pins 5, 6, 7 and 8 shorted together.

Typical Performance Curves

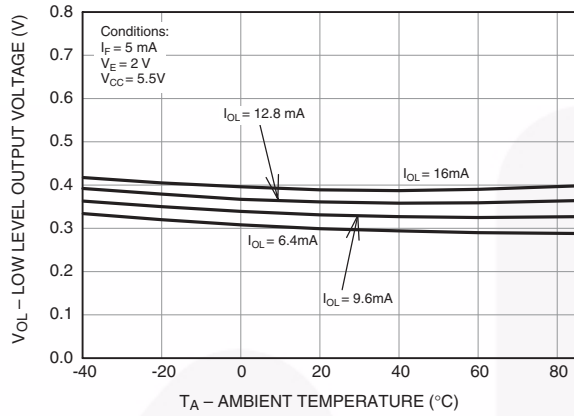**Fig.1 Low Level Output Voltage vs. Ambient Temperature**

Conditions:
I_F = 5 mA
V_E = 2 V
V_{CC} = 5.5V

I_{OL} = 12.8 mA

I_{OL} = 16mA

I_{OL} = 6.4mA

I_{OL} = 9.6mA

V_{OL} – LOW LEVEL OUTPUT VOLTAGE (V)

T_A – AMBIENT TEMPERATURE (°C)

Fig. 2 Input Diode Forward Voltage vs. Forward Current

I_F – FORWARD CURRENT (mA)

V_F – FORWARD VOLTAGE (V)
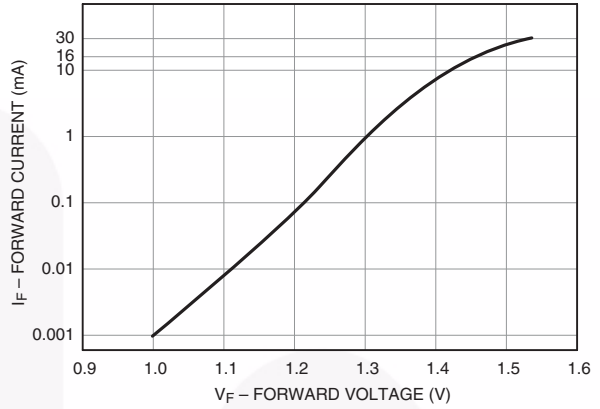
Fig.3 Switching Time vs. Forward Current

V_{CC} = 5 V

R_L = 4 kΩ (T_{PLH})

R_L = 1 kΩ (T_{PLH})

R_L = 1 kΩ
R_L = 4 kΩ (T_{PHL})
R_L = 350 kΩ

R_L = 350 Ω (T_{PLH})

T_P – PROPAGATION DELAY (ns)

I_F – FORWARD CURRENT (mA)
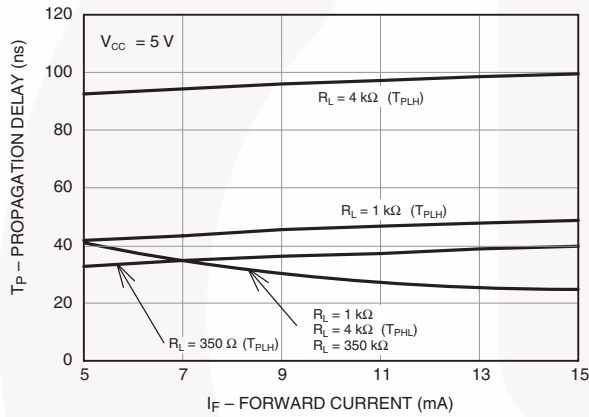
Fig. 4 Low Level Output Current vs. Ambient Temperature

I_F = 15mA

I_F = 10mA

I_F = 5mA

Conditions:
V_{CC} = 5V
V_E = 2V
V_{OL} = 0.6V

I_{OL} – LOW LEVEL OUTPUT CURRENT (mA)

T_A – AMBIENT TEMPERATURE (°C)
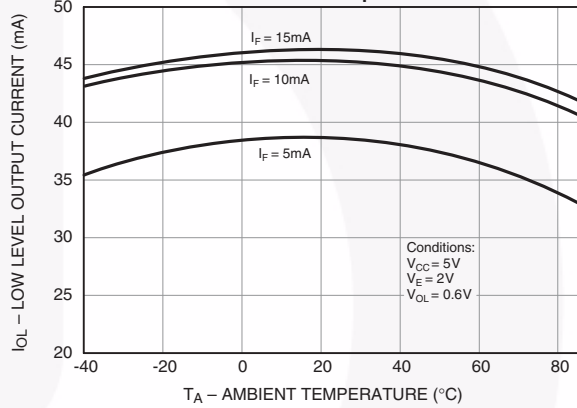
Fig. 5 Input Threshold Current vs. Ambient Temperature

Conditions:
V_{CC} = 5.0 V
V_O = 0.6 V

R_L = 350Ω

R_L = 1kΩ

R_L = 4kΩ

I_{FT} – INPUT THRESHOLD CURRENT (mA)

T_A – AMBIENT TEMPERATURE (°C)
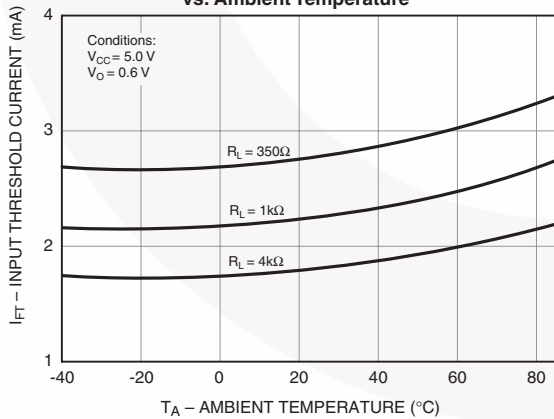
Fig. 6 Output Voltage vs. Input Forward Current

R_L = 350Ω

R_L = 4kΩ

R_L = 1kΩ

V_O – OUTPUT VOLTAGE (V)

I_F - FORWARD CURRENT (mA)
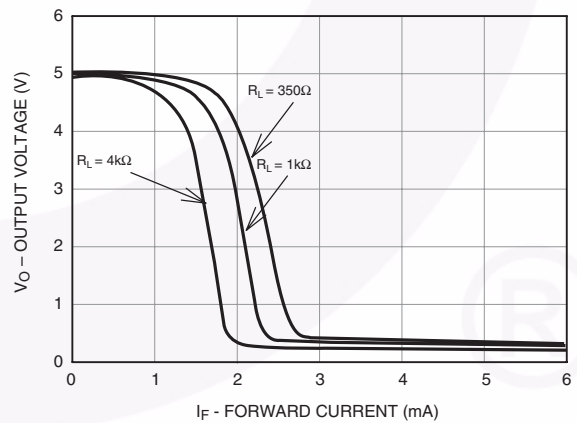
Typical Performance Curves (Continued)

Fig. 7 Pulse Width Distortion vs. Temperature

PWD – PULSE WIDTH DISTORTION (ns)

R_L = 4 kΩ

Conditions:
I_F = 7.5mA
V_{CC} = 5A

R_L = 1 kΩ

R_L = 350Ω
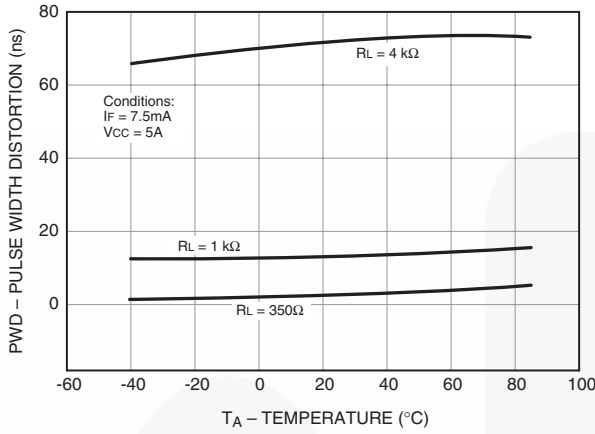
T_A – TEMPERATURE (°C)

Fig. 8 Rise and Fall Time vs. Temperature

T_r/T_f – RISE AND FALL TIME (ns)

R_L = 4 kΩ (tr)

Conditions:
I_F = 7.5 mA
V_{CC} = 5 V

R_L = 1 kΩ (tr)

R_L = 350Ω (tr)

R_L = 1 kΩ
R_L = 4 kΩ (tf)
R_L = 350Ω
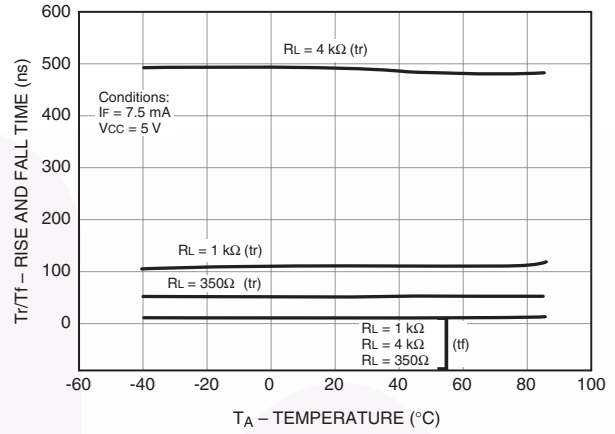
T_A – TEMPERATURE (°C)

Fig. 9 Enable Propagation Delay vs. Temperature

T_E – ENABLE PROPAGATION DELAY (ns)

R_L = 4 kΩ (TELH)

R_L = 1 kΩ (TELH)

R_L = 350Ω (TELH)

R_L = 350Ω
R_L = 1 kΩ (TEHL)
R_L = 4 kΩ
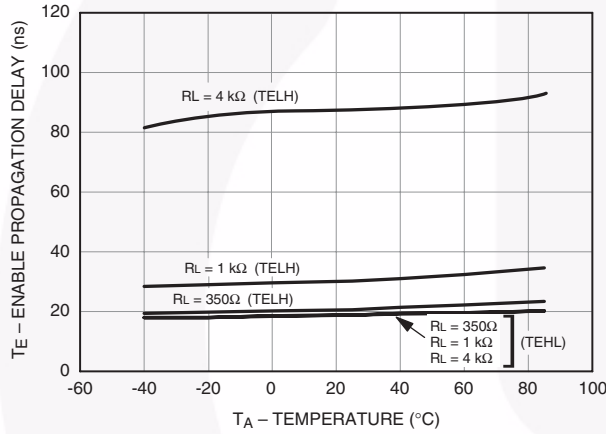
T_A – TEMPERATURE (°C)

Fig. 10 Switching Time vs. Temperature

T_P – PROPAGATION DELAY (ns)

R_L = 4 kΩ TPLH

R_L = 1 kΩ TPLH

R_L = 350Ω TPLH

R_L = 1 kΩ
R_L = 4 kΩ TPHL
R_L = 350Ω
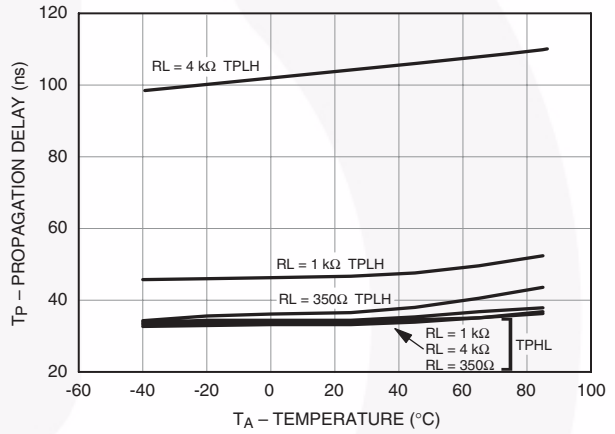
T_A – TEMPERATURE (°C)

Fig. 11 High Level Output Current vs. Temperature

I_{OH} – HIGH LEVEL OUTPUT CURRENT (µA)

Conditions:
V_{CC} = 5.5 V
V_O = 5.5 V
V_E = 2.0 V
I_F = 250 µA

T_A – TEMPERATURE (°C)
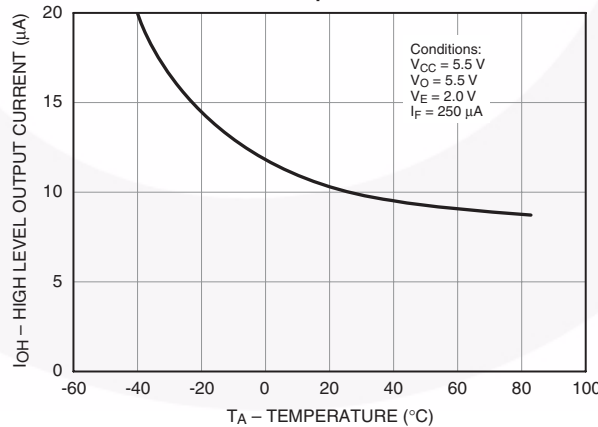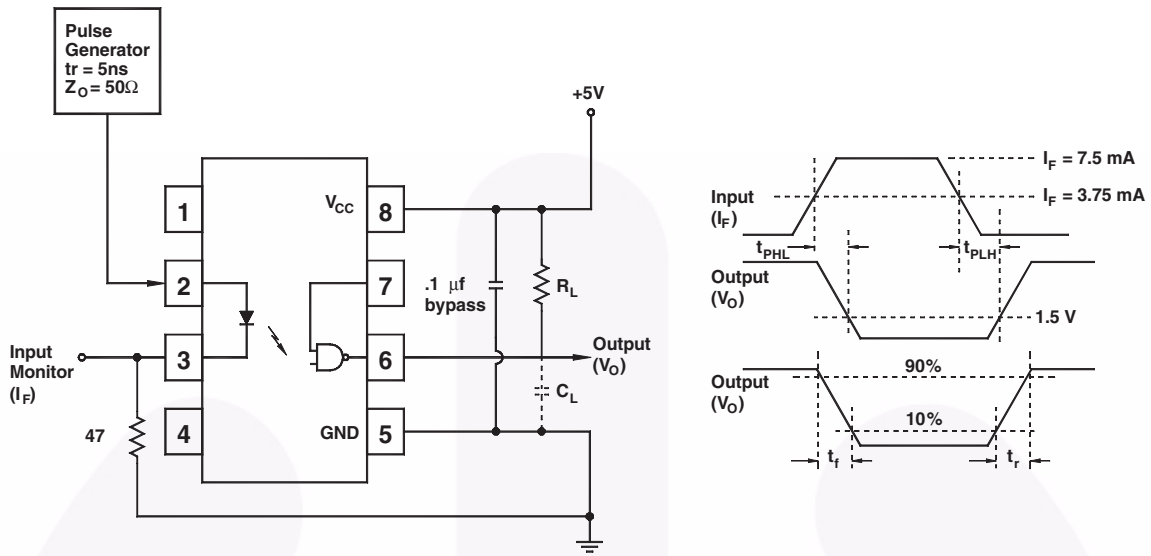
Test Circuits

Fig. 12 Test Circuit and Waveforms for t_{PLH}, t_{PHL}, t_r and t_f
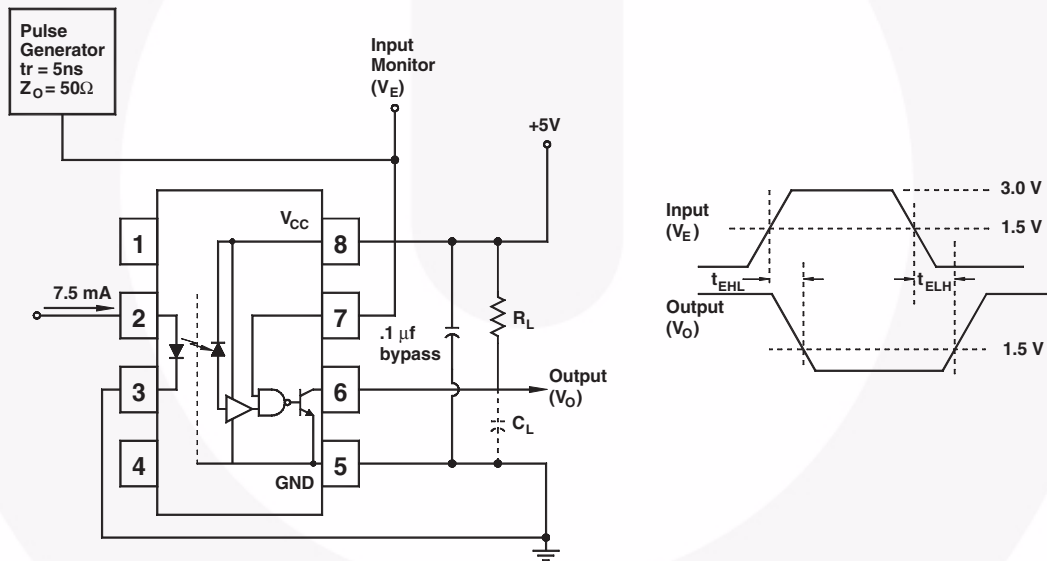

Fig. 13 Test Circuit t_{EHL} and t_{ELH}
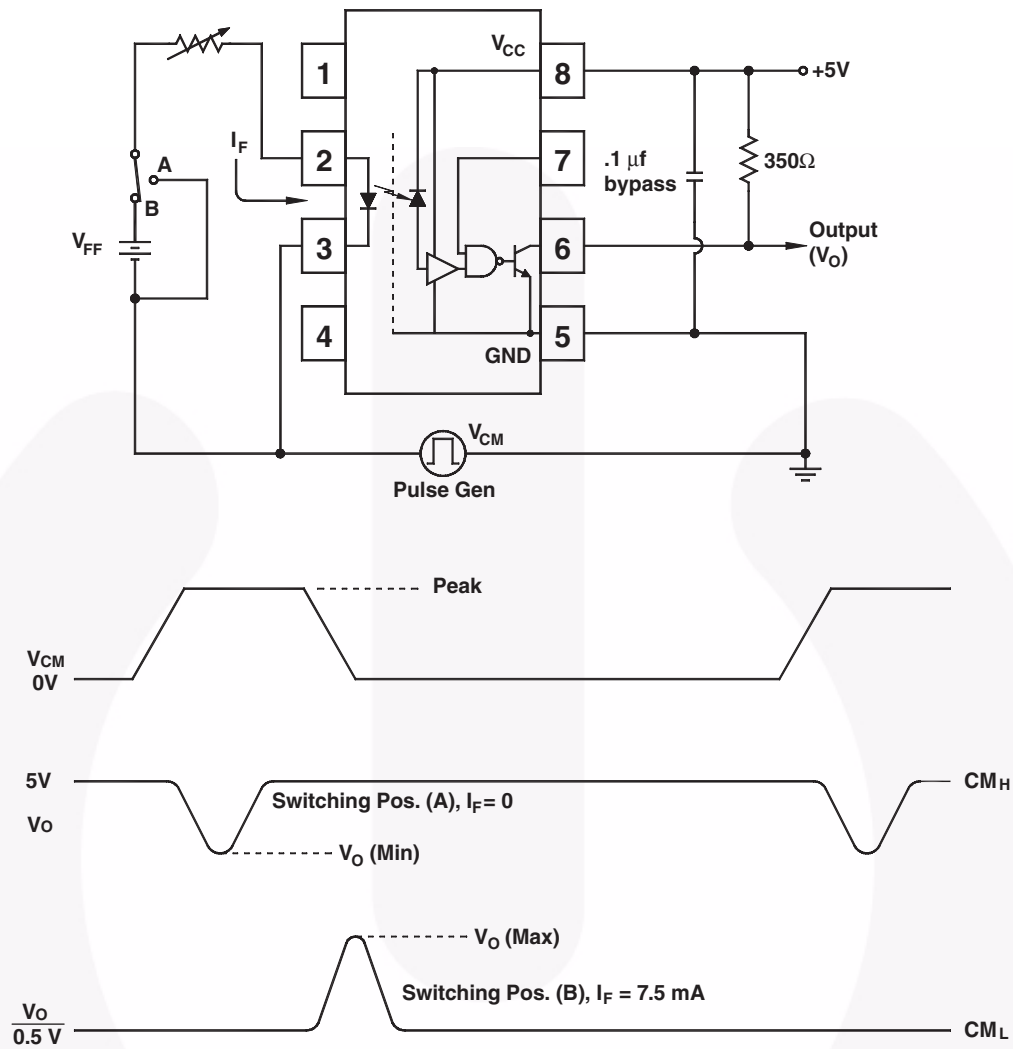
Test Circuits (Continued)

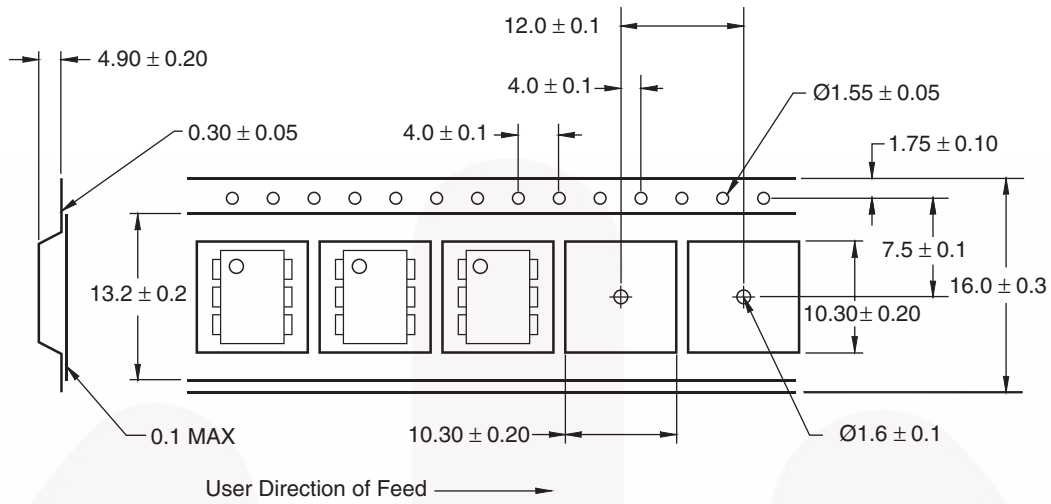**Fig. 14 Test Circuit Common Mode Transient Immunity**

Ordering Information

Option	Example Part Number	Description
S	6N137S	Surface Mount Lead Bend
SD	6N137SD	Surface Mount; Tape and Reel
W	6N137W	0.4" Lead Spacing
V	6N137V	VDE0884
WV	6N137WV	VDE0884; 0.4" Lead Spacing
SV	6N137SV	VDE0884; Surface Mount
SDV	6N137SDV	VDE0884; Surface Mount; Tape and Reel

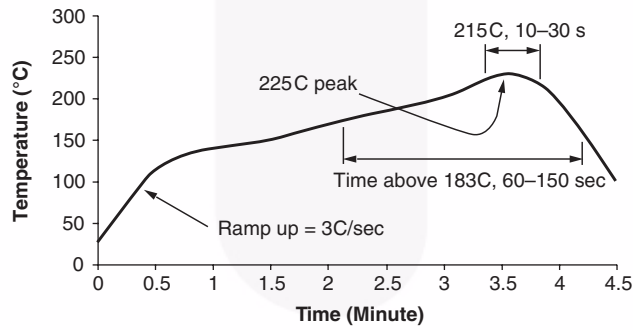## Marking Information
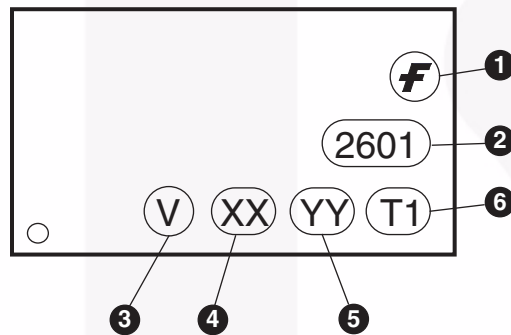
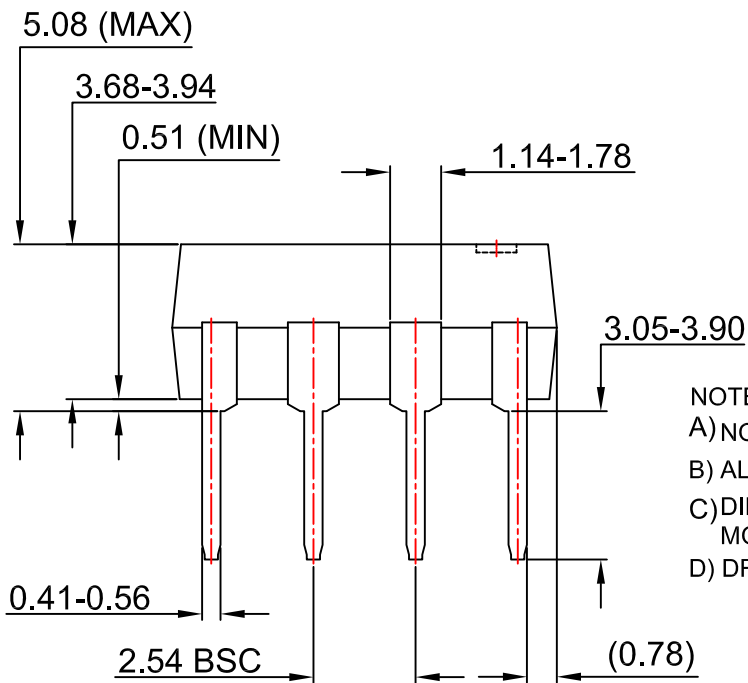| Definitions	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)
4	Two digit year code, e.g., '03'
5	Two digit work week ranging from '01' to '53'
6	Assembly package code

Tape Specifications

4.90 ± 0.20

0.30 ± 0.05

4.0 ± 0.1

12.0 ± 0.1

4.0 ± 0.1

∅1.55 ± 0.05

1.75 ± 0.10

7.5 ± 0.1

16.0 ± 0.3

13.2 ± 0.2

10.30 ± 0.20

0.1 MAX

10.30 ± 0.20

∅1.6 ± 0.1

User Direction of Feed ⟶

Reflow Profile

215C, 10–30 s

225C peak

Time above 183C, 60–150 sec

Ramp up = 3C/sec

Temperature (°C)

Time (Minute)

- Peak reflow temperature: 225C (package surface temperature)
- Time of temperature higher than 183C for 60–150 seconds
- One time soldering reflow is recommended
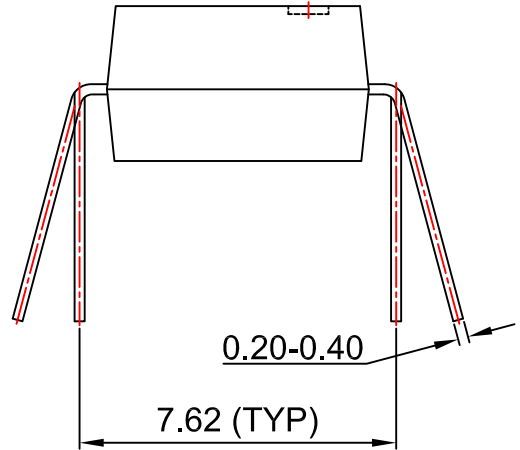
9.40-9.91

4　　　　　　　1　　　PIN 1

6.35-6.86

5　　　　　　　8

0.20-0.40

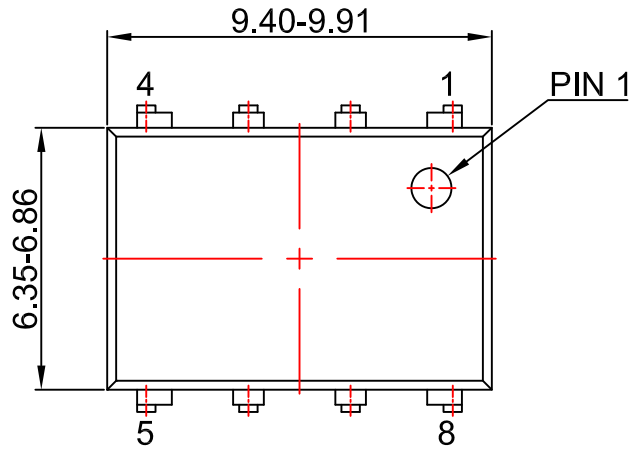7.62 (TYP)

5.08 (MAX)

3.68-3.94

0.51 (MIN)

1.14-1.78

3.05-3.90

0.41-0.56

2.54 BSC

(0.78)

NOTES:
A) NO STANDARD APPLIES TO THIS PACKAGE
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS ARE EXCLUSIVE OF BURRS,
   MOLD FLASH, AND TIE BAR EXTRUSION
D) DRAWING FILENAME AND REVSION: MKT-N08GREV7

FAIRCHILD™

9.40-9.91

4   1

PIN 1

6.35-6.86

5   8

(1.78)   (2.54)   (1.52)

(10.54)

(7.49)

(0.76)
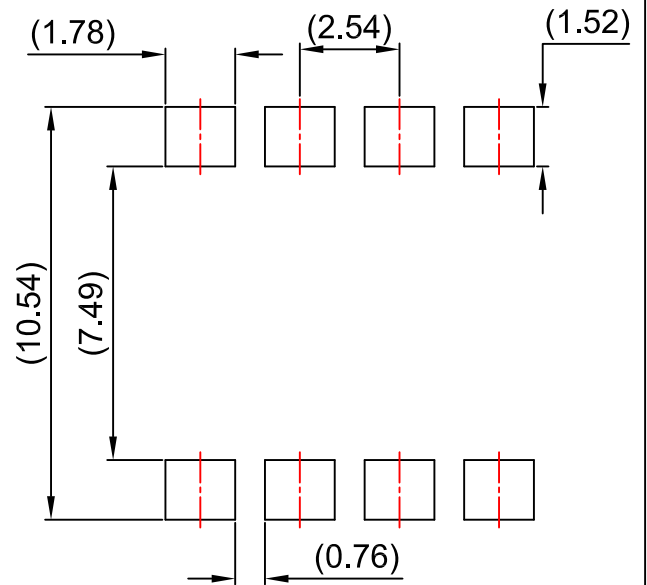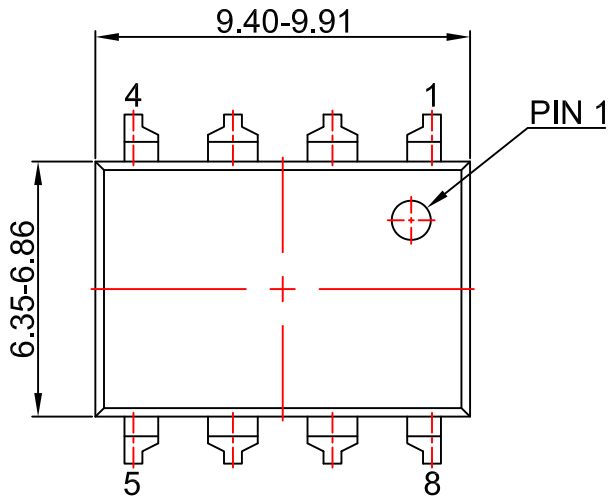
LAND PATTERN RECOMMENDATION

5.08 (MAX)

3.68-3.94

0.51 (MIN)

1.14-1.78

(0.78)

2.54BSC   0.41-0.56

0.20-0.40

0.40 (MIN)
B0TH SIDE

8.00 (MIN)

10.30 (MAX)

NOTES:

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   MOLD FLASH, AND TIE BAR EXTRUSION

D) DRAWING FILENAME AND REVSION: MKT-N08Hrev7.

FAIRCHILD™

9.40-9.91

4   1

PIN 1

6.35-6.86

5   8

15.0° (MAX)

10.16 (TYP)

0.20-0.40

5.08 (MAX)

3.68-3.94

0.51 (MIN)

1.14-1.78

3.05-3.90

0.41-0.56

2.54 BSC

(0.78)
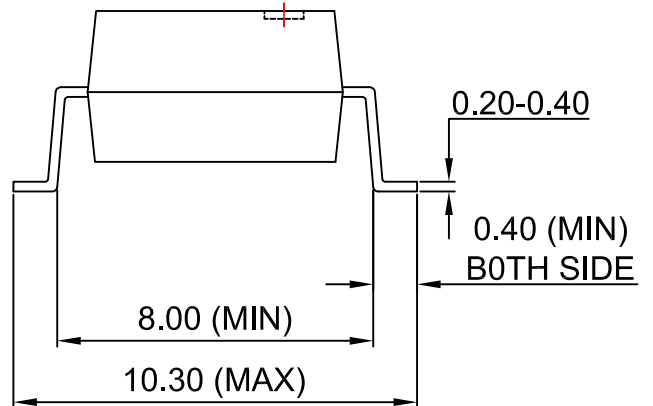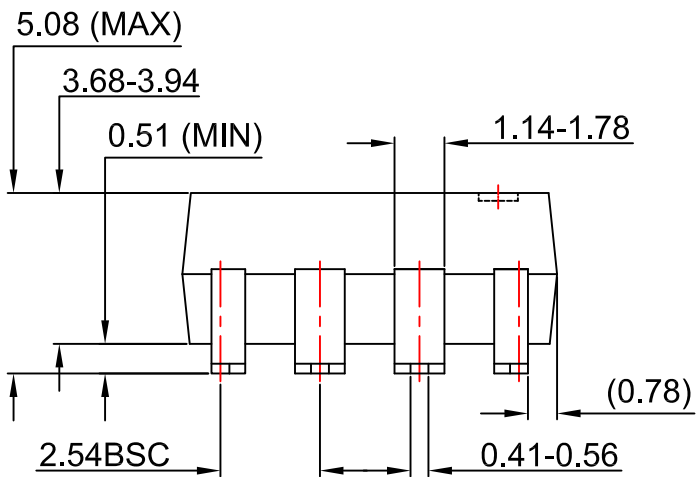
NOTES:

A) NO STANDARD APPLIES TO THIS PACKAGE

B) ALL DIMENSIONS ARE IN MILLIMETERS.

C) DIMENSIONS ARE EXCLUSIVE OF BURRS,
   MOLD FLASH, AND TIE BAR EXTRUSION
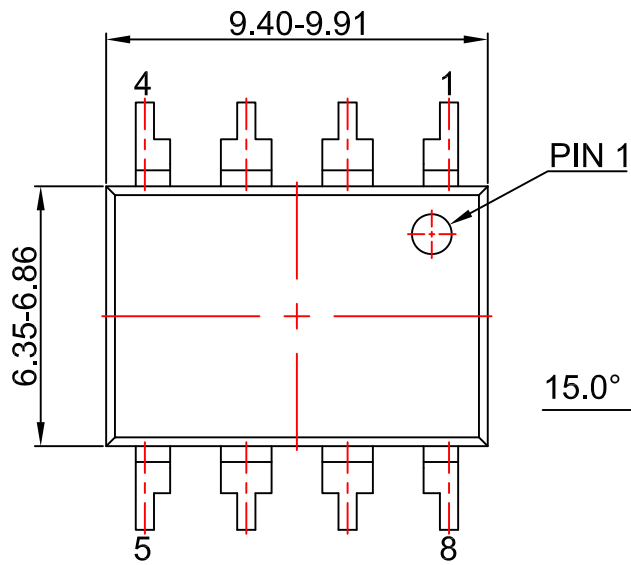
D) DRAWING FILENAME AND REVSION: MKT-N08REV7

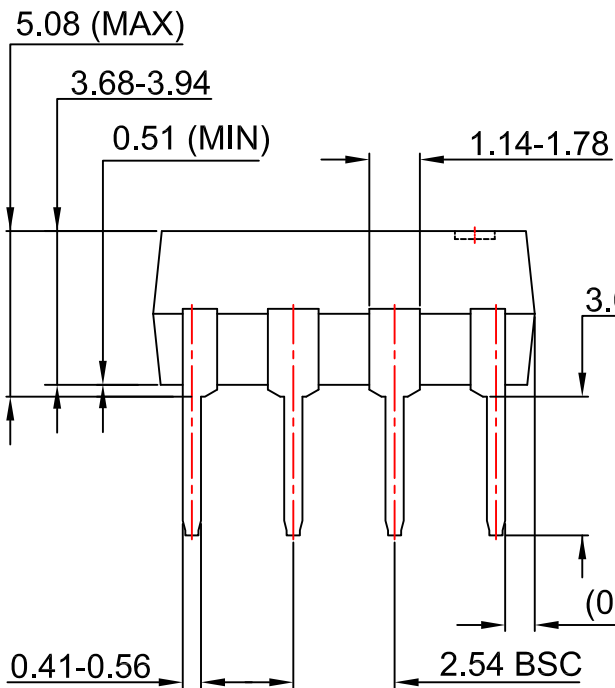FAIRCHILD™

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Definition of Terms

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