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Data Sheet November 2013

8 A, 1200 V, Hyperfast Diode

The RHRP8120 is a hyperfast diode with soft recovery characteristics. It has the half recovery time of ultrafast diodes and is silicon nitride passivated ionimplanted epitaxial planar construction. These devices are intended to be used as freewheeling/ clamping diodes and diodes in a variety of switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

Ordering Information

PART NUMBER	PACKAGE	BRAND	
RHRP8120	TO-220AC-2L	RHRP8120	

NOTE: When ordering, use the entire part number.

Symbol



Absolute Maximum Ratings T_C = 25°C. Unless Otherwise Specified

Features

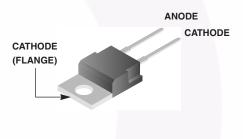
- Hyperfast Recovery t_{rr} = 70 ns (@ I_F= 8 A)
- Max Forward Voltage, V_F = 3.2 V (@ T_C = 25°C)
- 1200 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS Compliant

Applications

- · Switching Power Supplies
- Power Switching Circuits
- · General Purpose

Packaging

JEDEC TO-220AC



-65 to 175

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	RHRP8120	UNIT
Peak Repetitive Reverse Voltage	1200	V
Working Peak Reverse Voltage	1200	V
DC Blocking VoltageV _R	1200	V
Average Rectified Forward Current $I_{F(AV)}$ ($T_C = 140^{\circ}C$)	8	Α
Repetitive Peak Surge Current	16	Α
Nonrepetitive Peak Surge Current	100	Α
Maximum Power Dissipation	75	W
Avalanche Energy (See Figures 10 and 11)	20	mJ

Electrical Specifications T_C = 25°C, Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
V _F	I _F = 8 A	-	-	3.2	V
	$I_F = 8 \text{ A}, T_C = 150^{\circ}\text{C}$	-	-	2.6	V
I _R	V _R = 1200 V	-	-	100	μΑ
	V _R = 1200 V, T _C = 150 ^o C	-	-	500	μΑ
t _{rr}	I _F = 1 A, dI _F /dt = 200 A/μs	-	-	55	ns
	$I_F = 8 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	-	70	ns
t _a	I _F = 8 A, dI _F /dt = 200 A/μs	-	30	-	ns
t _b	I _F = 8 A, dI _F /dt = 200 A/μs	-	20	-	ns
Q _{rr}	I _F = 8 A, dI _F /dt = 200 A/μs	-	165	-	nC
CJ	V _R = 10 V, I _F = 0 A	-	25	-	pF
$R_{ heta JC}$		-	-	2	°C/W

DEFINITIONS

 V_F = Instantaneous forward voltage (pw = 300 μ s, D = 2%).

I_R = Instantaneous reverse current.

 T_{rr} = Reverse recovery time (See Figure 9), summation of t_a + t_b .

 t_a = Time to reach peak reverse current (See Figure 9).

 t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 9).

Q_{rr} = Reverse Recovery Charge.

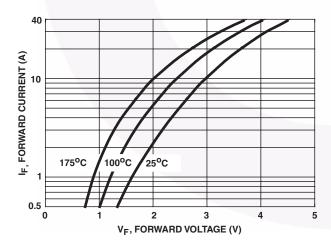
C_J = Junction Capacitance.

 $R_{\theta JC}$ = Thermal resistance junction to case.

pw = Pulse Width.

D = Duty Cycle.

Typical Performance Curves





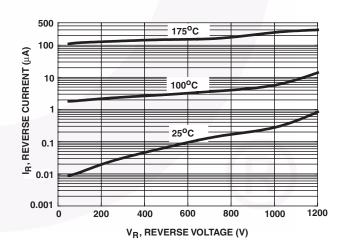


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

Typical Performance Curves (Continued)

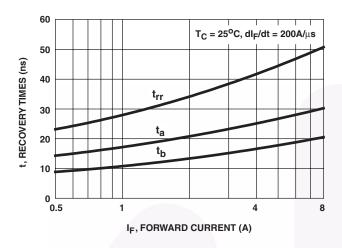


FIGURE 3. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

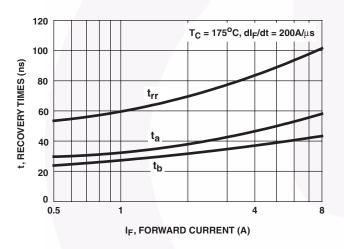


FIGURE 5. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

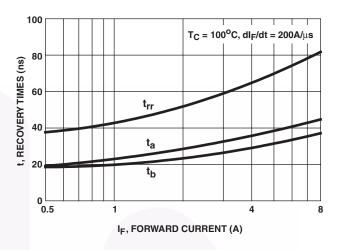


FIGURE 4. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

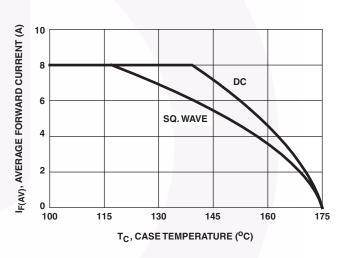


FIGURE 6. CURRENT DERATING CURVE

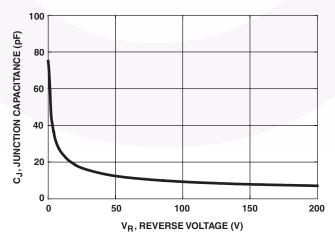


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

Test Circuits and Waveforms

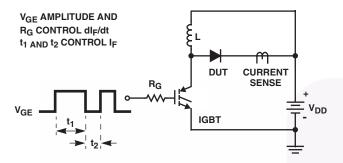


FIGURE 8. t_{rr} TEST CIRCUIT

 $I_{MAX} = 1A$ L = 40mH $R < 0.1\Omega$ $E_{AVL} = 1/2Li^2 \left[V_{R(AVL)} / (V_{R(AVL)} - V_{DD}) \right]$ $Q_1 = IGBT \left(BV_{CES} > DUT V_{R(AVL)} \right)$ L R CURRENT + O $SENSE V_{DD}$ V_{DD} O_1 O_1 O_2 O_3 O_4 O_5 O_4 O_4 O_4 O_4 O_5 O_4 O_5 O_6 O_7 O_8 O_8 O_9 O_9

FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

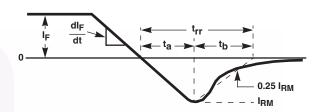


FIGURE 9. t_{rr} WAVEFORMS AND DEFINITIONS

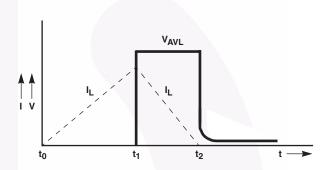
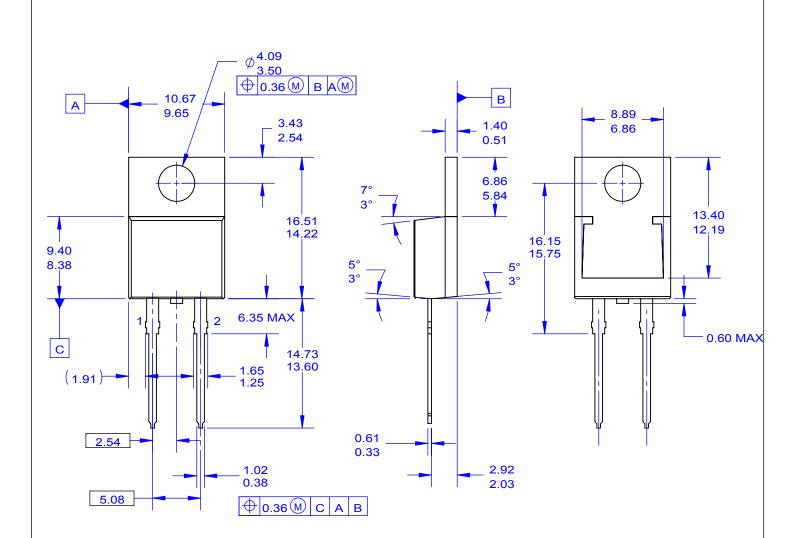
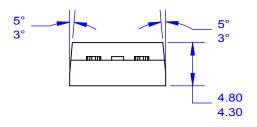


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS







NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220,ISSUE K, VARIATION AC,DATED APRIL 2002.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DRAWING FILE NAME: TO220A02REV5





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Definition of Terms

Deminition of Terms		
Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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