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# PN2907 / MMBT2907

## PNP General-Purpose Transistor

### Description

This device is designed for use with general-purpose amplifiers and switches requiring collector currents to 500 mA. Sourced from process 63.



### Ordering Information

| Part Number   | Top Mark | Package   | Packing Method |
|---------------|----------|-----------|----------------|
| PN2907BU      | PN2907   | TO-92 3L  | Bulk           |
| MMBT2907_D87Z | 2B       | SOT-23 3L | Tape and Reel  |

## Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

| Symbol         | Parameter                                        | Value       | Unit             |
|----------------|--------------------------------------------------|-------------|------------------|
| $V_{CEO}$      | Collector-Emitter Voltage                        | -40         | V                |
| $V_{CBO}$      | Collector-Base Voltage                           | -60         | V                |
| $V_{EBO}$      | Emitter-Base Voltage                             | -5.0        | V                |
| $I_C$          | Collector Current - Continuous                   | -800        | mA               |
| $T_J, T_{STG}$ | Operating and Storage Junction Temperature Range | -55 to +150 | $^\circ\text{C}$ |

### Notes:

- These ratings are based on a maximum junction temperature of  $150^\circ\text{C}$ .
- These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

## Thermal Characteristics

Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

| Symbol          | Parameter                               | Max.                  |                         | Unit                      |
|-----------------|-----------------------------------------|-----------------------|-------------------------|---------------------------|
|                 |                                         | PN2907 <sup>(3)</sup> | MMBT2907 <sup>(4)</sup> |                           |
| $P_D$           | Total Device Dissipation                | 625                   | 350                     | mW                        |
|                 | Derate Above $25^\circ\text{C}$         | 5.0                   | 2.8                     | mW/ $^\circ\text{C}$      |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case    | 83.3                  |                         | $^\circ\text{C}/\text{W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | 200                   | 357                     | $^\circ\text{C}/\text{W}$ |

### Notes:

- PCB size: FR-4 76 x 114 x 1.57 mm<sup>3</sup> (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.
- Device mounted on FR-4 PCB 1.6 inch x 1.6 inch x 0.06 inch.

## Electrical Characteristics

Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

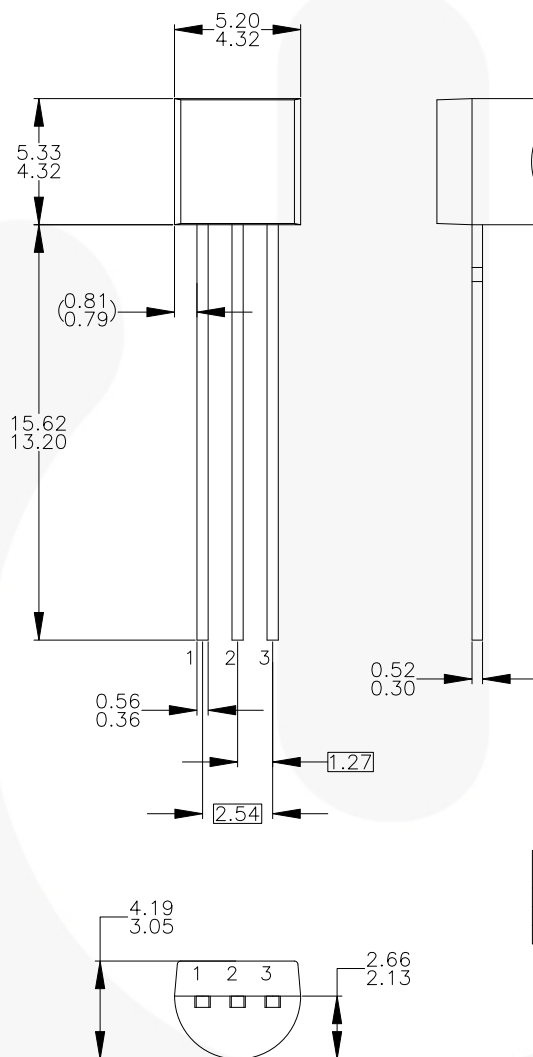
| Symbol                            | Parameter                                          | Conditions                                                                                    | Min. | Max. | Unit |
|-----------------------------------|----------------------------------------------------|-----------------------------------------------------------------------------------------------|------|------|------|
| Off Characteristics               |                                                    |                                                                                               |      |      |      |
| V <sub>(BR)CEO</sub>              | Collector-Emitter Breakdown Voltage <sup>(5)</sup> | I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0                                                   | -40  |      | V    |
| V <sub>(BR)CBO</sub>              | Collector-Base Breakdown Voltage                   | I <sub>C</sub> = -10 μA, I <sub>E</sub> = 0                                                   | -60  |      | V    |
| V <sub>(BR)EBO</sub>              | Emitter-Base Breakdown Voltage                     | I <sub>E</sub> = -10 μA, I <sub>C</sub> = 0                                                   | -5.0 |      | V    |
| I <sub>CEX</sub>                  | Collector Cut-Off Current                          | V <sub>CE</sub> = -30 V, V <sub>EB</sub> = -0.5 V                                             |      | -50  | nA   |
| I <sub>BL</sub>                   | Base Cut-Off Current                               | V <sub>CE</sub> = -30 V, V <sub>EB</sub> = -0.5 V                                             |      | -50  | nA   |
| I <sub>CBO</sub>                  | Collector Cut-Off Current                          | V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0                                                   |      | -20  | nA   |
|                                   |                                                    | V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C                           |      | -20  | μA   |
| On Characteristics <sup>(5)</sup> |                                                    |                                                                                               |      |      |      |
| h <sub>FE</sub>                   | DC Current Gain                                    | V <sub>CE</sub> = -10 V, I <sub>C</sub> = -0.1 mA                                             | 35   |      |      |
|                                   |                                                    | V <sub>CE</sub> = -10 V, I <sub>C</sub> = -1.0 mA                                             | 50   |      |      |
|                                   |                                                    | V <sub>CE</sub> = -10 V, I <sub>C</sub> = -10 mA                                              | 70   |      |      |
|                                   |                                                    | V <sub>CE</sub> = -10 V, I <sub>C</sub> = -150 mA                                             | 100  | 300  |      |
|                                   |                                                    | V <sub>CE</sub> = -10 V, I <sub>C</sub> = -500 mA                                             | 30   |      |      |
| V <sub>CE(sat)</sub>              | Collector-Emitter Saturation Voltage               | I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA                                             |      | -0.4 | V    |
|                                   |                                                    | I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA                                             |      | -1.6 |      |
| V <sub>BE(sat)</sub>              | Base-Emitter Saturation Voltage                    | I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA                                             |      | -1.3 | V    |
|                                   |                                                    | I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA                                             |      | -2.6 |      |
| Small Signal Characteristics      |                                                    |                                                                                               |      |      |      |
| C <sub>ob</sub>                   | Output Capacitance                                 | V <sub>CB</sub> = -10 V, f = 1.0 MHz                                                          |      | 8    | pF   |
| C <sub>ib</sub>                   | Input Capacitance                                  | V <sub>EB</sub> = -2.0 V, f = 1.0 MHz                                                         |      | 30   | pF   |
| h <sub>fe</sub>                   | Small-Signal Current Gain                          | I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -20 V, f = 100 MHz                                 | 2    |      |      |
| Switching Characteristics         |                                                    |                                                                                               |      |      |      |
| t <sub>on</sub>                   | Turn-On Time                                       | V <sub>CC</sub> = -30 V, I <sub>C</sub> = -150 mA, I <sub>B1</sub> = -15 mA                   |      | 45   | ns   |
| t <sub>d</sub>                    | Delay Time                                         |                                                                                               |      | 10   | ns   |
| t <sub>r</sub>                    | Rise Time                                          |                                                                                               |      | 40   | ns   |
| t <sub>off</sub>                  | Turn-Off Time                                      | V <sub>CC</sub> = -6.0 V, I <sub>C</sub> = -150 mA, I <sub>B1</sub> = I <sub>B2</sub> = -15mA |      | 100  | ns   |
| t <sub>s</sub>                    | Storage Time                                       |                                                                                               |      | 80   | ns   |
| t <sub>f</sub>                    | Fall Time                                          |                                                                                               |      | 30   | ns   |

### Note:

5. Pulse test: pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2.0\%$ .

## Physical Dimensions

## TO-92 (Bulk)



NOTES: UNLESS OTHERWISE SPECIFIED

- A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.  
 B) ALL DIMENSIONS ARE IN MILLIMETERS.  
 C) DRAWING CONFORMS TO ASME Y14.5M-1994.  
 D) TO-92 (92,94,96,97,98) PIN CONFIGURATION:

| PIN | 92 |   |   | 94 |   |   | 96 |   |   | 97 |   |   | 98 |   |   |
|-----|----|---|---|----|---|---|----|---|---|----|---|---|----|---|---|
|     | P  | F | M | P  | F | M | P  | F | M | P  | F | M | P  | F | M |
| 1   | E  | S | S | E  | S | S | B  | D | G | C  | G | D | C  | G | D |
| 2   | B  | D | G | C  | G | D | E  | S | S | B  | D | G | E  | S | S |
| 3   | C  | G | D | B  | D | G | C  | G | D | E  | S | S | B  | D | G |

## LEGEND:

P — BIPOLAR      E — EMITTER      D — DRAIN  
 F — JFET          B — BASE          S — SOURCE  
 M — DMOS          C — COLLECTOR      G — GATE

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:  
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"  
 ARE INTERCHANGEABLE AT JFET "F" OPTION.  
 F) DRAWING FILENAME: MKT-ZA03DREV3.

**Figure 1. 3-LEAD, TO92, JEDEC TO-92 COMPLIANT STRAIGHT LEAD CONFIGURATION (OLD TO92AM3)**

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## Physical Dimensions (Continued)

## SOT-23

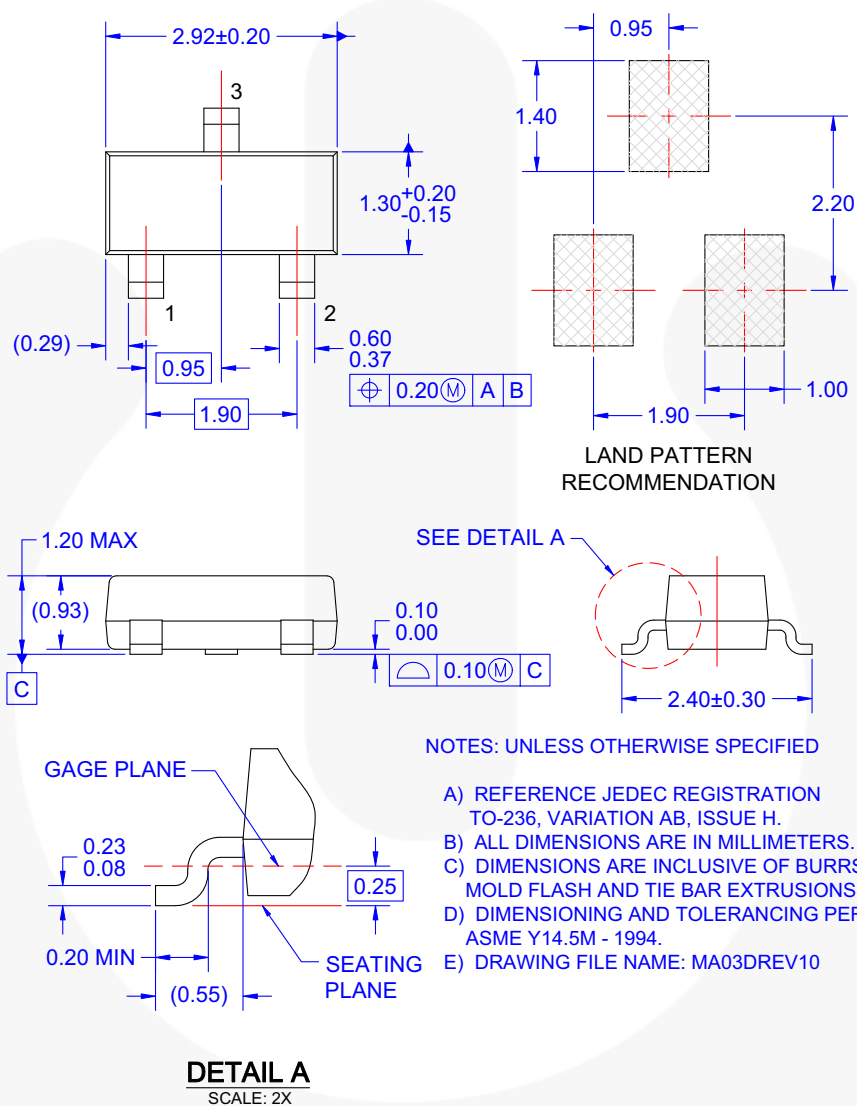


Figure 2. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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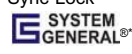
<http://www.fairchildsemi.com/dwg/MA/MA03D.pdf>

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| FAST®                                                                             | OptoHiT™                                       | SuperSOT™-6                                                                       | VisualMax™                                                                          |
| FastvCore™                                                                        | OPTOLOGIC®                                     | SuperSOT™-8                                                                       | VoltagePlus™                                                                        |
| FETBench™                                                                         | OPTOPLANAR®                                    | SupreMOS®                                                                         | XS™                                                                                 |
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