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November 2014

# TIP32 / TIP32A / TIP32C PNP Epitaxial Silicon Transistor

### **Features**

- · Medium Power Linear Switching Applications
- Complementary to TIP31 Series



1.Base 2.Collector 3.Emitter

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
TIP32	TIP32	TO-220 3L (Single Gauge)	Bulk
TIP32A	TIP32A	TO-220 3L (Single Gauge)	Bulk
TIP32ATU	TIP32A	TO-220 3L (Single Gauge)	Rail
TIP32C	TIP32C	TO-220 3L (Single Gauge)	Bulk
TIP32CTU	TIP32C	TO-220 3L (Single Gauge)	Rail

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Value	Unit	
		TIP32	-40		
V <sub>CBO</sub>	Collector-Base Voltage	TIP32A	-60	V	
		TIP32C	-100		
V <sub>CEO</sub>	Collector-Emitter Voltage	TIP32	-40		
		TIP32A	-60	V	
		TIP32C	-100		
V <sub>EBO</sub>	Emitter-Base Voltage		-5	V	
I <sub>C</sub>	Collector Current (DC)		-3	Α	
I <sub>CP</sub>	Collector Current (Pulse)		-5	Α	
I <sub>B</sub>	Base Current		-3	Α	
TJ	Junction Temperature		150	°C	
T <sub>STG</sub>	Storage Temperature Range		-65 to 150	°C	

## **Thermal Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter Value			
Pc	Collector Dissipation (T <sub>A</sub> = 25°C)	2	W	
r <sub>C</sub>	Collector Dissipation (T <sub>C</sub> = 25°C)	40	] vv	

## **Electrical Characteristics**

Values are at  $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	TIP32		-40		
		TIP32A	$I_C = -30 \text{ mA}, I_B = 0$	-60		V
		TIP32C		-100		
I <sub>CEO</sub>	Collector Cut-Off Current	TIP32 / TIP32A	V <sub>CE</sub> = -30 V, I <sub>B</sub> = 0		-0.3	mA
		TIP32C	$V_{CE} = -60 \text{ V}, I_{B} = 0$		-0.3	
I <sub>CES</sub>	Collector Cut-Off Current	TIP32	$V_{CE} = -40 \text{ V}, V_{EB} = 0$		-200	μΑ
		TIP32A	$V_{CE} = -60 \text{ V}, V_{EB} = 0$		-200	
		TIP32C	V <sub>CE</sub> = -100 V, V <sub>EB</sub> = 0		-200	
I <sub>EBO</sub>	Emitter Cut-Off Current		$V_{EB} = -5 \text{ V}, I_{C} = 0$		-1	mA
h <sub>FE</sub>	DC Current Gain <sup>(1)</sup>		$V_{CE} = -4 \text{ V}, I_{C} = -1 \text{ A}$	25		
			V <sub>CE</sub> = -4 V, I <sub>C</sub> =- 3 A	10	50	
V <sub>CE</sub> (sat)	V <sub>CE</sub> (sat) Collector-Emitter Saturation Voltage <sup>(1)</sup>		$I_C = -3 \text{ A}, I_B = -375 \text{ mA}$		-1.2	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(1)</sup>		$V_{CE} = -4 \text{ V}, I_{C} = -3 \text{ A}$		-1.8	V
f <sub>T</sub>	Current Gain Bandwidth Product		$V_{CE} = -10 \text{ V}, I_{C} = -500 \text{ mA},$ f = 1 MHz	3.0		MHz

## Note:

1. Pulse test: pw  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.

## **Typical Performance Characteristics**

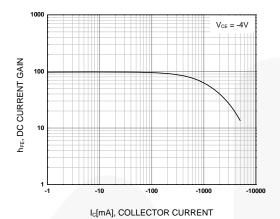


Figure 1. DC Current Gain

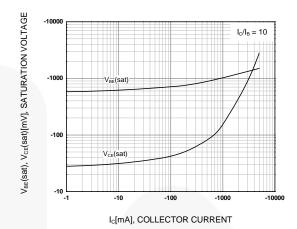


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

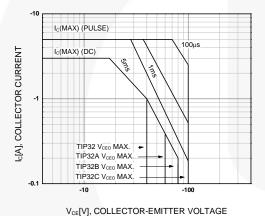


Figure 3. Safe Operating Area

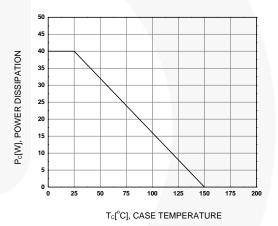
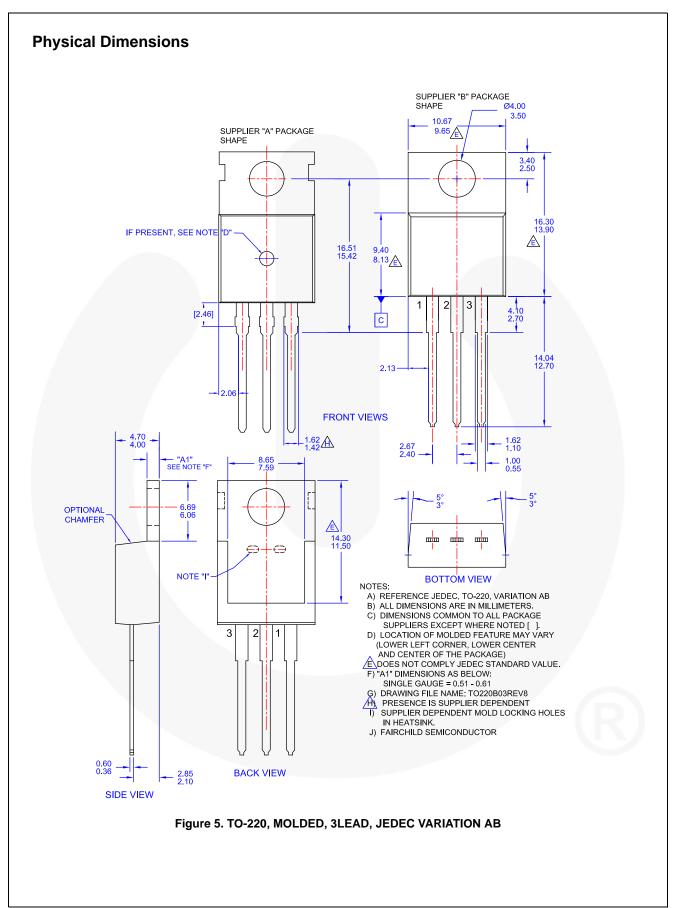


Figure 4. Power Derating







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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed Full Production		Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 172