



## Product/Process Change Notice - PCN 16\_0118 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

**PCN Title:** AD7124-4 and AD7124-8 Metal Edit and Specification Changes  
**Publication Date:** 23-Jun-2016  
**Effectivity Date:** 21-Sep-2016 *(the earliest date that a customer could expect to receive changed material)*

### Revision Description:

Initial Release

### Description Of Change

#### Die Change:

Metal silicon edits to PGA to improve noise performance at 125C.

Metal silicon edits to Interface logic to fix an anomaly with interface where changing 2 or mode bits in 3 bit MODE function in ADC\_CONTROL register may have put the part into an incorrect mode.

#### Datasheet Changes (typographical errors):

Sinc4 filter, 50 sps REJ60 bit set, ext clock. NMR reduced from 82 dB to 80 dB

Sinc3 filter, 50 sps REJ60 bit set, ext clock. NMR reduced from 66 dB to 65 dB

#### Datasheet Changes (Specification Changes):

See attachment for Details

### Reason For Change

To extend temperature capability to 125C and to fix an anomaly with Interface.

### Impact of the change (positive or negative) on fit, form, function & reliability

There is no impact to form, fit, function or reliability.

24 SCLKs exactly need to be used when writing to the ADC\_Control register. Extra SCLK pulses cause the incorrect value to be written to the ADC. If extra SCLK pulses occur but the correct 24 bits are framed by CS, then there are no issues.

### Product Identification *(this section will describe how to identify the changed material)*

The above change is effective from date code 1619 onwards.

### Summary of Supporting Information

This change has been verified through evaluation and characterization testing.

The Specification changes highlighted by this PCN are documented in the following datasheet revisions:

Rev. C of the AD7124-4 datasheet

Rev. D of the AD7124-8 datasheet

## Comments

The AD7124-4 and AD7124-8 operating temperature range is now extended from +105C to +125C

## Supporting Documents

**Attachment 1: Type:** Datasheet Specification Comparison

ADI\_PCN\_16\_0118\_Rev\_-\_AD7124\_currents\_105C\_125C.xlsx

**For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.**

**Americas:** [PCN\\_Americas@analog.com](mailto:PCN_Americas@analog.com)

**Europe:** [PCN\\_Europe@analog.com](mailto:PCN_Europe@analog.com)

**Japan:** [PCN\\_Japan@analog.com](mailto:PCN_Japan@analog.com)

**Rest of Asia:** [PCN\\_ROA@analog.com](mailto:PCN_ROA@analog.com)

**Appendix A - Affected ADI Models**

**Added Parts On This Revision - Product Family / Model Number (9)**

AD7124-4 / AD7124-4BCPZ	AD7124-4 / AD7124-4BCPZ-RL	AD7124-4 / AD7124-4BCPZ-RL7	AD7124-4 / AD7124-4BRUZ	AD7124-4 / AD7124-4BRUZ-RL
AD7124-4 / AD7124-4BRUZ-RL7	AD7124-8 / AD7124-8BCPZ	AD7124-8 / AD7124-8BCPZ-RL	AD7124-8 / AD7124-8BCPZ-RL7	

**Appendix B - Revision History**

<b>Rev</b>	<b>Publish Date</b>	<b>Effectivity Date</b>	<b>Rev Description</b>
Rev. -	23-Jun-2016	21-Sep-2016	Initial Release

Analog Devices, Inc.

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