



PRODUCT BULLETIN

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ISSUE DATE: 24-Mar-2016
NOTIFICATION: 17095
TITLE: Release of Erratum A-009942 Impacting DDR Block for T4x, T2x, T1x, B4x and LS1020/21/22 Family of Devices
EFFECTIVE DATE: 25-Mar-2016

DEVICE(S)

MPN
66T2080NSE8B
B4420NSE7QQMD
B4420NSN7QQMD
B4420NXE7QQMD
B4420NXN7QQMD
B4860NS7PUMND
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B4860NXN7QUMD
B4860NY7PUMND
B4860NY7QUMND
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B4860NYN7QUMD
G4860NSE7QUMD
G4860NSN7QUMD
G4860NTN7QUMD
G4860NXE7QUMD
G4860NXN7QUMD

G4860NYN7QUMD
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LS1020AXE7KQA
LS1020AXE7KQB
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AFFECTED CHANGE CATEGORIES

- ERRATA

DESCRIPTION OF CHANGE

This notification is for the release of DDR erratum A-009942.

A-009942: DDR Controller Can Train to Non-Optimal Setting

Description: During the receive data training, the DDRC may complete on a non-optimal setting.

Impact: With non-optimal training results, data corruption could be detected or initialization may fail.

Workaround:

- If operating at DDR-1333, then set DEBUG_29 to a value of 0x0080006a before MEM_EN is set.
- If operating at DDR-1600, then set DEBUG_29 to a value of 0x0070006f before MEM_EN is set.
- If operating at DDR-1867, then set DEBUG_29 to a value of 0x00700076 before MEM_EN is set.
- If operating at DDR-2133, then set DEBUG_29 to a value of 0x0060007b before MEM_EN is set.

This erratum must be applied in addition to any other errata that requires this register.

REASON FOR CHANGE

Notify customers with new erratum release.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No anticipated impact to product form, fit, function or reliability.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Support Case](#). Be aware that after you select this link to enter your request, you must choose the topic "Product Change Notification" once on the Salesforce page.

For sample inquiries - please go to www.nxp.com

QUALIFICATION STATUS: N/A

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:
[17095_PB_17095_DDR_Erratum_A_009942.pdf](#)
