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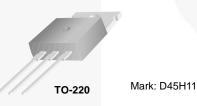
D45H11 PNP Power Amplifier

Features

- Sourced from process 5Q
- General-Purpose Switching Transistor
- Low Corrector-Emitter Saturation Voltage
- High-Fast Switching Speed

Description

This device is designed for power amplifier, regulator, and switching circuits where speed is important.



1. Base 2. Collector 3. Emitter

Ordering Information

Part Number	Marking	Package	Packing Method
D45H11	D45H11	TO-220 3L	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-80	V
Ι _C	Collector Current - Continuous	-10	A
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
р	Total Device Dissipation	60	W
PD	Derate Above 25°C	480	mW/°C
R _{θJC}	Thermal Resistance, Junction to Case	2.1	°C/W
R _{0JA}	Thermal Resistance, Junction to Ambient	62.5	°C/W

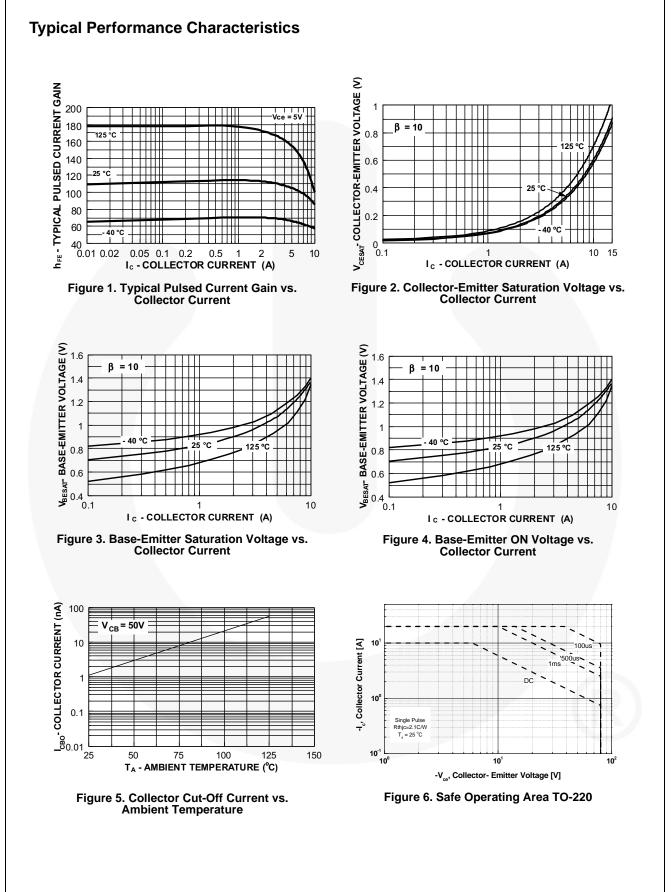
Note:

1. Device mounted on FR-4 PCB 36 mm x 18 mm x 1.5 mm: mounting pad for the collector lead minimum 6 cm².

Electrical Characteristics

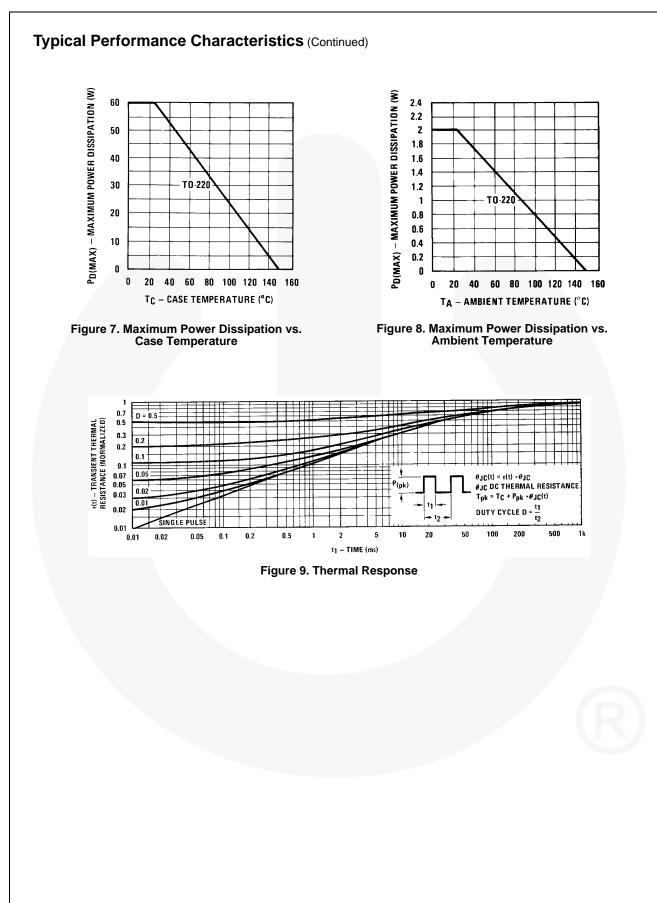
Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

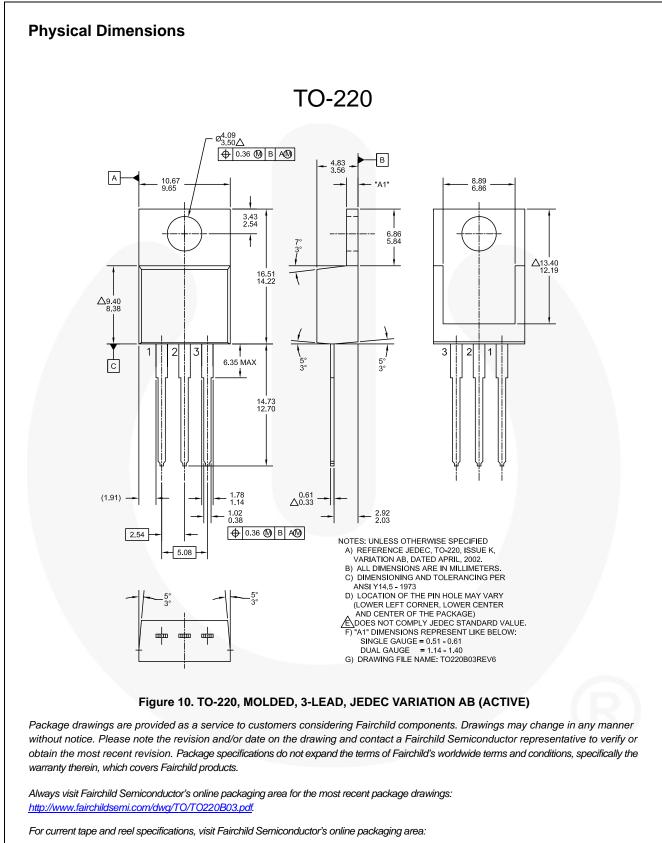
Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Charact	eristics				
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = -100 \text{ mA}, I_{\rm B} = 0$	-80		V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -80 \text{ V}, I_{E} = 0$		-10	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -5 V, I_{C} = 0$		-100	μA
On Charact	eristics				
h _{FE} [DC Current Gain	$V_{CE} = -1 V, I_{C} = -2 A$	60		
		$V_{CE} = -1 V, I_{C} = -4 A$	40		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_{\rm C} = -8 \text{ A}, I_{\rm B} = -0.4 \text{ A}$		-1.0	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_{\rm C} = -8 \text{ A}, I_{\rm B} = -0.8 \text{ A}$		-1.5	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -2 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$	-0.54	-0.65	V
Small Signa	I Characteristics				
f _T	Current Gain Bandwidth Product	$I_{C} = -500 \text{ mA}, V_{CE} = -10 \text{ V}$	40		MHz



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http://www.fairchildsemi.com/packing_dwg/PKG-TO220B03.pdf.

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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