Silicon Epitaxial Planar Small Signal Diode





Description

These diodes are also available in other case styles including the DO-35 case with the type designation IN4148, the MiniMELF case with the type designation LL4148 and the MicroMELF case with the type designation MCL4148.

Features

SOD 123 package

Fast switching

Reverse voltage: 100 VoltsForward current: 150mA

Absolute Maximum Ratings (T_A = 25°C)

Parameter	Symbol	Value	Unit	
Peak Reverse Voltage	VRM	100	V	
Reverse Voltage	VR	75	V	
Average Rectified Current Half Wave Rectification With Resistive Load f ≥ 50Hz (Note 1)	lf(AV)	150	mA	
Non-repetitive Peak Forward Surge Current @ = 1.0µs	IFSM	2	Α	
Power Dissipation (Note 1)	Po	400	mW	
Thermal Resistance Junction to Ambient Air (Note 1)	Reja	450	°C/W	
Junction Temperature	TJ	150	°C	
Storage Temperature	Tstg	-65 to +150	°C	

Electrical Characteristics (T_A = 25°C)

Parameter	Symbol	Test Conditions	Min.	Max.	Unit
Forward Voltage	VF	IF = 10mA	-	1	V
Reverse Current	lR	V _R = 20V V _R = 75V V _R = 20V, T _J = 150°C	- -	25 5 50	nΑ μΑ μΑ
Capacitance	Ст	V _F = V _R = 0V	-	4	pF
Reverse Recovery Time	trr	from I _F = 10mA to I _R = 1mA, V_R = 6V, R_L = 100 Ω	-	4	ns
Voltage rise when switching ON (tested with 50mA pulse)	VFR	tp = 0.1us, rise time <30ns, fp = (5 to 100)kHz	-	2.5	V
Rectification Efficiency	η _V	f = 100MHz, VRF = 2V	0.45	-	-

Note: 1. Valid provided that electrodes are kept at ambient temperature.

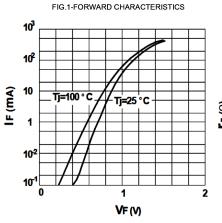


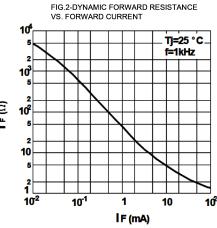


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Rating and Characteristic Curves





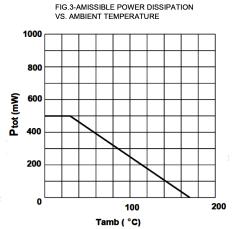
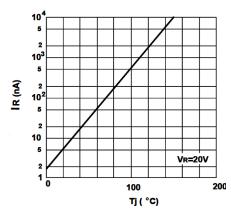
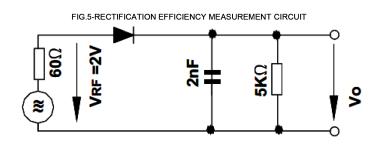
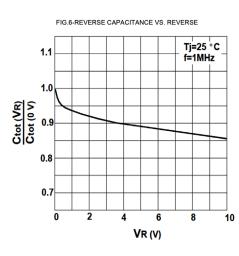
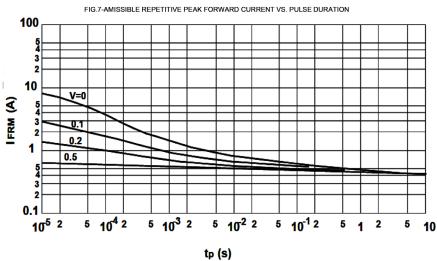


FIG.4-LEAKAGE CURRENT VS. JUNVTION TEMPERATURE









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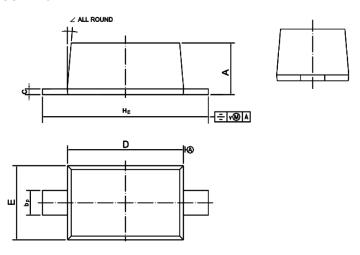


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Dimensions:

SOD-123



Unit	Α	bp	С	D	E	HE	V	4
mm	1.15 1.05	0.6 0.5	0.135 0.127	2.7 2.6	1.65 1.55	3.9 3.7	0.2	5°

Part Number Table

Description	Part Number		
Silicon Epitaxial Planar Small Signal Diode	1N4148W		

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