

SCT3030AL

N-channel SiC power MOSFET

V_{DSS}	650V
R _{DS(on)} (Typ.)	30m $Ω$
I _D	70A
P_D	262W

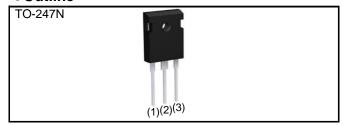
Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

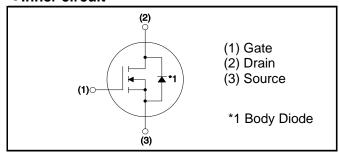
Application

- · Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

Outline



•Inner circuit



Packaging specifications

	 	
	Packing	Tube
	Reel size (mm)	-
Type	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3030AL

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	650	V
Continuous drain current	T _c = 25°C	I _D *1	70	А
Continuous drain current	T _c = 100°C	I _D ^{*1}	49	А
Pulsed drain current		I _{D,pulse} *2	175	А
Gate - Source voltage		V_{GSS}	−4 to 22	V
Junction temperature		T _j	175	°C
Range of storage temperature		T _{stg}	-55 to +175	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R _{thJC}	ı	0.44	0.57	°C/W

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r alallicici	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	650	1	ı	V
		$V_{DS} = 650 \text{V}, \ V_{GS} = 0 \text{V}$				
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	1	10	μΑ
		T _j = 150°C	-	2	-	
Gate - Source leakage current	I_{GSS+}	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -4V$, $V_{DS} = 0V$	1	-	-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_D = 13.3 \text{mA}$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 27A$				
Static drain - source on - state resistance	R _{DS(on)} *3	T _j = 25°C	-	30	39	mΩ
		T _j = 125°C	-	39.6	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	7	-	Ω

●Electrical characteristics (T_a = 25°C)

Davamatar	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g _{fs} *3	$V_{DS} = 10V, I_D = 27A$	-	9.4	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	1526	-	
Output capacitance	C _{oss}	V _{DS} = 500V	-	89	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	42	-	
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V$ to 300V	-	230	-	pF
Turn - on delay time	t _{d(on)} *3	$V_{DD} = 300V, I_D = 18A$	-	22	-	
Rise time	t _r *3	V _{GS} = 18V/0V	-	41	ı	nc
Turn - off delay time	t _{d(off)} *3	$R_L = 17\Omega$	-	48	ı	ns
Fall time	t _f *3	$R_G = 0\Omega$	-	27	ı	
Turn - on switching loss	E _{on} *3	$V_{DD} = 300V, I_{D} = 27A$ $V_{GS} = 18V/0V$	-	168	-	1
Turn - off switching loss	E _{off} *3	$R_G = 0\Omega L=250\mu H$ * E_{on} includes diode reverse recovery	-	112	-	μJ

•Gate Charge characteristics $(T_a = 25^{\circ}C)$

Dorometer	Cymbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*3}	V _{DD} = 300V	-	104	-	
Gate - Source charge	Q _{gs} *3	I _D = 27A	-	25	-	nC
Gate - Drain charge	Q _{gd} *3	V _{GS} = 18V	-	42	-	
Gate plateau voltage	V _(plateau)	$V_{DD} = 300V, I_D = 27A$	-	9.6	ı	V

^{*1} Limited only by maximum temperature allowed.

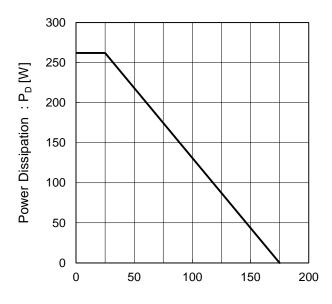
^{*2} PW \leq 10 $\mu s,$ Duty cycle \leq 1%

^{*3} Pulsed

•Body diode electrical characteristics (Source-Drain) $(T_a = 25^{\circ}C)$

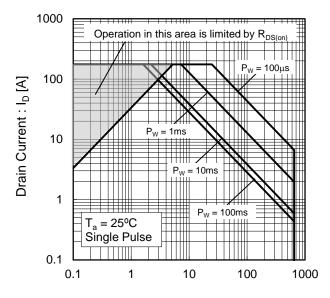
Parameter	Symbol	Conditions	Values			Unit
raiainetei	Symbol		Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	-T _c = 25°C	-	1	70	А
Inverse diode direct current, pulsed	I _{SM} *2		-	-	175	А
Forward voltage	V _{SD} *3	$V_{GS} = 0V, I_{S} = 27A$	-	3.2	ı	V
Reverse recovery time	t _{rr} *3	I _F = 27A, V _R = 600V di/dt = 1100A/μs	-	26	ı	ns
Reverse recovery charge	Q _{rr} *3		-	130	-	nC
Peak reverse recovery current	I _{rrm} *3		-	10	-	Α

Fig.1 Power Dissipation Derating Curve



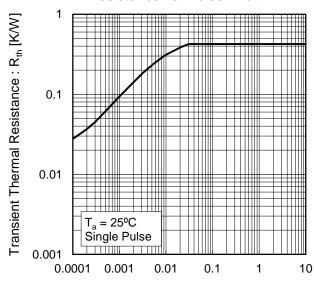
Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area



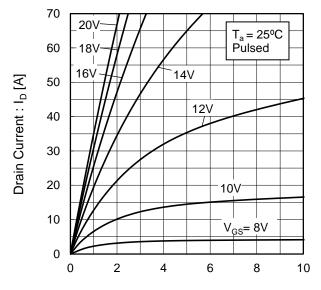
Drain - Source Voltage : V_{DS} [V]

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



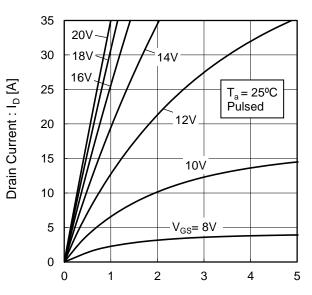
Pulse Width: P_W [s]

Fig.4 Typical Output Characteristics(I)

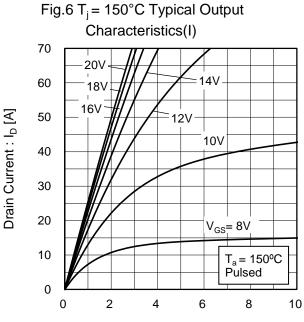


Drain - Source Voltage : V_{DS} [V]

Fig.5 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

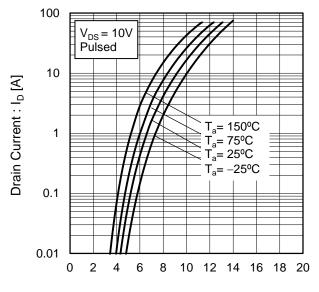


Drain - Source Voltage : V_{DS} [V]

Fig.7 T_i = 150°C Typical Output Characteristics(II) 35 20V 18V 30 Drain Current : I_D [A] 25 10V 20 15 $V_{GS} = 8V$ 10 5 $T_a = 150^{\circ}C$ Pulsed 0 2 1 3 5 0

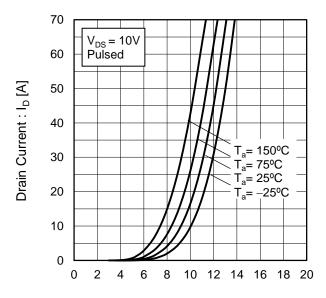
Drain - Source Voltage : V_{DS} [V]

Fig.8 Typical Transfer Characteristics (I)



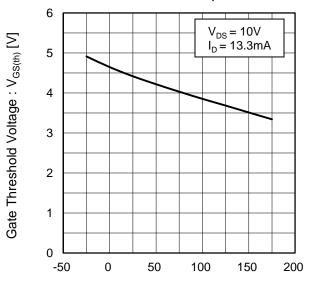
Gate - Source Voltage : V_{GS} [V]

Fig.9 Typical Transfer Characteristics (II)



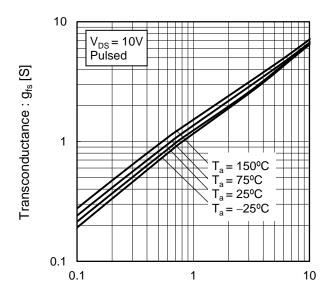
Gate - Source Voltage : V_{GS} [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature



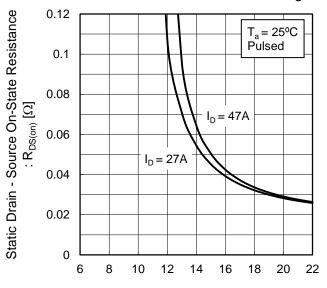
Junction Temperature : T_j [°C]

Fig.11 Transconductance vs. Drain Current



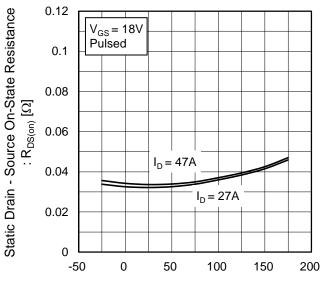
Drain Current : I_D [A]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



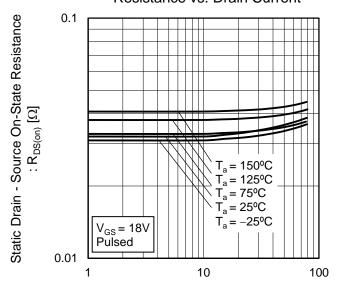
Gate - Source Voltage : V_{GS} [V]

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



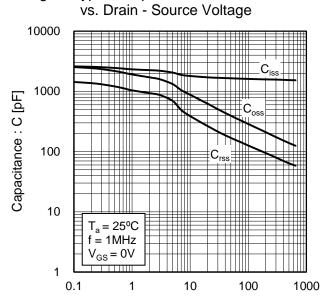
Junction Temperature : T_i [°C]

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current



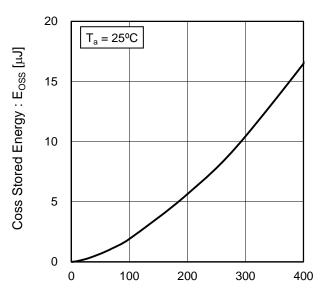
Drain Current : I_D [A]

Fig.15 Typical Capacitance



Drain - Source Voltage : V_{DS} [V]

Fig.16 Coss Stored Energy



Drain - Source Voltage : V_{DS} [V]

Fig.17 Switching Characteristics

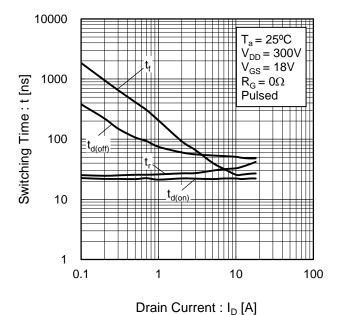
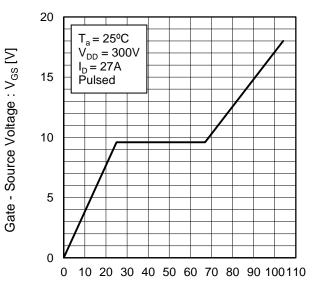
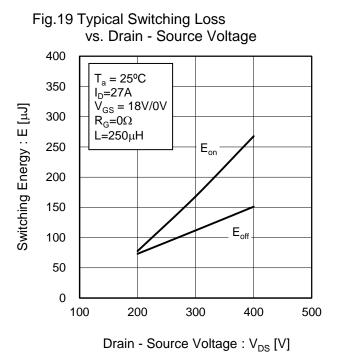


Fig.18 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]



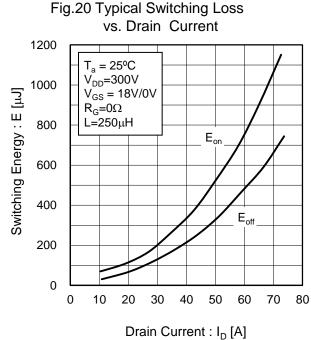
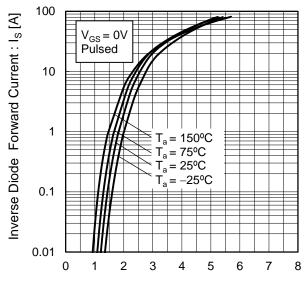


Fig.21 Typical Switching Loss vs. External Gate Resistance 1200 $T_a = 25^{\circ}C$ V_{DD}=300V 1000 $I_D = 27A$ $V_{GS} = 18V/0V$ 800 L=250μH E_{on} 600 400 200 0 5 0 10 15 20 25 30 External Gate Resistance : $R_G [\Omega]$

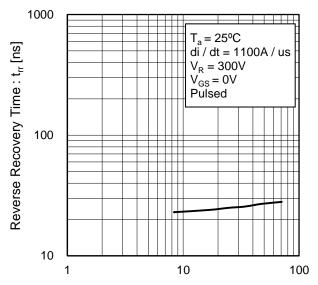
Switching Energy : E $[\mu J]$

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.23 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

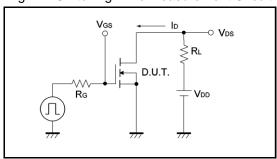


Fig.2-1 Gate Charge Measurement Circuit

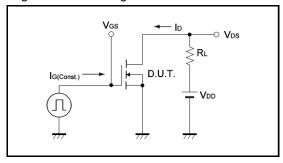


Fig.3-1 Switching Energy Measurement Circuit

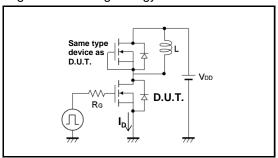


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

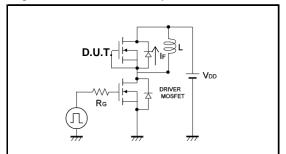


Fig.1-2 Switching Waveforms

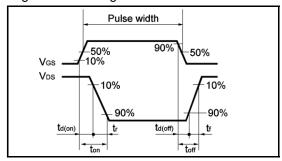


Fig.2-2 Gate Charge Waveform

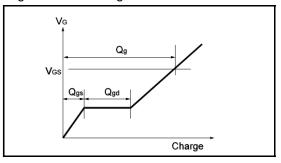
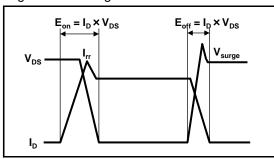
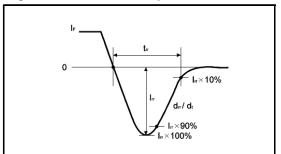


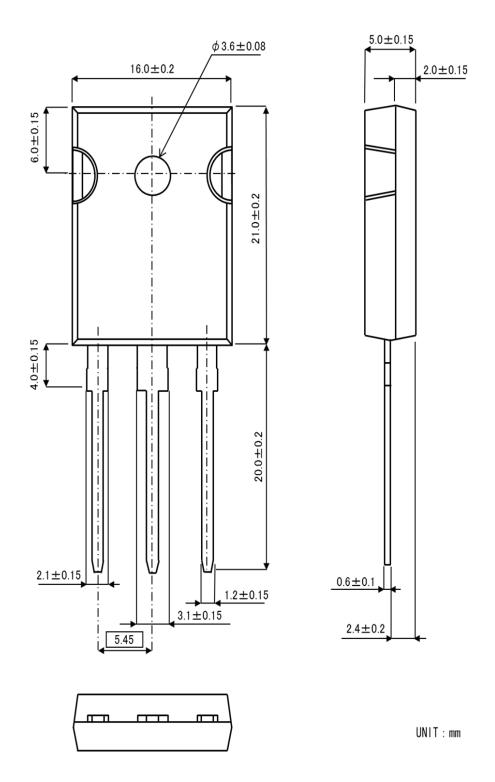
Fig.3-2 Switching Waveforms





Dimensions

TO-247



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SCT3030AL - Web Page

Distribution Inventory

Part Number	SCT3030AL
Package	TO-247N
Unit Quantity	450
Minimum Package Quantity	30
Packing Type	Tube
Constitution Materials List	inquiry
RoHS	Yes