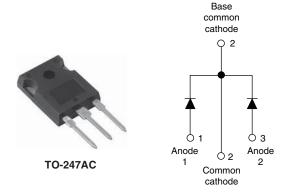


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Vishay Semiconductors

# Hyperfast Rectifier, 2 x 30 A FRED Pt®



PRODUCT SUMMARY							
Package	TO-247AC						
I <sub>F(AV)</sub>	2 x 30 A						
$V_{R}$	300 V						
V <sub>F</sub> at I <sub>F</sub>	0.92 V						
t <sub>rr</sub> typ.	See Recovery table						
T <sub>J</sub> max.	175 °C						
Diode variation	Common cathode						

#### **FEATURES**

- · Hyperfast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC®-JESD 47







ROHS
COMPLIANT
HALOGEN
FREE
Available

#### **DESCRIPTIONS / APPLICATIONS**

VS-60CPH03PbF, VS-60CPH03-N3 series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Repetitive peak reverse voltage	$V_{RRM}$		300	V						
Average rectified forward current total device	I <sub>E(A\/)</sub>	T <sub>C</sub> = 143 °C	30							
			60	Α						
Non-repetitive peak surge current per leg	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	300							
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C						

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	300	-	-					
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	1.08	1.25	V				
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 125 °C	=	0.92	1.00					
December 1 and 1 a	I <sub>R</sub>	$V_R = V_R$ rated	=	0.05	60					
Reverse leakage current		T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	=	20	300	μΑ				
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 300 V	=	70	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	3.5	-	nΗ				



# VS-60CPH03PbF, VS-60CPH03-N3

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS				
		$I_F = 1.0 \text{ A}, dI_F/dt = 1.0 \text{ A}$	-	-	55					
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	39	-	ns			
		T <sub>J</sub> = 125 °C		-	57	-				
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 30 A	-	2.8	-	^			
		T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = - 200 A/μs V <sub>R</sub> = 200 V	-	7.5	-	A			
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	55	-	20			
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	214	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C			
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.5	0.9				
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount		-	40	°C/W			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.4	-				
Weight			-	6.0	-	g			
vveignt			-	0.22	-	OZ.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-247AC	60CPH03						

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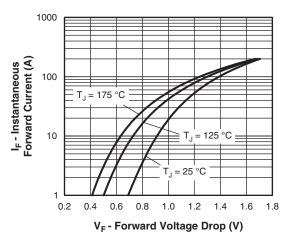


Fig. 1 - Typical Forward Voltage Drop Characteristics

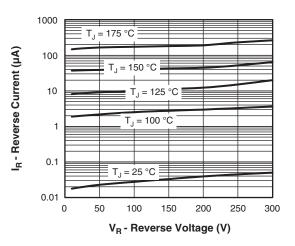


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

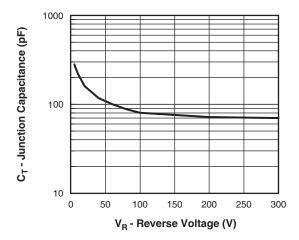


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

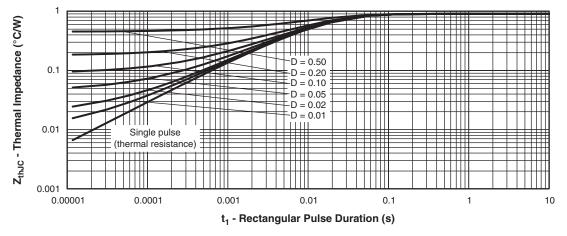


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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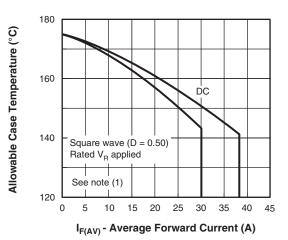


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

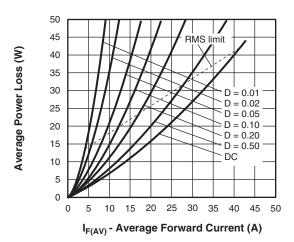


Fig. 7 - Forward Power Loss Characteristics

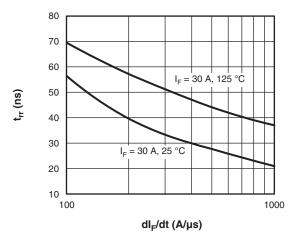


Fig. 6 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

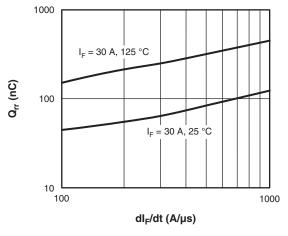


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 

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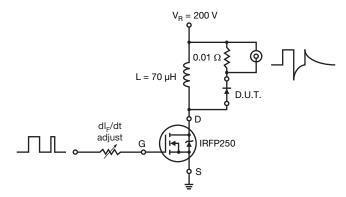
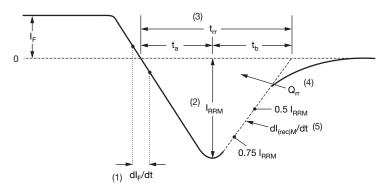


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{r}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  ${\rm Q_{rr}}$  area under curve defined by  ${\rm t_{rr}}$  and  ${\rm I_{RRM}}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

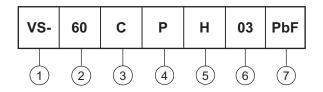
Fig. 10 - Reverse Recovery Waveform and Definitions

## VS-60CPH03PbF, VS-60CPH03-N3

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### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (60 = 60 A)

Circuit configuration:

C = common cathode

4 - Package:

P = TO-247AC (modified)

5 - H = hyperfast recovery

- Voltage code (03 = 300 V)

7 - Environmental digit:

PbF = lead (Pb)-free and RoHS-compliant

-N3 = halogen-free, RoHS-compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-60CPH03PbF	25	500	Antistatic plastic tube						
VS-60CPH03-N3	25	500	Antistatic plastic tube						

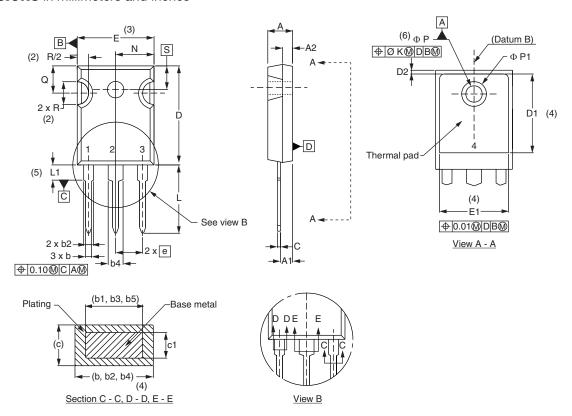
LINKS TO RELATED DOCUMENTS								
Dimensions		www.vishay.com/doc?95542						
Part marking information	TO-247ACPbF	www.vishay.com/doc?95226						
	TO-247AC-N3	www.vishay.com/doc?95007						



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### TO-247 - 50 mils L/F

#### **DIMENSIONS** in millimeters and inches



CVMPOL	SYMBOL MILLIMETERS		INCHES		MILLIMETERS INC		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STIME	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053		
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3	
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-		
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC		
b1	0.99	1.35	0.039	0.053			ØΚ	0.2	254	0.0	)10		
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634		
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169		
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0.3			
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144		
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291		
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224		
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216		
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	'BSC		

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}$  Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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