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#### **General Description**

This device is particularly suited for compact power management in portable electronic equipment where 3V to 20V input and 2.3A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT<sup>TM</sup>-6 package.

#### Features

- $V_{\text{DROP}} = 0.2V @ V_{\text{IN}} = 12V, I = 2.5 \text{ A. } R_{\text{I}} = 0.08 \Omega$  $V_{\text{DROP}}^{\text{DROP}} = 0.2V @ V_{\text{IN}}^{\text{IN}} = 5V, I = 1.6 \text{ A. } R_{(ON)}^{\text{ON}} = 0.125 \Omega.$
- Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6kV Human Body Model).
- High performance PowerTrench<sup>™</sup> technology for extremely low on-resistance.
- SuperSOT<sup>TM</sup>-6 package design using copper lead frame for superior thermal and electrical capabilities.

### Applications

- Power management
- Load actuation





## SuperSOT<sup>™</sup>-6

| Symbol   | Parameter  |  |   | Ratings                 | Units        |
|--|--|--|---|-------------------------|--------------|
| Vin  | Input Voltage  | e Range  | (Note 1)  | 3 - 20                  | V            |
| V <sub>on/off</sub>  | On/Off Volta   | ge Range   |   | 1.5 - 8                 | V            |
| D  | Load Curren  | t - Continuous   | (Note 2)  | 2.3                     | A            |
|  |  | - Pulsed   |   | 10                      |              |
| PD   | Maximum Po   | ower Dissipation   | (Note 1)  | 0.7                     | W            |
| T <sub>J</sub> , T <sub>stg</sub>  | Operating an   | d Storage Temperature I  | Range   | -55 to +150             | ۰C           |
| ESD  | Electrostatic Discharge Rating MIL-STD-883D<br>Human-Body-Model (100pf/1500 Ohm) |  | TD-883D<br>n)                                       | 6                       | kV           |
|  |  |  |   |                         |              |
| Therma   | I Charact  | eristics   | nient (Note 2)                                      | 180                     | °C/W         |
| <mark>Therma</mark><br>R <sub>өл</sub><br>R <sub>өлс</sub>                             | I Charact<br>Thermal Res<br>Thermal Res  | eristics<br>istance, Junction-to-Amb<br>istance, Junction-to-Cas                               | Dient (Note 2)<br>e (Note 2)                        | 180<br>60               | °C/W<br>°C/W |
| Therma<br><sub>R<sub>θ</sub>JA</sub><br><sub>R<sub>θ</sub>Jc</sub><br>Packag<br>Device | I Charact<br>Thermal Res<br>Thermal Res<br><b>e Marking</b><br>Marking           | eristics<br>istance, Junction-to-Amb<br>istance, Junction-to-Cas<br>I and Ordering I<br>Device | e (Note 2)<br>e (Note 2)<br>nformation<br>Reel Size | 180<br>60<br>Tape width | ○C/W<br>○C/W |

# FDC6330L

March 2015

|                  |                           |  |     |                |                      | 1     |
|------------------|---------------------------|--|-----|----------------|----------------------|-------|
| Symbol Parameter |                           | Test Conditions  | Min | Тур            | Max                  | Units |
| OFF Ch           | aracteristics             |  |     |                |                      |       |
| I <sub>FL</sub>  | Leakage Current           | $V_{IN} = 20 \text{ V}, V_{ON/OFF} = 0 \text{ V}$  |     |                | 1                    | μA    |
| ON Cha           | Conduction Voltage        | $V_{\rm IN} = 12  \text{V}$ VONCES = 3.3 V II = 2.5 A  | 1   |                | 0.2                  | V     |
| • DROF           | Conduction Voltage        | $V_{\rm IN} = 12  V_{\rm I},  V_{\rm ONOFF} = 0.0  V_{\rm I},  L = 2.0  V_{\rm I}$                                 |     |                | 0.2                  | V     |
|                  |                           | $V_{IN} = 5 V, V_{ON/OFF} = 3.3 V, I_L = 1.6 A$  |     |                | 0.2                  | v     |
| ( <sub>ON)</sub> | Q2 - Static On-Resistance | $V_{IN} = 5 V, V_{ON/OFF} = 3.3 V, I_L = 1.6 A$<br>$V_{GS} = -12 V, I_D = -2.3 A$<br>$V_{GS} = -5 V, I_D = -1.9 A$ |     | 0.054<br>0.081 | 0.2<br>0.08<br>0.125 | Ω     |

Notes:

1. Range of V \_in can be up to 30V, but R \_1 and R \_2 must be scaled such that V \_GS of Q2 does not exceed 20V.

2.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta JA}$  is determined by the user's board design.

3. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2.0%.

### FDC6330L Load Switch Application



#### **External Component Recommendation:**

For applications where  $Co \le 1\mu F$ .

For slew rate control, select R2 in the range of 1k -  $4.7k\Omega$  .

For additional in-rush current control,  $C1 \le 1000 pF$  can be added.

Select R1 so that the R1/R2 ratio ranges from 10 - 100. R1 is required to turn Q2 off.

FDC6330L Rev. 1.3



# FDC6330L



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