Tektronix[®]

100 V Bias Tee

PSPL5544 Datasheet



The PSPL5544 is an ultra-broadband, high-current, coaxial bias insertion tee and DC blocking capacitor. The high DC blocking voltage (100 V) and high bias current capability (2 A) make it an excellent choice for broadband measurements of power devices and circuits.

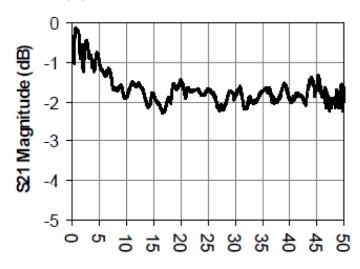
Key performance specifications

- 50 kHz to 40 GHz
- 8 ps rise time
- 100 V, 2 A

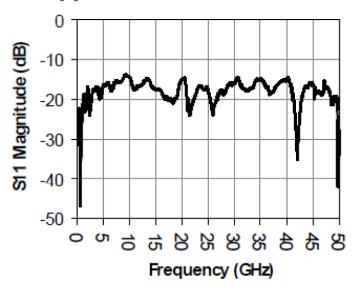
Typical performance

The following figures show the linear sweep from 40 MHz to 50 GHz. The AC connector is the input (port 1) and the AC + DC connector is the output (port 2).

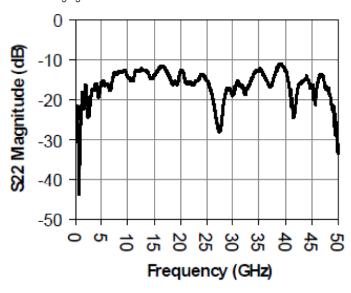
The following figure shows the S21 insertion loss.



The following figure shows the S11 return loss.



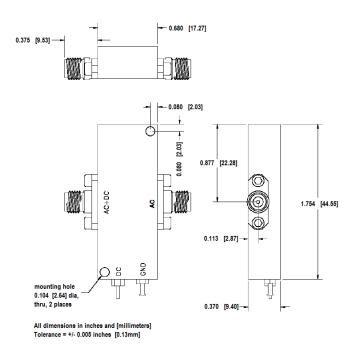
The following figure shows the S22 return loss.



Specifications

Parameter	Symbol	Units	Minimum ¹	Typical	Maximum ¹	Comments
Impedance	Z	ohms		50		
Upper 3 dB frequency	f _{c,h}	GHz	40			
Lower 3 dB frequency	f _{c,l}	kHz		50		
Rise time	t _r	ps		8		10 – 90%
Insertion loss	S ₂₁	dB		See plot	3	Measured with open on DC port.
Input (AC), Return Loss	S ₁₁	dB	10	See plot		
Output (AC+DC), Return Loss	S ₂₂	dB	8	See plot		
DC voltage	V	V			100	
DC current	1	A			2	
Capacitance	С	μF		0.2		
Inductance	L	μH		100		
Resistance	R	ohms		0.5		
CW RF power	Р	W			10	
Isolation	S ₁₃	dB		> 35		f > 100 MHz
DC path bandwidth	f _{c,DC}	kHz		42		
RF connectors	2.92 mm jacks (f)					
Warranty	One year					

Mechanical dimensions



^{1 *}Maximum and minimum values are based on manufacturing test limits at 25 °C.

Ordering information

Models

PSPL5544 BIAS TEE, 100 V, 2 A

Options

PSPL5544 292JJ $2.92\ mm$ connectors (f) on the AC and AC + DC ports, solder connection on the DC port

Datasheet

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11 Jul 2016 1PW-30574-2

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