

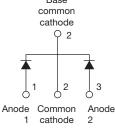
**Vishay Semiconductors** 

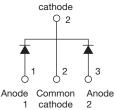
# Ultrafast Rectifier, 2 x 5 A FRED Pt<sup>®</sup>





**TO-262AA** 





Base

common

VS-MURB1020CTPbF

VS-MURB1020CT-1PbF

PRODUCT SUMMARY	
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA
I <sub>F(AV)</sub>	2 x 5 A
V <sub>R</sub>	200 V
V <sub>F</sub> at I <sub>F</sub>	0.87 V
t <sub>rr</sub>	25 ns
T <sub>J</sub> max.	175 °C
Diode variation	Common cathode

### **FEATURES**

- · Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C FREE
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RA	LUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage		V <sub>RRM</sub>		200	V	
Average rectified forward current	per leg	I		5		
Average rectilied forward current	total device	I <sub>F(AV)</sub>	Rated V <sub>R</sub> , T <sub>C</sub> = 149 °C	10	А	
Non-repetitive peak surge current p	er leg	I <sub>FSM</sub>		50	A	
Peak repetitive forward current per leg		I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 149 °C	10		
Operating junction and storage tem	peratures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C	

ELECTRICAL SPECIFICATI	CTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-		
		I <sub>F</sub> = 5 A, T <sub>J</sub> = 25 °C	-	0.99	1.08	0.99 V	
Forward voltage	VF	I <sub>F</sub> = 5 A, T <sub>J</sub> = 125 °C	-	0.87	0.99		
Forward voltage	v <sub>F</sub>	I <sub>F</sub> = 10 A, T <sub>J</sub> = 25 °C	-	1.12	1.25		
		I <sub>F</sub> = 10 A, T <sub>J</sub> = 125 °C	-	1.02	1.20		
	1	$V_{\rm R} = V_{\rm R}$ rated	-	-	10		
Reverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA	
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	8	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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1

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### Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25 \ ^{\circ}C$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS MIN. TYP. MAX.				UNITS
		$I_F = 1.0 \text{ A}, \ dI_F/dt = 50$	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	35	
Boyoroo roooyory timo	+	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A,	, I <sub>REC</sub> = 0.25 V	-	-	25	ns
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	24	-	115
		T <sub>J</sub> = 125 °C		-	35	-	
Deale recovery ourrent		T <sub>J</sub> = 25 °C	$I_F = 5 A$	-	3.3	-	۸
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 160 V	-	5.0	-	A
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	33	-	nC
		T <sub>J</sub> = 125 °C		-	76	-	no

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	5	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Woight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking davias	Case style TO-263AB (D <sup>2</sup> PAK)	MURB	B1020CT			
Marking device		Case style TO-262	MURB1020CT-1			



### **Vishay Semiconductors**

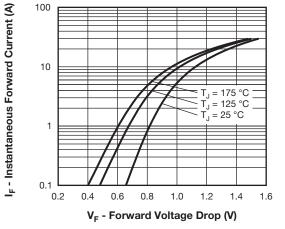


Fig. 1 - Typical Forward Voltage Drop Characteristics

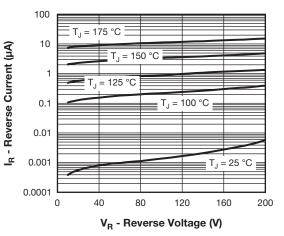


Fig. 2 - Typical Values of Reverse Current vs. **Reverse Voltage** 

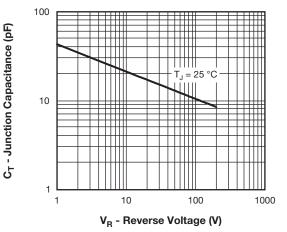


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

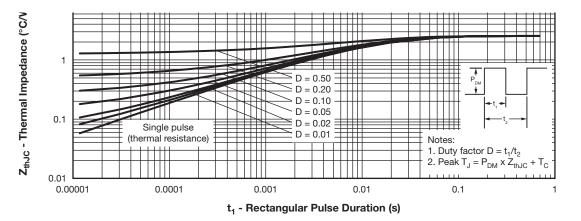
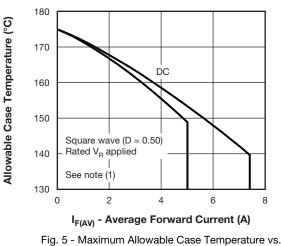


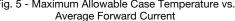
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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## VS-MURB1020CTPbF, VS-MURB1020CT-1PbF

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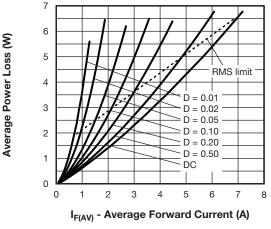
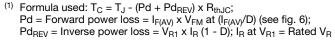


Fig. 6 - Forward Power Loss Characteristics

#### Note



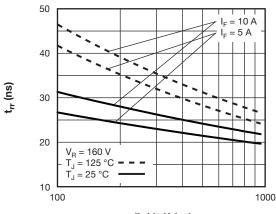




Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

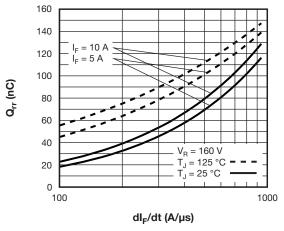


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



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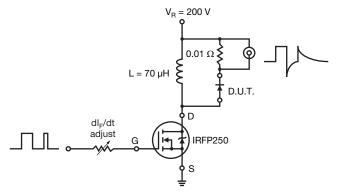


Fig. 9 - Reverse Recovery Parameter Test Circuit

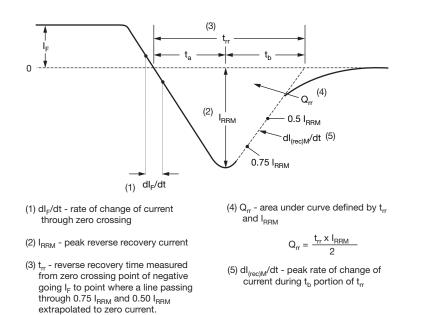
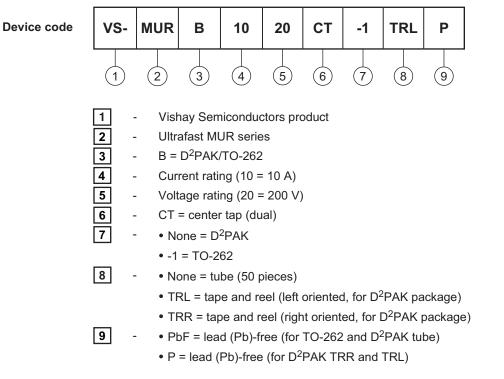


Fig. 10 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**



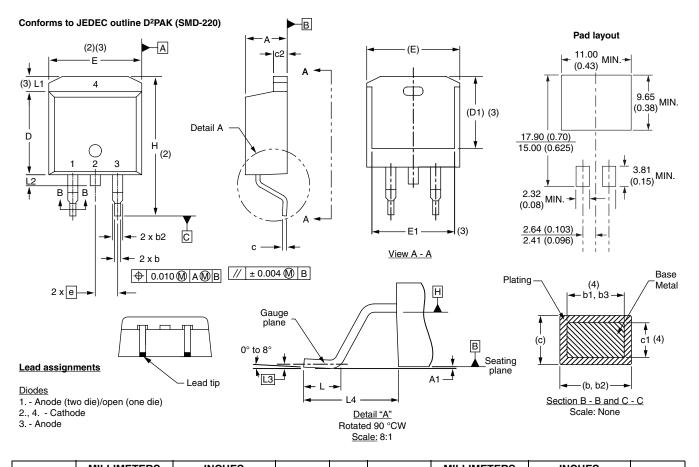
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032

Vishay High Power Products

# D<sup>2</sup>PAK, TO-262

### DIMENSIONS FOR D<sup>2</sup>PAK in millimeters and inches

SHA



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

#### Notes

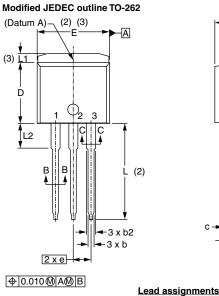
- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch

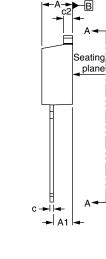
**Vishay High Power Products** 

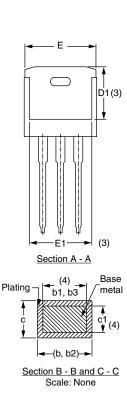
D<sup>2</sup>PAK, TO-262



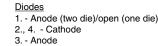
### DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



SYMBOL	MILLIM	IETERS	INCH	INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- <sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Controlling dimension: inches

<sup>(6)</sup> Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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