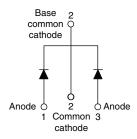


**Vishay Semiconductors** 

# Schottky Rectifier, 2 x 10 A





PRODUCT SUMMARY						
Package	TO-220AB					
I <sub>F(AV)</sub>	2 x 10 A					
V <sub>R</sub>	35 V, 40 V, 45 V					
V <sub>F</sub> at I <sub>F</sub>	0.57 V					
I <sub>RM</sub> max.	15 mA at 125 °C					
T <sub>J</sub> max.	175 °C					
Diode variation	Common cathode					
E <sub>AS</sub>	13 mJ					

### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation



• High purity, high temperature epoxy encapsulation for enhanced mechanical strength RoHS and moisture resistance



- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and gualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### DESCRIPTION

The VS-20CTQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL CHARACTERISTICS VALUES					
I <sub>F(AV)</sub>	Rectangular waveform	20	А		
V <sub>RRM</sub>	Range	35 to 45	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1060	А		
V <sub>F</sub>	10 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.57	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS- 20CTQ035PbF	VS- 20CTQ035-N3	VS- 20CTQ040PbF	VS- 20CTQ040-N3	VS- 20CTQ045PbF	VS- 20CTQ045-N3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>							
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	35	40	40	45	45	V

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_C = 145$ °C	, rectangular waveform	20				
Maximum peak one cycle non-repetitive surge current per leg	Isou	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	1060	A			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	265				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 2.0 \text{ A}, L = 6.5 \text{ mH}$		13	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero Frequency limited by T <sub>J</sub> maximu		2.0	А			

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
		10 A	T <sub>.1</sub> = 25 °C	0.64	V		
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	20 A	1j=25 C	0.76			
	¥FM (*)	10 A	T 105 %C	0.57			
		20 A	T <sub>J</sub> = 125 °C	0.68			
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	2	mA		
See fig. 2		T <sub>J</sub> = 125 °C	$v_{\rm R} = haleu v_{\rm R}$	15			
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF		
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs		

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,\,duty\,cycle$  < 2  $\,\%$ 

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C		
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	3.25			
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation		°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50			
Approvimate weight				2	g		
Approximate weight				0.07	oz.		
	minimum			6 (5)	kgf ⋅ cm		
Mounting torque	maximum			12 (10)	$(lbf \cdot in)$		
				20CT	Q035		
Marking device	Marking device		Case style TO-220AB	20CT	Q040		
				20CT	Q045		

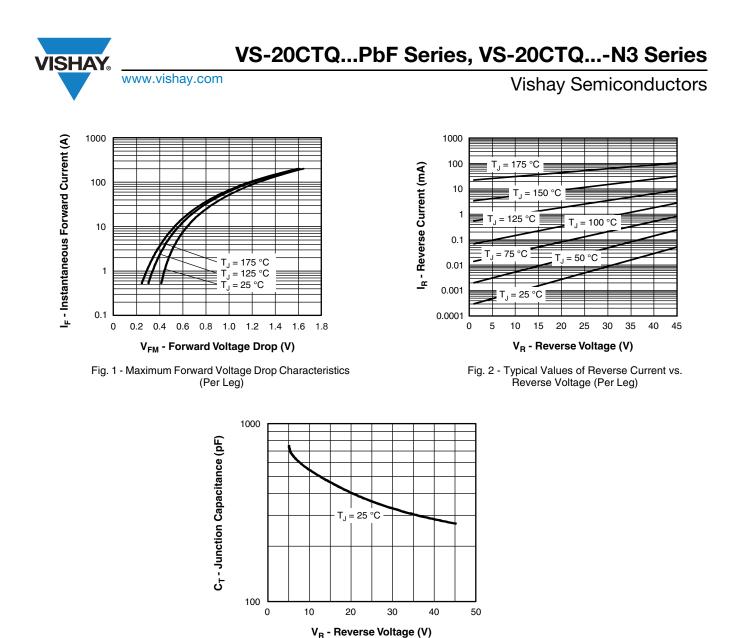


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

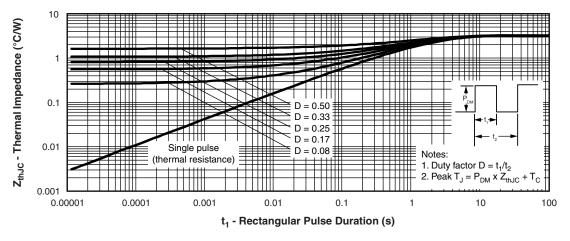
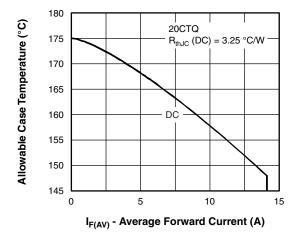


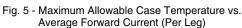
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

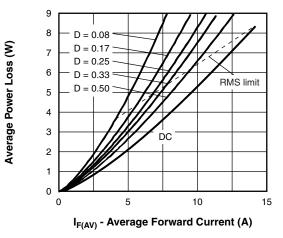


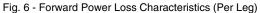
## VS-20CTQ...PbF Series, VS-20CTQ...-N3 Series

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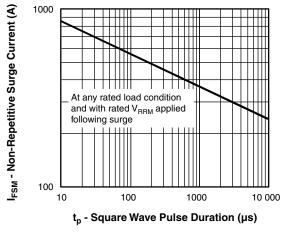


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

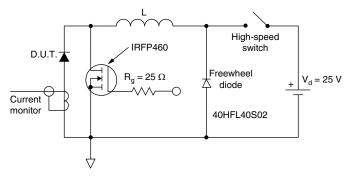


Fig. 8 - Unclamped Inductive Test Circuit



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### **ORDERING INFORMATION TABLE**

Device code	VS-	20	С	т	Q	045	PbF
		2	3	4	5	6	7
1 2 3	- - -	Curren Circuit	Semico t rating configur	ct			
4	-	C = Common cathode Package T = TO-220					
5 6 7	- - -	Voltage Enviror	Schottky "Q" series Voltage rating Environmental digit • PbF = Lead (Pb)-free and R			035 = 040 = 045 =	40 V 45 V

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-20CTQ035PbF	50	1000	Antistatic plastic tube				
VS-20CTQ035-N3	50	1000	Antistatic plastic tube				
VS-20CTQ040PbF	50	1000	Antistatic plastic tube				
VS-20CTQ040-N3	50	1000	Antistatic plastic tube				
VS-20CTQ045PbF	50	1000	Antistatic plastic tube				
VS-20CTQ045-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Daut manification	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



**Vishay Semiconductors** 

**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





.ead	assignments

**Diodes** 

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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