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Vishay General Semiconductor

# **Dual Low-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.33$  V at  $I_F = 5.0$  A



## VBT2045C



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 10 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	160 A			
$V_F$ at $I_F$ = 10 A	0.41 V			
T <sub>J</sub> max.	150 °C			
Package	TO-263AB			
Diode variations	Dual common cathode			

#### FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C ROMPLIANT
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

#### **MECHANICAL DATA**

#### Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VBT2045C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (fig. 1)	per device		20	٨	
	per diode	IF(AV)	10	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	160	A	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.44	-	V	
	I <sub>F</sub> = 10 A			0.49	0.58		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.33	-		
	I <sub>F</sub> = 10 A			0.41	0.52		
Reverse current per diode	$V_{\rm B} = 45 \text{ V}$ $T_{\rm A} = 25$	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	2000	μA	
	$v_{\rm R} = 45 V$	$V_{\rm R} = 43 \text{ V}$ $T_{\rm A} = 125 \text{ °C}$		10	30	mA	

Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT2045C	UNIT	
Typical thermal resistance	per diode	R <sub>θJC</sub>	3.0	°C/W	
	per device		2.0	C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT2045C-E3/4W	1.38	4W	50/tube	Tube	
TO-263AB	VBT2045C-E3/8W	1.38	8W	800/reel	Tape and reel	

#### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

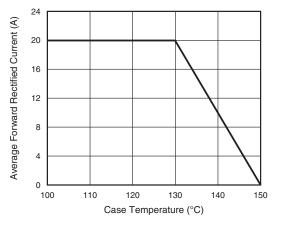


Fig. 1 - Maximum Forward Current Derating Curve

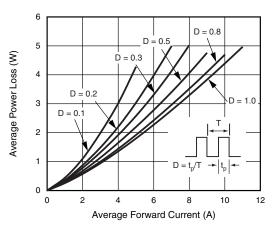
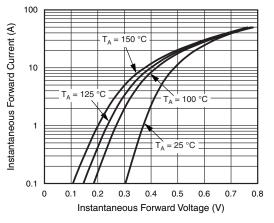


Fig. 2 - Forward Power Loss Characteristics Per Diode

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Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

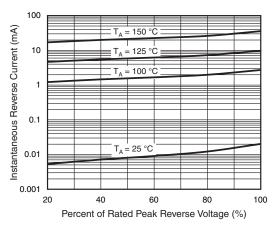
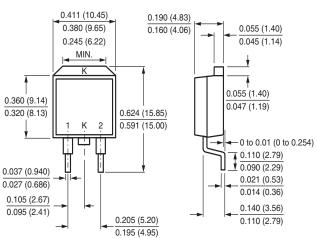


Fig. 4 - Typical Reverse Characteristics Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



#### **TO-263AB**

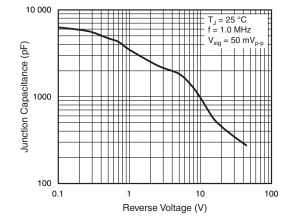


Fig. 5 - Typical Junction Capacitance Per Diode

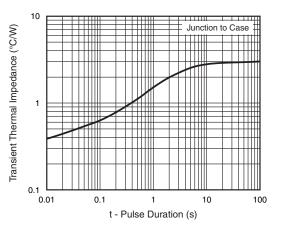
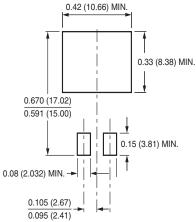


Fig. 6 - Typical Transient Thermal Impedance Per Diode

#### **Mounting Pad Layout**



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