Vishay General Semiconductor

Surface Mount Schottky Barrier Rectifier



DO-214AC (SMA)

FEATURES

- Low profile package
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency
- High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Not recommended for PCB bottom side wave mounting
- AEC-Q101 gualified
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3_X - RoHS-compliant and AEC-Q101 qualified ("_X" denotes revision code e.g. A, B,)

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise noted)					
PARAMETER	SYMBOL	SS22S	SS23S	SS24S	UNIT
Device marking code		22S	23S	24S	
Maximum repetitive peak reverse voltage	V _{RRM}	20	30	40	V
Maximum average forward rectified current (fig. 1)	I _{F(AV)}	2.0			А
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	40			А
Voltage rate of change (rated V _R)	dV/dt	10 000			V/µs
Operating junction and storage temperature range	T _{J,} T _{STG}	-55 to +150 °C			°C

I _{F(AV)}	2.0 A			
V _{RRM}	20 V, 30 V, 40 V			
I _{FSM}	40 A			
V_F at I_F = 2.0 A	0.517 V			
T _J max.	150 °C			
Package	DO-214AC (SMA)			
Diode variations	Single			

PRIMARY CHARACTERISTICS

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RoHS COMPLIANT

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 1 A	T _J = 25 °C	V _F ⁽¹⁾	0.436	-	V
	I _F = 2 A			0.517	0.55	
Reverse current	Rated V _R	$T_{J} = 25 \text{ °C}$ $I_{B}^{(2)}$	13	200	μA	
	naleu v _R	T _J = 100 °C	IR (=/	1.65	8	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	130	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	SS22S	SS23S	SS24S	UNIT
Typical thermal resistance	R _{0JA} ⁽¹⁾	75			°C/W
rypical merma resistance	R _{0JL} ⁽¹⁾	25			

Note

(1) PCB mounted with 0.4" x 0.4" (10 mm x 10 mm) copper pad areas

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SS24S-E3/61T	0.064	61T	1800	7" diameter plastic tape and reel	
SS24S-E3/5AT	0.064	5AT	7500	13" diameter plastic tape and reel	
SS24SHE3_A/H ⁽¹⁾	0.064	Н	1800	7" diameter plastic tape and reel	
SS24SHE3_A/I (1)	0.064	Ι	7500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

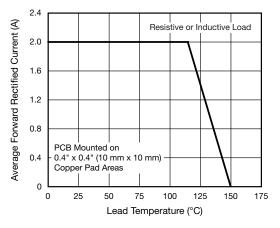


Fig. 1 - Forward Current Derating Curve

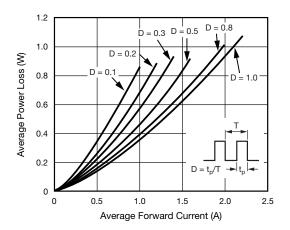


Fig. 2 - Forward Power Loss Characteristics



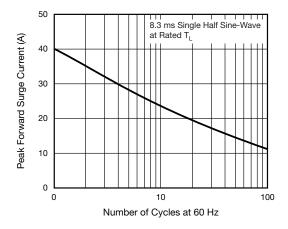


Fig. 3 - Maximum Non-Repetitive Peak Forward Surge Current

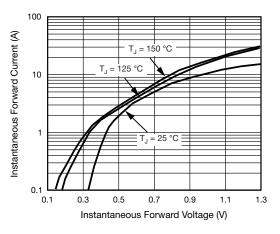
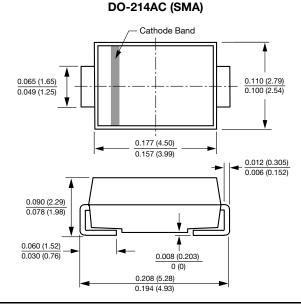
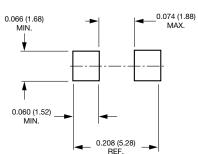


Fig. 4 - Typical Instantaneous Forward Characteristics

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Mounting Pad Layout



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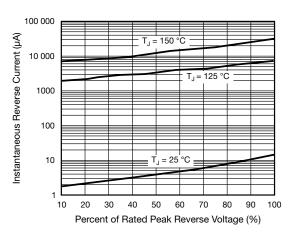


Fig. 5 - Typical Reverse Leakage Characteristics

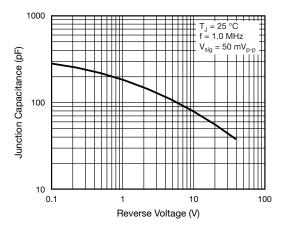


Fig. 6 - Typical Junction Capacitance

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