FEATURES
Pin programmable 2.5 V or 3.0 V output
Ultralow drift: 3 ppm/°C max
High accuracy: 2.5 V or 3.0 V ±1 mV max
Low noise: 100 nV/√Hz
Noise reduction capability
Low quiescent current: 1 mA max
Output trim capability
Plug-in upgrade for present references
Temperature output pin
Series or shunt mode operation (±2.5 V, ±3.0 V)

FUNCTIONAL BLOCK DIAGRAM

GENERAL DESCRIPTION
The AD780 is an ultrahigh precision band gap reference voltage that provides a 2.5 V or 3.0 V output from inputs between 4.0 V and 36 V. Low initial error and temperature drift combined with low output noise and the ability to drive any value of capacitance make the AD780 the ideal choice for enhancing the performance of high resolution analog-to-digital converters (ADCs) and digital-to-analog converters (DACs), and for any general-purpose precision reference application. A unique low headroom design facilitates a 3.0 V output from a 5.0 V 10% input, providing a 20% boost to the dynamic range of an ADC over performance with existing 2.5 V references.

The AD780 can be used to source or sink up to 10 mA, and can be used in series or shunt mode, thus allowing positive or negative output voltages without external components. This makes it suitable for virtually any high performance reference application. Unlike some competing references, the AD780 has no region of possible instability. The part is stable under all load conditions when a 1 µF bypass capacitor is used on the supply.

A temperature output pin on the AD780 provides an output voltage that varies linearly with temperature, allowing the part to be configured as a temperature transducer while providing a stable 2.5 V or 3.0 V output.

The AD780 is a pin compatible performance upgrade for the LT1019(A)–2.5 and the AD680. The latter is targeted toward low power applications.

The AD780 is available in three grades in PDIP and SOIC packages. The AD780AN, AD780AR, AD780BN, AD780BR, and AD780CR are specified for operation from −40°C to +85°C.

PRODUCT HIGHLIGHTS
1. The AD780 provides a pin programmable 2.5 V or 3.0 V output from a 4 V to 36 V input.
2. Laser trimming of both initial accuracy and temperature coefficients results in low errors over temperature without the use of external components. The AD780BN has a maximum variation of 0.9 mV from −40°C to +85°C.
3. For applications that require even higher accuracy, an optional fine-trim connection is provided.
4. The AD780 noise is extremely low, typically 4 mV p-p from 0.1 Hz to 10 Hz and a wideband spectral noise density of typically 100 nV/√Hz. This can be further reduced, if desired, by using two external capacitors.
5. The temperature output pin enables the AD780 to be configured as a temperature transducer while providing a stable output reference.
COMPARABLE PARTS
View a parametric search of comparable parts.

DOCUMENTATION
Application Notes
• AN-713: The Effect of Long-Term Drift on Voltage References

Data Sheet
• AD780: 2.5 V/3.0 V High Precision Reference Data Sheet
• AD780: Military Data Sheet

TOOLS AND SIMULATIONS
• AD780 SPICE Macro-Model

REFERENCE DESIGNS
• CN0030
• CN0044
• CN0047
• CN0100
• CN0116

DESIGN RESOURCES
• AD780 Material Declaration
• PCN-PDN Information
• Quality And Reliability
• Symbols and Footprints

DISCUSSIONS
View all AD780 EngineerZone Discussions.

SAMPLE AND BUY
Visit the product page to see pricing options.

TECHNICAL SUPPORT
Submit a technical question or find your regional support number.

DOCUMENT FEEDBACK
Submit feedback for this data sheet.
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## SPECIFICATIONS

$T_A = 25^\circ C$, $V_{IN} = 5$ V, unless otherwise noted.

### Table 1.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>AD780AN/AD780AR</th>
<th>AD780CR</th>
<th>AD780BN/AD780BR</th>
<th>Unit</th>
</tr>
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<tr>
<td><strong>OUTPUT VOLTAGE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.5 V Out</td>
<td>2.495</td>
<td>2.505</td>
<td>2.4985</td>
<td>2.5015</td>
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<td>3.0 V Out</td>
<td>2.995</td>
<td>3.005</td>
<td>2.9950</td>
<td>3.0050</td>
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<td><strong>SOLDER HEAT SHIFT</strong></td>
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<tr>
<td>Mean</td>
<td>−1.1</td>
<td>−1.1</td>
<td>−1.1</td>
<td>mV</td>
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<tr>
<td>Sigma</td>
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<td>0.4</td>
<td>0.4</td>
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<tr>
<td><strong>OUTPUT VOLTAGE DRIFT$^1$</strong></td>
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<tr>
<td>$−40^\circ C$ to $+85^\circ C$</td>
<td>7</td>
<td>7</td>
<td>3</td>
<td>ppm/°C</td>
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<tr>
<td>$−55^\circ C$ to $+125^\circ C$</td>
<td>20</td>
<td>20</td>
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<td>ppm/°C</td>
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<td><strong>LINE REGULATION</strong></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>2.5 V Output, $4 V \leq +V_{IN} \leq 36$ V, $T_{MIN}$ to $T_{MAX}$</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>μV/V</td>
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<tr>
<td>3.0 V Output, $4.5 V \leq +V_{IN} \leq 36$ V, $T_{MIN}$ to $T_{MAX}$</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>μV/V</td>
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<tr>
<td><strong>LOAD REGULATION, SERIES MODE</strong></td>
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<tr>
<td>Sourcing $0 mA &lt; I_{OUT} &lt; 10$ mA</td>
<td>50</td>
<td>50</td>
<td>50</td>
<td>μV/mA</td>
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<tr>
<td>$T_{MIN}$ to $T_{MAX}$</td>
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<td></td>
</tr>
<tr>
<td>Sinking $−10$ mA $&lt; I_{OUT} &lt; 0$ mA</td>
<td>75</td>
<td>75</td>
<td>75</td>
<td>μV/mA</td>
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<tr>
<td>$−40^\circ C$ to $+85^\circ C$</td>
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<td>75</td>
<td>75</td>
<td>μV/mA</td>
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<tr>
<td>$−55^\circ C$ to $+125^\circ C$</td>
<td>150</td>
<td>150</td>
<td>150</td>
<td>μV/mA</td>
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<tr>
<td><strong>LOAD REGULATION, SHUNT MODE</strong></td>
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<td></td>
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<tr>
<td>$I &lt; I_{SHUNT} &lt; 10$ mA</td>
<td>75</td>
<td>75</td>
<td>75</td>
<td>μV/mA</td>
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<td><strong>QUIESCENT CURRENT, 2.5 V SERIES MODE$^2$</strong></td>
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<tr>
<td>$−40^\circ C$ to $+85^\circ C$</td>
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<td>$−55^\circ C$ to $+125^\circ C$</td>
<td>0.8</td>
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<td><strong>MINIMUM SHUNT CURRENT</strong></td>
<td>0.7</td>
<td>1.0</td>
<td>0.7</td>
<td>1.0</td>
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<tr>
<td><strong>OUTPUT NOISE</strong></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.1 Hz to 10 Hz</td>
<td>4</td>
<td>4</td>
<td>4</td>
<td>μV p-p</td>
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<tr>
<td>Spectral Density, 100 Hz</td>
<td>100</td>
<td>100</td>
<td>100</td>
<td>nV/√Hz</td>
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<tr>
<td><strong>LONG-TERM STABILITY$^3$</strong></td>
<td>20</td>
<td>20</td>
<td>20</td>
<td>± ppm/1000 Hr</td>
</tr>
<tr>
<td><strong>TRIM RANGE</strong></td>
<td>4.0</td>
<td>4.0</td>
<td>4.0</td>
<td>± %</td>
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<tr>
<td><strong>TEMPERATURE PIN</strong></td>
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<td></td>
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<tr>
<td>Voltage Output @ $25^\circ C$</td>
<td>500</td>
<td>560</td>
<td>620</td>
<td>mV</td>
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<tr>
<td>Temperature Sensitivity</td>
<td>1.9</td>
<td>1.9</td>
<td>1.9</td>
<td>mV/°C</td>
</tr>
<tr>
<td>Output Resistance</td>
<td>3</td>
<td>3</td>
<td>3</td>
<td>kΩ</td>
</tr>
<tr>
<td><strong>SHORT-CIRCUIT CURRENT TO GROUND</strong></td>
<td>30</td>
<td>30</td>
<td>30</td>
<td>mA</td>
</tr>
<tr>
<td><strong>TEMPERATURE RANGE</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Specified Performance (A, B, C)</td>
<td>$−40$</td>
<td>$+85$</td>
<td>$−40$</td>
<td>$+85$</td>
</tr>
<tr>
<td>Operating Performance (A, B, C)$^4$</td>
<td>$−55$</td>
<td>$+125$</td>
<td>$−55$</td>
<td>$+125$</td>
</tr>
</tbody>
</table>

$^1$ Maximum output voltage drift is guaranteed for all packages.

$^2$ 3.0 V mode typically adds 100 μA to the quiescent current. Also, $I_q$ increases by 2 μA/V above an input voltage of 5 V.

$^3$ The long-term stability specification is noncumulative. The drift in subsequent 1,000 hour periods is significantly lower than in the first 1,000 hour period.

$^4$ The operating temperature range is defined as the temperature extremes at which the device will still function. Parts may deviate from their specified performance outside their specified temperature range.
ABSOLUTE MAXIMUM RATINGS

Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product. This is a stress rating only; functional operation of the product at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

Table 2.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Values</th>
</tr>
</thead>
<tbody>
<tr>
<td>+VIN to Ground</td>
<td>36 V</td>
</tr>
<tr>
<td>TRIM Pin to Ground</td>
<td>36 V</td>
</tr>
<tr>
<td>TEMP Pin to Ground</td>
<td>36 V</td>
</tr>
<tr>
<td>Power Dissipation (25°C)</td>
<td>500 mW</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>−65°C to +150°C</td>
</tr>
<tr>
<td>Lead Temperature (Soldering 10 sec)</td>
<td>300°C</td>
</tr>
<tr>
<td>Output Protection</td>
<td>Output safe for indefinite short to ground and momentary short to VIN.</td>
</tr>
<tr>
<td>ESD Classification</td>
<td>Class 1 (1000 V)</td>
</tr>
</tbody>
</table>

NOTES

Both VOUT pads must be connected to the output.

Die Thickness: The standard thickness of Analog Devices, Inc. bipolar dice is 10 mil ± 1 mil.

Die Dimensions: The dimensions given are the maximum possible die size.

Backings: The standard backside surface is silicon (not plated). Analog Devices does not recommend gold-backed dice for most applications.

Edges: A diamond saw is used to separate wafers into dice, thus providing perpendicular edges halfway through the die. In contrast to scribed dice, this technique provides a more uniform die shape and size. The perpendicular edges facilitate handling (such as tweezer pickup), while the uniform shape and size simplify substrate design and die attach.

Top Surface: The standard top surface of the die is covered by a layer of passivation. All areas are covered except bonding pads and scribe lines.

Surface Metallization: The metallization to Analog Devices bipolar dice is aluminum/copper. The minimum thickness is 10,000 Å.

Bonding Pads: All bonding pads have a minimum size of 4.0 mil by 6.0 mil. The passivation windows have a minimum size of 3.5 mil by 5.3 mil.

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.
THEORY OF OPERATION

Band gap references are the high performance solution for low supply voltage and low power voltage reference applications. In this technique, a voltage with a positive temperature coefficient is combined with the negative coefficient of a transistor’s $V_{be}$ to produce a constant band gap voltage.

In the AD780, the band gap cell contains two NPN transistors (Q6 and Q7) that differ in emitter area by 12x. The difference in their $V_{be}$ produces a PTAT current in R5. This, in turn, produces a PTAT voltage across R4 that, when combined with the $V_{be}$ of Q7, produces a voltage ($V_{bg}$) that does not vary with temperature. Precision laser trimming of the resistors and other patented circuit techniques are used to further enhance the drift performance.

![Schematic Diagram](image)

The output voltage of the AD780 is determined by the configuration of Resistors R13, R14, and R15 in the amplifier’s feedback loop. This sets the output to either 2.5 V or 3.0 V, depending on whether R15 (Pin 8) is grounded or not connected.

A unique feature of the AD780 is the low headroom design of the high gain amplifier, which produces a precision 3 V output from an input voltage as low as 4.5 V (or 2.5 V from a 4.0 V input). The amplifier design also allows the part to work with $+V_{IN} = V_{OUT}$ when current is forced into the output terminal. This allows the AD780 to work as a 2-terminal shunt regulator, providing a $–2.5$ V or $–3.0$ V reference voltage output without external components.

The PTAT voltage is also used to provide the user with a thermometer output voltage (at Pin 3) that increases at a rate of approximately 2 mV/°C.

The DNC (Pin 7) of the AD780 is a 20 kΩ resistor to $+V_{IN}$ that is used solely for production test purposes. Users who are currently using the LT1019 self-heater pin (Pin 7) must take into account the different load on the heater supply.
APPLYING THE AD780

The AD780 can be used without any external components to achieve specified performance. If power is supplied to Pin 2 and Pin 4 is grounded, Pin 6 provides a 2.5 V or 3.0 V output depending on whether Pin 8 is left unconnected or grounded.

A bypass capacitor of 1 µF (+VIN to GND) should be used if the load capacitance in the application is expected to be greater than 1 nF. The AD780 in 2.5 V mode typically draws 700 µA of Iq at 5 V. This increases by ~2 µA/V up to 36 V.

Initial error can be nulled using a single 25 kΩ potentiometer connected between VOUT, TRIM, and GND. This is a coarse trim with an adjustment range of 4%, and is only included here for compatibility purposes with other references. A fine trim can be implemented by inserting a large value resistor (e.g., 1 MΩ to 5 MΩ) in series with the wiper of the potentiometer (see Figure 5). The trim range, expressed as a fraction of the output, is simply greater than or equal to 2.1 kΩ/RNULL for either the 2.5 V or 3.0 V mode.

The external null resistor affects the overall temperature coefficient by a factor equal to the percentage of VOUT nulled.

For example, a 1 mV (0.03%) shift in the output caused by the trim circuit, with a 100 ppm/°C null resistor, adds less than 0.06 ppm/°C to the output drift (0.03% × 200 ppm/°C, since the resistors internal to the AD780 also have temperature coefficients of less than 100 ppm/°C).

NOISE PERFORMANCE

The impressive noise performance of the AD780 can be further improved, if desired, by adding two capacitors: a load capacitor (C1) between the output and ground, and a compensation capacitor (C2) between the TEMP pin and ground. Suitable values are shown in Figure 6.

C1 and C2 also improve the settling performance of the AD780 when subjected to load transients. The improvement in noise performance is shown in Figure 7, Figure 8, Figure 9, and Figure 10.
NOISE COMPARISON

The wideband noise performance of the AD780 can also be expressed in ppm. The typical performance with C1 and C2 is 0.6 ppm; without external capacitors, typical performance is 1.2 ppm.

This performance is, respectively, 7× and 3× lower than the specified performance of the LT1019.

TEMPERATURE PERFORMANCE

The AD780 provides superior performance over temperature by means of a combination of patented circuit design techniques, precision thin-film resistors, and drift trimming. Temperature performance is specified in terms of ppm/°C; because of nonlinearity in the temperature characteristic, the box test method is used to test and specify the part. The nonlinearity takes the form of the characteristic S-shaped curve shown in Figure 11. The box test method forms a rectangular box around this curve, enclosing the maximum and minimum output voltages over the specified temperature range. The specified drift is equal to the slope of the diagonal of this box.

TEMPERATURE OUTPUT PIN

The AD780 provides a TEMP output (Pin 3) that varies linearly with temperature. This output can be used to monitor changes in system ambient temperature, and to initiate calibration of the system, if desired. The voltage $V_{\text{TEMP}}$ is 560 mV at 25°C, and the temperature coefficient is approximately 2 mV/°C.

Figure 12 shows the typical $V_{\text{TEMP}}$ characteristic curve over temperature taken at the output of the op amp with a noninverting gain of 5.

Since the TEMP voltage is acquired from the band gap core circuit, current pulled from this pin has a significant effect on $V_{\text{OUT}}$. Care must be taken to buffer the TEMP output with a suitable op amp, for example, an OP07, AD820, or AD711 (all of which would result in less than a 100 µV change in $V_{\text{OUT}}$). The relationship between $I_{\text{TEMP}}$ and $V_{\text{OUT}}$ is

\[
\Delta V_{\text{OUT}} = 5.8 \text{ mV/µA } I_{\text{TEMP}} \text{ (2.5 V Range)}
\]

or

\[
\Delta V_{\text{OUT}} = 6.9 \text{ mV/µA } I_{\text{TEMP}} \text{ (3.0 V Range)}
\]
Notice how sensitive the current dependent factor on $V_{OUT}$ is. A large amount of current, even in tens of microamps, drawn from the TEMP pin can cause the $V_{OUT}$ and TEMP output to fail. The choice of C1 and C2 was dictated primarily by the need for a relatively flat response that rolled off early in the high frequency noise at the output. However, there is considerable margin in the choice of these capacitors. For example, the user can actually put a huge C2 on the TEMP pin with none on the output pin. However, one must either put very little or a lot of capacitance at the TEMP pin. Intermediate values of capacitance can sometimes cause oscillation. In any case, the user should follow the recommendation in Figure 6.

**TEMPERATURE TRANSDUCER CIRCUIT**

The circuit shown in Figure 13 is a temperature transducer that amplifies the TEMP output voltage by a gain of a little over +5 to provide a wider full-scale output range. The digital potentiometer can be used to adjust the output so it varies by exactly 10 mV/°C.

To minimize resistance changes with temperature, resistors with low temperature coefficients, such as metal film resistors, should be used.

**SUPPLY CURRENT OVER TEMPERATURE**

The quiescent current of the AD780 varies slightly over temperature and input supply range. The test limit is 1 mA over the industrial and 1.3 mA over the military temperature range. Typical performance with input voltage and temperature variation is shown in Figure 14.

**TURN-ON TIME**

The time required for the output voltage to reach its final value within a specified error band is defined as the turn-on settling time. The two major factors that affect this are the active circuit settling time and the time for the thermal gradients on the chip to stabilize. Typical settling performance is shown in Figure 15. The AD780 settles to within 0.1% of its final value within 10 µs.
DYNAMIC PERFORMANCE

The output stage of the AD780 has been designed to provide superior static and dynamic load regulation.

Figure 16 and Figure 17 show the performance of the AD780 while driving a 0 mA to 10 mA load.

The dynamic load may be resistive and capacitive. For example, the load may be connected via a long capacitive cable. Figure 18 and Figure 19 show the performance of the AD780 driving a 1000 pF, 0 mA to 10 mA load.

LINE REGULATION

Line regulation is a measure of change in output voltage due to a specified change in input voltage. It is intended to simulate worst-case unregulated supply conditions and is measured in µV/V. Figure 20 shows typical performance with 4.0 V < Vin < 15.0 V.
The AD780 is ideally suited to be the reference for most 5 V high resolution ADCs. The AD780 is stable under any capacitive load, has superior dynamic load performance, and its 3.0 V output provides the converter with the maximum dynamic range without requiring an additional and expensive buffer amplifier. One of the many ADCs that the AD780 is suited for is the AD7884, a 16-bit, high speed sampling ADC (see Figure 21). This part previously needed a precision 5 V reference, resistor divider, and buffer amplifier to do this function.

The AD780 is also ideal for use with higher resolution converters, such as the AD7710/AD7711/AD7712 (see Figure 22). While these parts are specified with a 2.5 V internal reference, the AD780 in 3 V mode can be used to improve the absolute accuracy, temperature stability, and dynamic range. It is shown in Figure 22 with the two optional noise reduction capacitors.

Some 5 V high resolution ADCs can accommodate reference voltages up to 4.5 V. The AD780 can be used to provide a precision 4.5 V reference voltage from a 5 V supply using the circuit shown in Figure 23. This circuit provides a regulated 4.5 V output from a supply voltage as low as 4.7 V. The high quality tantalum 10 µF capacitor, in parallel with the ceramic AD780 0.1 µF capacitor and the 3.9 Ω resistor, ensures a low output impedance around 50 MHz.

A precise −2.5 V reference capable of supplying up to 100 mA to a load can be implemented with the AD780 in series mode, using the bootstrap circuit shown in Figure 25.
OUTLINE DIMENSIONS

Figure 26. 8-Lead Standard Small Outline Package [SOIC_N] Narrow Body (R-8)
Dimensions shown in millimeters and (inches)

Figure 27. 8-Lead Plastic Dual-In-Line Package [PDIP] Narrow Body (N-8)
Dimensions shown in inches and (millimeters)
### ORDERING GUIDE

<table>
<thead>
<tr>
<th>Model</th>
<th>Initial Error</th>
<th>Temperature Range</th>
<th>Temperature Coefficient</th>
<th>Package Description</th>
<th>Package Option</th>
<th>Qty. per Tube/Reel/Wafflepack</th>
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<tbody>
<tr>
<td>AD780ANZ</td>
<td>±5.0 mV</td>
<td>−40°C to +85°C</td>
<td>7 ppm/°C</td>
<td>8-Lead PDIP</td>
<td>N-8</td>
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<td>AD780AR</td>
<td>±5.0 mV</td>
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<td>7 ppm/°C</td>
<td>8-Lead SOIC_N</td>
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<td>±5.0 mV</td>
<td>−40°C to +85°C</td>
<td>7 ppm/°C</td>
<td>8-Lead SOIC_N</td>
<td>R-8</td>
<td>750</td>
</tr>
<tr>
<td>AD780BNZ</td>
<td>±1.0 mV</td>
<td>−40°C to +85°C</td>
<td>3 ppm/°C</td>
<td>8-Lead PDIP</td>
<td>N-8</td>
<td>50</td>
</tr>
<tr>
<td>AD780BR</td>
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<td>−40°C to +85°C</td>
<td>3 ppm/°C</td>
<td>8-Lead SOIC_N</td>
<td>R-8</td>
<td>98</td>
</tr>
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<td>AD780BR-REEL7</td>
<td>±1.0 mV</td>
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<td>3 ppm/°C</td>
<td>8-Lead SOIC_N</td>
<td>R-8</td>
<td>750</td>
</tr>
<tr>
<td>AD780BRZ</td>
<td>±1.0 mV</td>
<td>−40°C to +85°C</td>
<td>3 ppm/°C</td>
<td>8-Lead SOIC_N</td>
<td>R-8</td>
<td>98</td>
</tr>
<tr>
<td>AD780BRZ-REEL</td>
<td>±1.0 mV</td>
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<td>R-8</td>
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<td>R-8</td>
<td>98</td>
</tr>
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<td>AD780CRZ-REEL7</td>
<td>±1.5 mV</td>
<td>−40°C to +85°C</td>
<td>7 ppm/°C</td>
<td>8-Lead SOIC_N</td>
<td>R-8</td>
<td>750</td>
</tr>
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<td>DIE</td>
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1 Z = RoHS Compliant Part.