



Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at
www.onsemi.com

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

5.3A, 1200V, NPT Series N-Channel IGBT

The HGTD1N120BNS and HGTP1N120BN are Non-Punch Through (NPT) IGBT designs. They are new members of the MOS gated high voltage switching IGBT family. IGBTs combine the best features of MOSFETs and bipolar transistors. This device has the high input impedance of a MOSFET and the low on-state conduction loss of a bipolar transistor.

The IGBT is ideal for many high voltage switching applications operating at moderate frequencies where low conduction losses are essential, such as: AC and DC motor controls, power supplies and drivers for solenoids, relays and contactors.

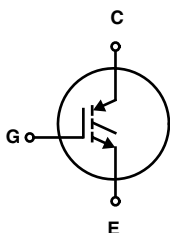
Formerly Developmental Type TA49316.

Ordering Information

| PART NUMBER | PACKAGE | BRAND |
|--------------|----------|---------|
| HGTD1N120BNS | TO-252AA | 1N120B |
| HGTP1N120BN | TO-220AB | 1N120BN |

NOTE: When ordering, use the entire part number. Add the suffix 9A to obtain the TO-252AA in tape and reel, i.e. HGTD1N120BNS9A

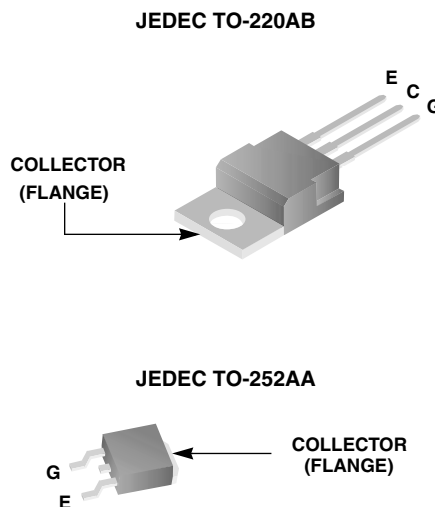
Symbol



Features

- 5.3A, 1200V, $T_C = 25^\circ\text{C}$
- 1200V Switching SOA Capability
- Typical $E_{OFF} \dots\dots\dots 120\mu\text{J}$ at $T_J = 150^\circ\text{C}$
- Short Circuit Rating
- Low Conduction Loss
- Avalanche Rated
- *Temperature Compensating SABER™ Model*
Thermal Impedance SPICE Model
www.fairchildsemi.com
- Related Literature
 - TB334, "Guidelines for Soldering Surface Mount Components to PC Boards"

Packaging



HGTD1N120BNS, HGTP1N120BN

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

| | ALL TYPES | UNITS |
|---|-------------|---------------------|
| Collector to Emitter Voltage | 1200 | V |
| Collector Current Continuous | | |
| At $T_C = 25^\circ\text{C}$ | 5.3 | A |
| At $T_C = 110^\circ\text{C}$ | 2.7 | A |
| Collector Current Pulsed (Note 1) | 6 | A |
| Gate to Emitter Voltage Continuous | ± 20 | V |
| Gate to Emitter Voltage Pulsed | ± 30 | V |
| Switching Safe Operating Area at $T_J = 150^\circ\text{C}$ (Figure 2) | 6A at 1200V | |
| Power Dissipation Total at $T_C = 25^\circ\text{C}$ | 60 | W |
| Power Dissipation Derating $T_C > 25^\circ\text{C}$ | 0.476 | W/ $^\circ\text{C}$ |
| Forward Voltage Avalanche Energy (Note 2) | 10 | mJ |
| Operating and Storage Junction Temperature Range | -55 to 150 | $^\circ\text{C}$ |
| Maximum Lead Temperature for Soldering | | |
| Leads at 0.063in (1.6mm) from Case for 10s | 300 | $^\circ\text{C}$ |
| Package Body for 10s, see Techbrief 334 | 260 | $^\circ\text{C}$ |
| Short Circuit Withstand Time (Note 3) at $V_{GE} = 15\text{V}$ | 8 | μs |
| Short Circuit Withstand Time (Note 3) at $V_{GE} = 13\text{V}$ | 13 | μs |

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTES:

1. Single Pulse; $V_{GE} = 15\text{V}$; Pulse width limited by maximum junction temperature.
2. $I_{CE} = 7\text{A}$, $L = 400\mu\text{H}$, $V_{GE} = 15\text{V}$, $T_J = 25^\circ\text{C}$.
3. $V_{CE(PK)} = 840\text{V}$, $T_J = 125^\circ\text{C}$, $R_G = 82\Omega$.

Electrical Specifications $T_C = 25^\circ\text{C}$, Unless Otherwise Specified

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNITS | |
|---|---------------|--|---------------------------|-----|-----------|-------|---------------|
| Collector to Emitter Breakdown Voltage | BV_{CES} | $I_C = 250\mu\text{A}$, $V_{GE} = 0\text{V}$ | 1200 | - | - | V | |
| Emitter to Collector Breakdown Voltage | BV_{ECS} | $I_C = 10\text{mA}$, $V_{GE} = 0\text{V}$ | 15 | - | - | V | |
| Collector to Emitter Leakage Current | I_{CES} | $V_{CE} = 1200\text{V}$ | $T_C = 25^\circ\text{C}$ | - | - | 250 | μA |
| | | | $T_C = 125^\circ\text{C}$ | - | 20 | - | μA |
| | | | $T_C = 150^\circ\text{C}$ | - | - | 1.0 | mA |
| Collector to Emitter Saturation Voltage | $V_{CE(SAT)}$ | $I_C = 1.0\text{A}$ $V_{GE} = 15\text{V}$ | $T_C = 25^\circ\text{C}$ | - | 2.5 | 2.9 | V |
| | | | $T_C = 150^\circ\text{C}$ | - | 3.8 | 4.3 | V |
| Gate to Emitter Threshold Voltage | $V_{GE(TH)}$ | $I_C = 50\mu\text{A}$, $V_{CE} = V_{GE}$ | 6.0 | 7.1 | - | V | |
| Gate to Emitter Leakage Current | I_{GES} | $V_{GE} = \pm 20\text{V}$ | - | - | ± 250 | nA | |
| Switching SOA | SSOA | $T_J = 150^\circ\text{C}$, $R_G = 82\Omega$, $V_{GE} = 15\text{V}$, $L = 2\text{mH}$, $V_{CE(PK)} = 1200\text{V}$ | 6 | - | - | A | |
| Gate to Emitter Plateau Voltage | V_{GEP} | $I_C = 1.0\text{A}$, $V_{CE} = 600\text{V}$ | - | 9.2 | - | V | |
| On-State Gate Charge | $Q_{G(ON)}$ | $I_C = 1.0\text{A}$ $V_{CE} = 600\text{V}$ | $V_{GE} = 15\text{V}$ | - | 14 | 20 | nC |
| | | | $V_{GE} = 20\text{V}$ | - | 15 | 21 | nC |

HGTD1N120BNS, HGTP1N120BN

Electrical Specifications $T_C = 25^\circ\text{C}$, Unless Otherwise Specified (Continued)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNITS |
|-------------------------------------|-----------------|--|-----|-----|-----|--------------------|
| Current Turn-On Delay Time | $t_{d(ON)I}$ | IGBT and Diode at $T_J = 25^\circ\text{C}$ $I_{CE} = 1.0\text{A}$ $V_{CE} = 960\text{V}$ $V_{GE} = 15\text{V}$ $R_G = 82\Omega$ $L = 4\text{mH}$ Test Circuit (Figure 18) | - | 15 | 20 | ns |
| Current Rise Time | t_{rI} | | - | 11 | 14 | ns |
| Current Turn-Off Delay Time | $t_{d(OFF)I}$ | | - | 67 | 76 | ns |
| Current Fall Time | t_{fI} | | - | 226 | 300 | ns |
| Turn-On Energy (Note 5) | E_{ON1} | | - | 70 | - | μJ |
| Turn-On Energy (Note 5) | E_{ON2} | | - | 172 | 187 | μJ |
| Turn-Off Energy (Note 4) | E_{OFF} | | - | 90 | 123 | μJ |
| Current Turn-On Delay Time | $t_{d(ON)I}$ | IGBT and Diode at $T_J = 150^\circ\text{C}$ $I_{CE} = 1.0\text{A}$ $V_{CE} = 960\text{V}$ $V_{GE} = 15\text{V}$ $R_G = 82\Omega$ $L = 4\text{mH}$ Test Circuit (Figure 18) | - | 13 | 17 | ns |
| Current Rise Time | t_{rI} | | - | 11 | 15 | ns |
| Current Turn-Off Delay Time | $t_{d(OFF)I}$ | | - | 75 | 88 | ns |
| Current Fall Time | t_{fI} | | - | 258 | 370 | ns |
| Turn-On Energy (Note 5) | E_{ON1} | | - | 145 | - | μJ |
| Turn-On Energy (Note 5) | E_{ON2} | | - | 385 | 440 | μJ |
| Turn-Off Energy (Note 4) | E_{OFF} | | - | 120 | 175 | μJ |
| Thermal Resistance Junction To Case | $R_{\theta JC}$ | | - | - | 2.1 | $^\circ\text{C/W}$ |

NOTES:

- Turn-Off Energy Loss (E_{OFF}) is defined as the integral of the instantaneous power loss starting at the trailing edge of the input pulse and ending at the point where the collector current equals zero ($I_{CE} = 0\text{A}$). All devices were tested per JEDEC Standard No. 24-1 Method for Measurement of Power Device Turn-Off Switching Loss. This test method produces the true total Turn-Off Energy Loss.
- Values for two Turn-On loss conditions are shown for the convenience of the circuit designer. E_{ON1} is the turn-on loss of the IGBT only. E_{ON2} is the turn-on loss when a typical diode is used in the test circuit and the diode is at the same T_J as the IGBT. The diode type is specified in Figure 18.

Typical Performance Curves (Unless Otherwise Specified)

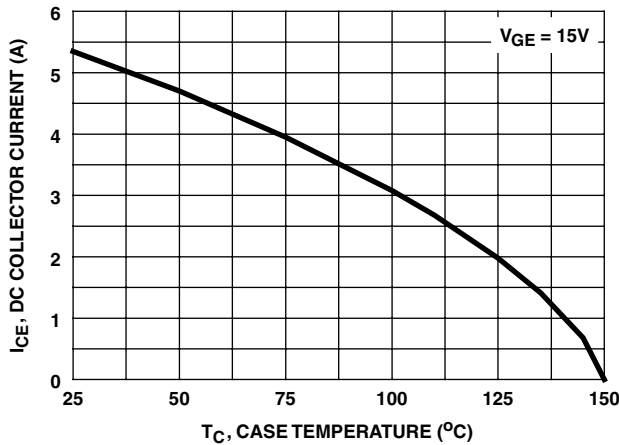


FIGURE 1. DC COLLECTOR CURRENT vs CASE TEMPERATURE

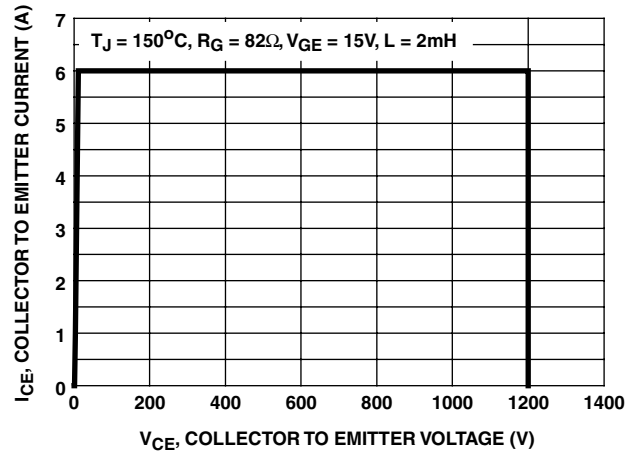


FIGURE 2. MINIMUM SWITCHING SAFE OPERATING AREA

Typical Performance Curves (Unless Otherwise Specified) (Continued)

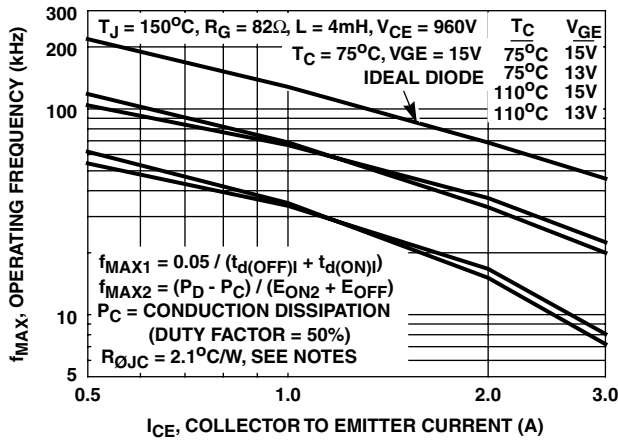


FIGURE 3. OPERATING FREQUENCY vs COLLECTOR TO EMITTER CURRENT

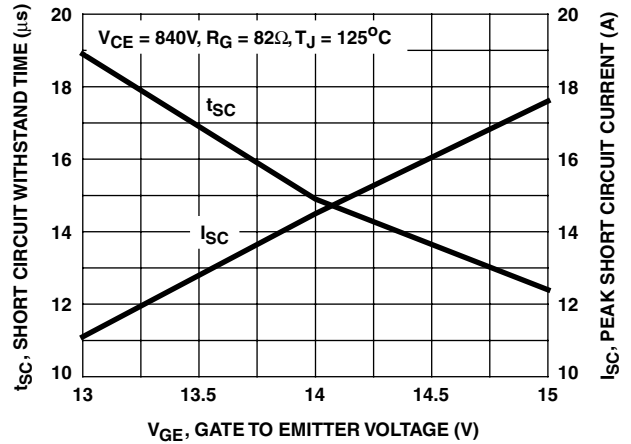


FIGURE 4. SHORT CIRCUIT WITHSTAND TIME

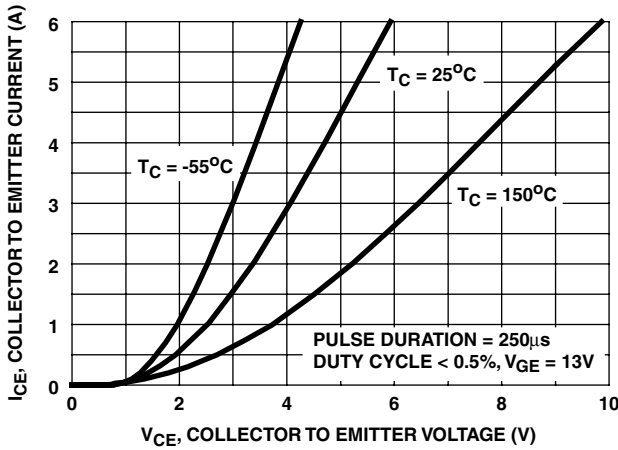


FIGURE 5. COLLECTOR TO EMITTER ON-STATE VOLTAGE

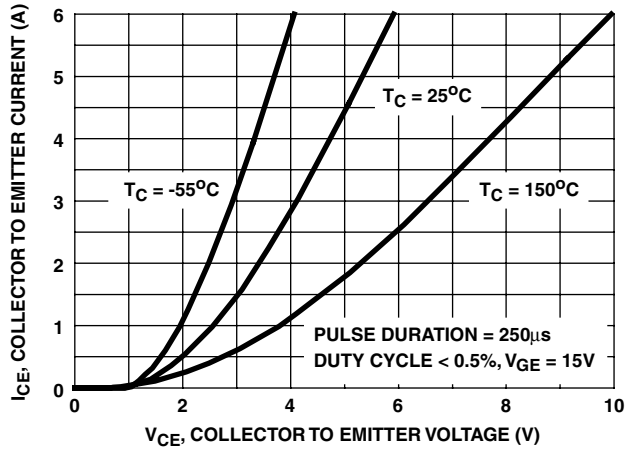


FIGURE 6. COLLECTOR TO EMITTER ON-STATE VOLTAGE

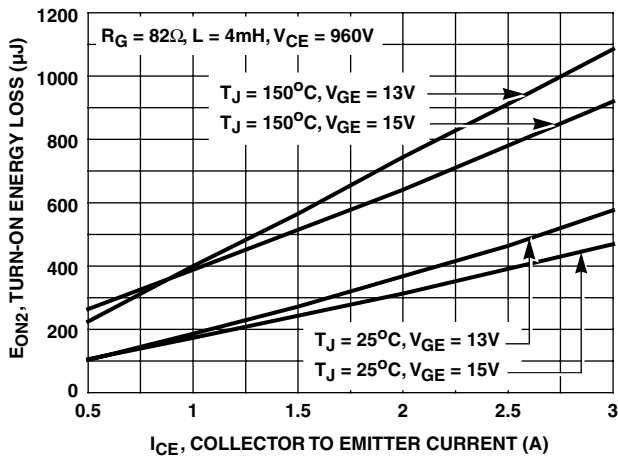


FIGURE 7. TURN-ON ENERGY LOSS vs COLLECTOR TO EMITTER CURRENT

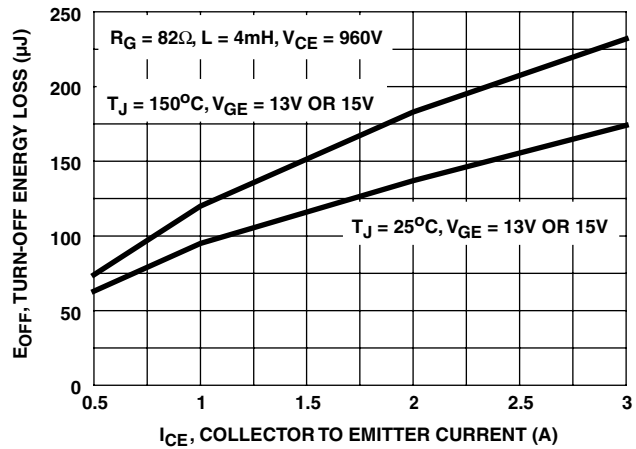


FIGURE 8. TURN-OFF ENERGY LOSS vs COLLECTOR TO EMITTER CURRENT

Typical Performance Curves (Unless Otherwise Specified) (Continued)

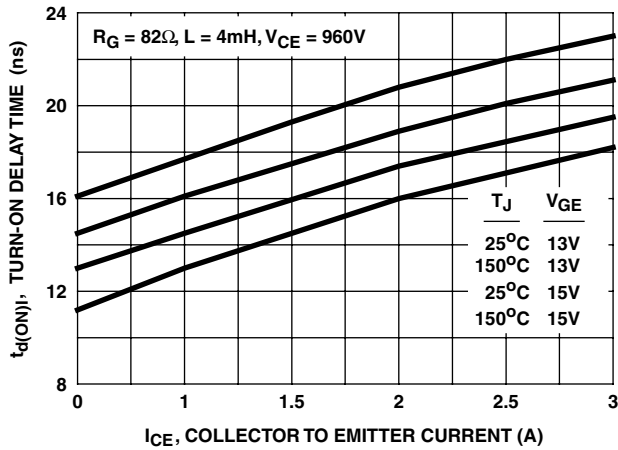


FIGURE 9. TURN-ON DELAY TIME vs COLLECTOR TO EMITTER CURRENT

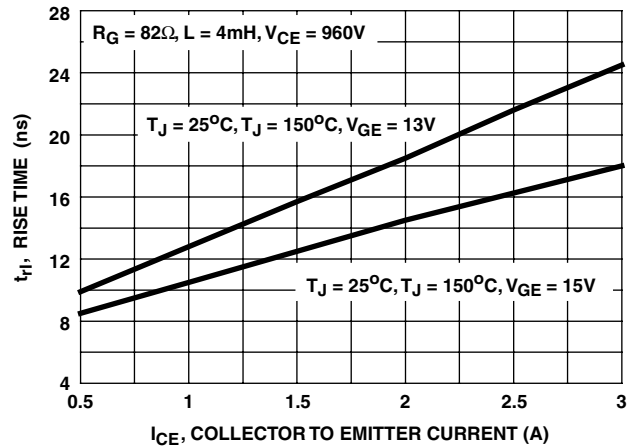


FIGURE 10. TURN-ON RISE TIME vs COLLECTOR TO EMITTER CURRENT

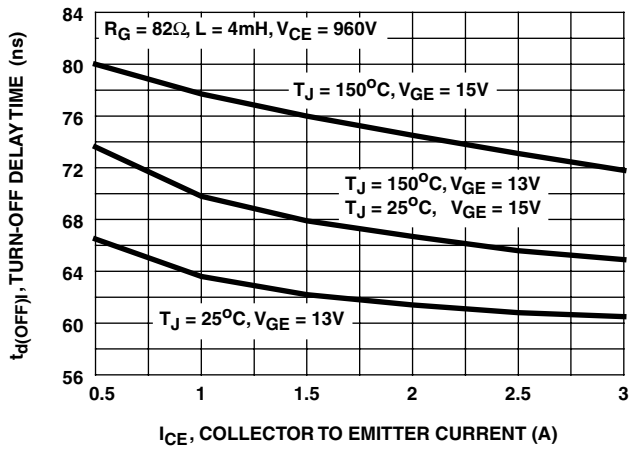


FIGURE 11. TURN-OFF DELAY TIME vs COLLECTOR TO EMITTER CURRENT

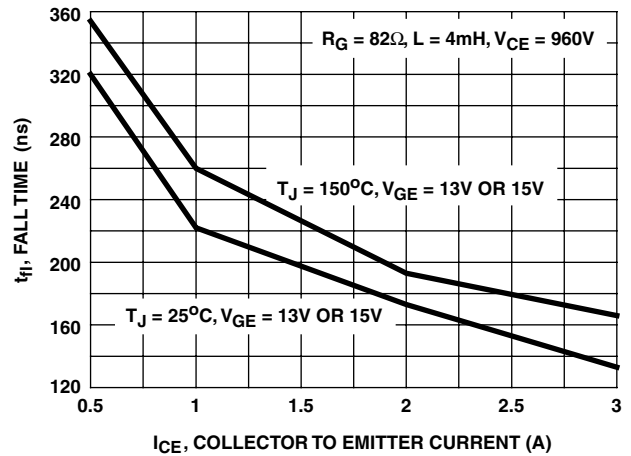


FIGURE 12. TURN-OFF FALL TIME vs COLLECTOR TO EMITTER CURRENT

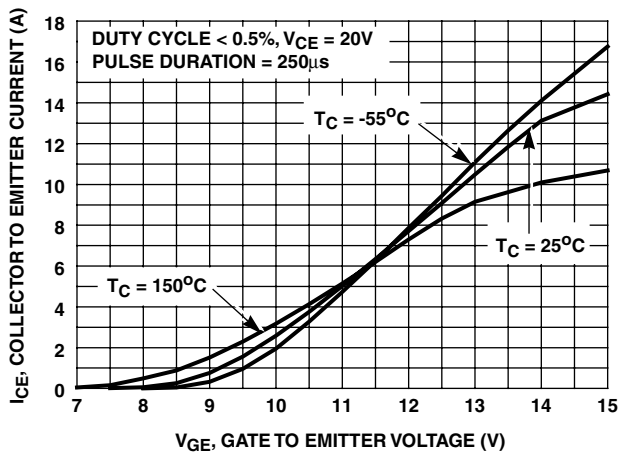


FIGURE 13. TRANSFER CHARACTERISTIC

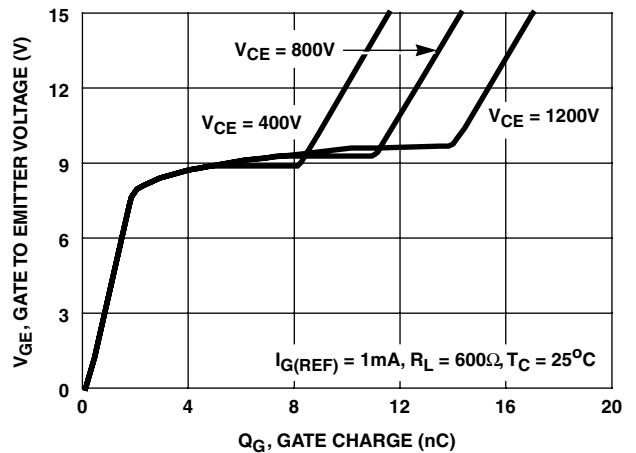


FIGURE 14. GATE CHARGE WAVEFORMS

Typical Performance Curves (Unless Otherwise Specified) (Continued)

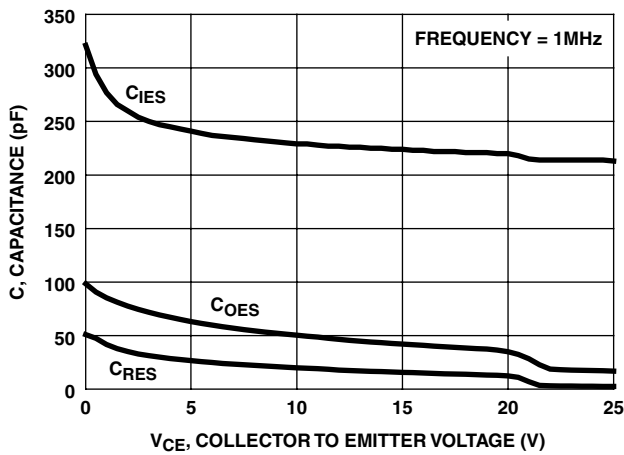


FIGURE 15. CAPACITANCE vs COLLECTOR TO EMITTER VOLTAGE

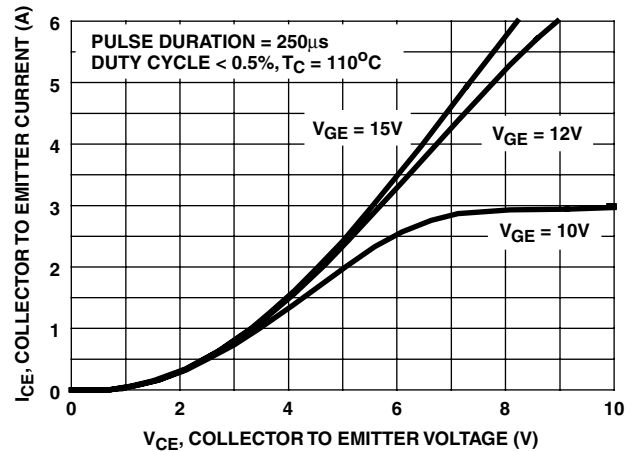


FIGURE 16. COLLECTOR TO EMITTER ON-STATE VOLTAGE

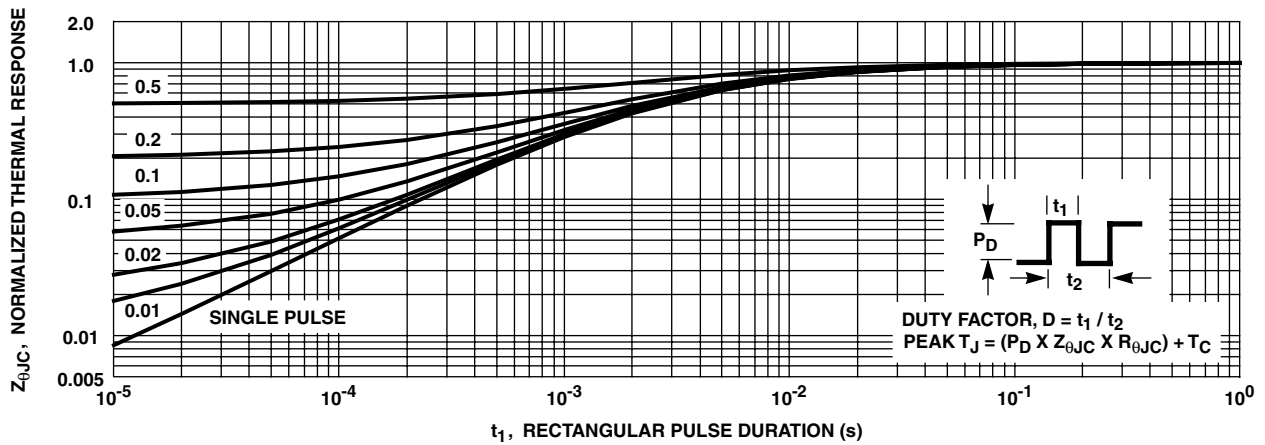


FIGURE 17. NORMALIZED TRANSIENT THERMAL RESPONSE, JUNCTION TO CASE

Test Circuit and Waveforms

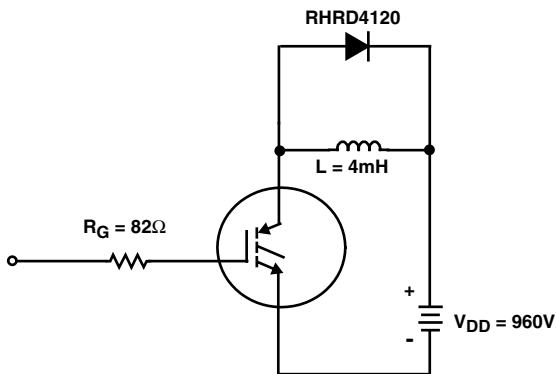


FIGURE 18. INDUCTIVE SWITCHING TEST CIRCUIT

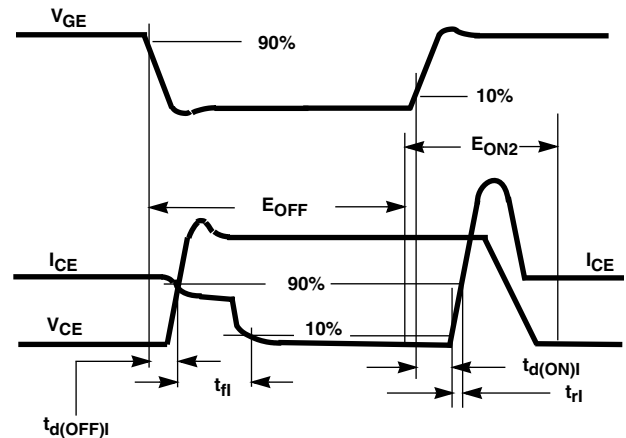


FIGURE 19. SWITCHING TEST WAVEFORMS

Handling Precautions for IGBTs

Insulated Gate Bipolar Transistors are susceptible to gate-insulation damage by the electrostatic discharge of energy through the devices. When handling these devices, care should be exercised to assure that the static charge built in the handler's body capacitance is not discharged through the device. With proper handling and application procedures, however, IGBTs are currently being extensively used in production by numerous equipment manufacturers in military, industrial and consumer applications, with virtually no damage problems due to electrostatic discharge. IGBTs can be handled safely if the following basic precautions are taken:

1. Prior to assembly into a circuit, all leads should be kept shorted together either by the use of metal shorting springs or by the insertion into conductive material such as "ECCOSORB™ LD26" or equivalent.
2. When devices are removed by hand from their carriers, the hand being used should be grounded by any suitable means - for example, with a metallic wristband.
3. Tips of soldering irons should be grounded.
4. Devices should never be inserted into or removed from circuits with power on.
5. **Gate Voltage Rating** - Never exceed the gate-voltage rating of V_{GEM} . Exceeding the rated V_{GE} can result in permanent damage to the oxide layer in the gate region.
6. **Gate Termination** - The gates of these devices are essentially capacitors. Circuits that leave the gate open-circuited or floating should be avoided. These conditions can result in turn-on of the device due to voltage buildup on the input capacitor due to leakage currents or pickup.
7. **Gate Protection** - These devices do not have an internal monolithic Zener diode from gate to emitter. If gate protection is required an external Zener is recommended.

Operating Frequency Information

Operating frequency information for a typical device (Figure 3) is presented as a guide for estimating device performance for a specific application. Other typical frequency vs collector current (I_{CE}) plots are possible using the information shown for a typical unit in Figures 6, 7, 8, 9 and 11. The operating frequency plot (Figure 3) of a typical device shows f_{MAX1} or f_{MAX2} ; whichever is smaller at each point. The information is based on measurements of a typical device and is bounded by the maximum rated junction temperature.

f_{MAX1} is defined by $f_{MAX1} = 0.05 / (t_{d(OFF)} + t_{d(ON)})$. Deadtime (the denominator) has been arbitrarily held to 10% of the on-state time for a 50% duty factor. Other definitions are possible. $t_{d(OFF)}$ and $t_{d(ON)}$ are defined in Figure 19. Device turn-off delay can establish an additional frequency limiting condition for an application other than T_{JM} . $t_{d(OFF)}$ is important when controlling output ripple under a lightly loaded condition.

f_{MAX2} is defined by $f_{MAX2} = (P_D - P_C) / (E_{OFF} + E_{ON2})$. The allowable dissipation (P_D) is defined by $P_D = (T_{JM} - T_C) / R_{\theta JC}$. The sum of device switching and conduction losses must not exceed P_D . A 50% duty factor was used (Figure 3) and the conduction losses (P_C) are approximated by $P_C = (V_{CE} \times I_{CE}) / 2$.

E_{ON2} and E_{OFF} are defined in the switching waveforms shown in Figure 19. E_{ON2} is the integral of the instantaneous power loss ($I_{CE} \times V_{CE}$) during turn-on and E_{OFF} is the integral of the instantaneous power loss ($I_{CE} \times V_{CE}$) during turn-off. All tail losses are included in the calculation for E_{OFF} ; i.e., the collector current equals zero ($I_{CE} = 0$).

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

| | | | | |
|-----------------------------------|----------------------------------|----------------------------------|------------------------------|-------------------|
| ACE _x TM | FAST [®] | OPTOLOGIC TM | SMART START TM | VCX TM |
| Bottomless TM | FAST _r TM | OPTOPLANAR TM | STAR*POWER TM | |
| CoolFET TM | FRFET TM | PACMAN TM | Stealth TM | |
| CROSSVOLT TM | GlobalOptoisolator TM | POP TM | SuperSOT TM -3 | |
| DenseTrench TM | GTO TM | Power247 TM | SuperSOT TM -6 | |
| DOMET TM | HiSeC TM | PowerTrench [®] | SuperSOT TM -8 | |
| EcoSPARK TM | ISOPLANAR TM | QFET TM | SyncFET TM | |
| E ² CMOS TM | LittleFET TM | QST TM | TinyLogic TM | |
| EnSigna TM | MicroFET TM | QT Optoelectronics TM | TruTranslation TM | |
| FACT TM | MicroPak TM | Quiet Series TM | UHC TM | |
| FACT Quiet Series TM | MICROWIRE TM | SILENT SWITCHER [®] | UltraFET [®] | |

STAR*POWER is used under license

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|------------------------|---|
| Advance Information | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| Obsolete | Not In Production | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only. |

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>
For additional information, please contact your local
Sales Representative