

Title of Change:	Change marking design and bonding method for WDFN6 devices
Proposed first ship date:	20 June 2017 or earlier after customer approval
Contact information:	Contact your local ON Semiconductor Sales Office or <Mutsuki.Niki@onsemi.com>
Samples:	Contact your local ON Semiconductor Sales Office
Additional Reliability Data:	Contact your local ON Semiconductor Sales Office or <Satoru.Fujinuma@onsemi.com>.
Type of notification:	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change. ON Semiconductor will consider this change accepted, unless an inquiry is made in writing within 30 days of delivery of this notice. To do so, contact <PCN.Support@onsemi.com>.
Change Part Identification:	Affected products will be identified with date code.
Change category:	<input type="checkbox"/> Wafer Fab Change <input checked="" type="checkbox"/> Assembly Change <input type="checkbox"/> Test Change <input type="checkbox"/> Other _____

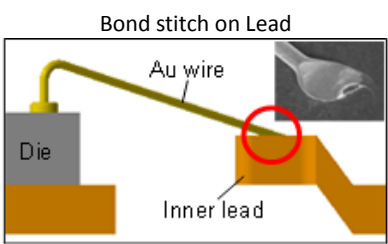
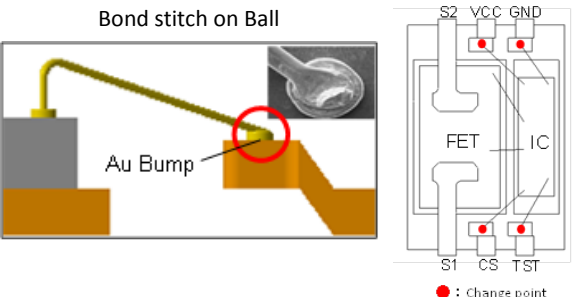
Change Sub-Category(s):	<input type="checkbox"/> Manufacturing Site Change/Addition	<input type="checkbox"/> Material Change	<input type="checkbox"/> Datasheet/Product Doc change
	<input checked="" type="checkbox"/> Manufacturing Process Change	<input type="checkbox"/> Product specific change	<input checked="" type="checkbox"/> Shipping/Packaging/Marking
			<input type="checkbox"/> Other: _____

Sites Affected:	<input type="checkbox"/> All site(s)	<input type="checkbox"/> not applicable	<input checked="" type="checkbox"/> ON Semiconductor site(s) : ON Carmona, Philippines ON Shenzhen, China	<input type="checkbox"/> External Foundry/Subcon site(s)
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Description and Purpose:

This Process Change Notification is to notify customers of the following changes:

- Change marking design of WDFN6 to adopt the eject pin at mold release
- Change top mark from 3-liner to 2-liner
- Change WDFN device bonding method from bond stitch on lead to on ball (BSOB)

	Before Change					After Change				
	Description					Description				
Package design	No eject pin					Adopt the eject pin				
Part number	5111C01	5111C02	5111C05	5112C01	5132C01	5111C01	5111C02	5111C05	5112C01	5132C01
Marking design	05111 C01MT AYWW .	05111 C02MT AYWW .	05111 C05MT AYWW .	05112 C01MT AYWW .	05132 C01MT AYWW .	C0501 AYWW .	C0502 AYWW .	C0505 AYWW .	CB201 AYWW .	CD201 AYWW .
bonding method										



Reliability Data Summary:

QV DEVICE NAME: LC05132C01NMTTTG

PACKAGE: WDFN6

Test	Specification	Condition	Interval	Results
TC	JESD22-A104	Ta= -55°C to +125°C	500 cyc	0/231
PC	J-STD-020 JESD-A113	MSL 3 @ 260 °C	-	-

Electrical Characteristic Summary:

Electrical characteristics are not impacted.

List of affected Standard Parts:

Part Number	Qualification Vehicle
LC05111C01MTTG	LC05132C01NMTTTG
LC05111C02MTTG	
LC05111C05MTTG	
LC05112C01MTTG	
LC05132C01MTTG	