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DF005S - DF10S
Bridge Rectifiers

Features
- Maximum Surge Rating: $I_{FSM} = 50 \text{ A}$
  $I^2t = 10 \text{ A}^2\text{Sec}$
- Optimized $V_F$: Typical 0.94 V at 1.5 A, 25°C
- Glass Passivated Junctions
- Lead Free Compliant to EU RoHS 2002/95/EU Directives
- Green Molding Compound: IEC61249
- Qualified with IR Reflow and Wave Soldering
- UL Certified, UL #E258596

Description
With the ever-pressing need to improve power supply efficiency, improve surge rating, improve reliability, and reduce size, the DFxS family sets a standard in performance.

The design offers an surge rating of 50 A. This is important when improving reliability and increasing efficiency. High efficiency designs strive to reduce circuit resistance, which, unfortunately can result in increased inrush surge. As such high surge current ratings can be required to maintain or improve reliability.

The design also offers better efficiency by achieving a 1.5 A $V_F$ of 1.1 V maximum at 25°C. This lower $V_F$ also supports cooler and more efficient operation.

Finally, the DFxS achieves all this in a SDIP surface mount form factor, reducing board space and volumetric requirements vs. competitive devices.

Ordering Information

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<th>Part Number</th>
<th>Top Mark</th>
<th>Package</th>
<th>Packing Method</th>
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<td>DF005S</td>
<td>DF005S</td>
<td>SDIP 4L</td>
<td>Tape and Reel</td>
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<tr>
<td>DF01S</td>
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<td>SDIP 4L</td>
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<td>DF10S</td>
<td>DF10S</td>
<td>SDIP 4L</td>
<td>Tape and Reel</td>
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</table>
**Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^\circ C$ unless otherwise noted.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Unit</th>
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<tbody>
<tr>
<td>$V_{RRM}$</td>
<td>Maximum Repetitive Reverse Voltage</td>
<td>DF005S: 50</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DF01S: 100</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>DF02S: 200</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>DF04S: 400</td>
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<td></td>
<td></td>
<td>DF06S: 600</td>
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<td></td>
<td></td>
<td>DF08S: 800</td>
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</tr>
<tr>
<td></td>
<td></td>
<td>DF10S: 1000</td>
<td></td>
</tr>
<tr>
<td>$V_{RMS}$</td>
<td>Maximum RMS Bridge Input Voltage</td>
<td>DF005S: 35</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DF01S: 70</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>DF02S: 140</td>
<td></td>
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<td></td>
<td></td>
<td>DF04S: 280</td>
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<td></td>
<td>DF06S: 560</td>
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<tr>
<td></td>
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<td>DF08S: 700</td>
<td></td>
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<tr>
<td>$I_{F(AV)}$</td>
<td>Average Rectified Forward Current at $T_A = 40^\circ C$</td>
<td>DF005S: 1.5</td>
<td>A</td>
</tr>
<tr>
<td>$I_{FSM}$</td>
<td>Non-Repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine Wave</td>
<td>DF005S: 50</td>
<td>A</td>
</tr>
<tr>
<td>$T_{STG}$</td>
<td>Storage Temperature Range</td>
<td>DF005S: -55 to +150</td>
<td>°C</td>
</tr>
<tr>
<td>$T_J$</td>
<td>Operating Junction Temperature</td>
<td>DF005S: -55 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

**Thermal Characteristics**

Values are at $T_A = 25^\circ C$ unless otherwise noted.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Unit</th>
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</thead>
<tbody>
<tr>
<td>$P_D$</td>
<td>Power Dissipation</td>
<td>DF005S: 3.1</td>
<td>W</td>
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<tr>
<td>$R_{\theta JA}$</td>
<td>Thermal Resistance, Junction-to-Ambient Single-Die Measurement$^{(1)}$ (Maximum Land Pattern: 13 x 13 mm)</td>
<td>DF005S: 62</td>
<td>°C/W</td>
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<td>Multi-Die Measurement$^{(2)}$ (Maximum Land Pattern: 13 x 13 mm)</td>
<td>DF005S: 50</td>
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<tr>
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<td></td>
<td>Multi-Die Measurement$^{(2)}$ (Minimum Land Pattern: 1.3 x 1.5 mm)</td>
<td>DF005S: 105</td>
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<tr>
<td>$\psi_{JL}$</td>
<td>Thermal Characterization Parameter, Junction to Lead Single-Die Measurement$^{(2)}$ (Maximum and Minimum Land Pattern)</td>
<td>DF005S: 27</td>
<td>°C/W</td>
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</tbody>
</table>

**Notes:**

1. Device mounted on PCB with 0.5 inch × 0.5 inch (13 mm × 13 mm).
2. The thermal resistances ($R_{\theta JA}$ & $\psi_{JL}$) are characterized with the device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm.

Heating effect from adjacent dice is considered and only two dices are powered at the same time.
### Electrical Characteristics

Values are at $T_A = 25^\circ C$ unless otherwise noted.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
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<tbody>
<tr>
<td>$V_F$</td>
<td>Forward Voltage, per Element</td>
<td>$I_F = 1.5$ A</td>
<td></td>
<td>1.1</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$I_R$</td>
<td>Reverse Current, per Element at Rated $V_R$</td>
<td>$T_A = 25^\circ C$</td>
<td></td>
<td>5.0</td>
<td></td>
<td>$\mu$A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$T_A = 125^\circ C$</td>
<td></td>
<td>500</td>
<td></td>
<td>$\mu$A</td>
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<tr>
<td>$I^2t$</td>
<td>Rating for Fusing ($t &lt; 8.35$ ms)</td>
<td></td>
<td></td>
<td>10</td>
<td></td>
<td>$A^2s$</td>
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<tr>
<td>$C_J$</td>
<td>Typical Capacitance, per Leg</td>
<td>$V_R = 4.0$ V, $f = 1.0$ MHz</td>
<td></td>
<td>25</td>
<td></td>
<td>$pF$</td>
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</tbody>
</table>
Typical Performance Characteristics

- **Figure 3. Non-Repetitive Surge Current**
- **Figure 4. Forward Current Derating Curve**
- **Figure 5. Forward Voltage Characteristics**
- **Figure 6. Reverse Current vs. Reverse Voltage**
Physical Dimensions

Figure 7. 4-LEAD, SDIP, 6.5 MM WIDE

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- TinyPower™
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- TransSiC™
- TriFault Detect™
- TRUECURRENT™
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<th>Datasheet Identification</th>
<th>Product Status</th>
<th>Definition</th>
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<td>Advance Information</td>
<td>Formative / In Design</td>
<td>Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.</td>
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<tr>
<td>Preliminary</td>
<td>First Production</td>
<td>Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.</td>
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<tr>
<td>No Identification Needed</td>
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<td>Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.</td>
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<tr>
<td>Obsolete</td>
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<td>Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.</td>
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Rev. I74