Data and signal line chokes

Common-mode chokes, ring core
0.005 … 47 mH, 100 … 1200 mA, +60 °C

Series/Type: B82793C0/S0
Date: March 2016
Data and signal line chokes  B82793C0/S0

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Rated voltage 42 V AC/80 V DC
Rated inductance 0.005 ... 47 mH
Rated current 100 ... 1200 mA

Construction
- Current-compensated double choke
- Ferrite core
- LCP case (UL 94 V-0), silicone potting
- Bifilar winding (B82793C0)
- Sector winding (B82793S0)

Features
- High rated currents, reduced components height
- Qualified to AEC-Q200 (L ≤ 4.7 mH)
- Suitable for reflow soldering
- RoHS-compatible

Function
- B82793C0: Suppression of asymmetrical interference coupled in on lines, whereas data signals up to some MHz can pass unaffectedly.
- B82793S0: Suppression of asymmetrical and symmetrical interference (by $L_{stray}$) coupled in on lines. The high-frequency portions of the symmetrical data signal are decreased so far that EMC problems can be significantly reduced.

Applications
- Automotive applications, e.g. CAN bus
- Industrial applications
- Types with $L_R > 4.7$ mH only for telecom applications

Terminals
- Base material CuSn6
- Layer composition Ni, Sn
- Hot-dipped

Marking
- Marking on component: Manufacturer, process location (coded), winding method (coded), ordering code (short form), date of manufacture (YWWD)
- Minimum data on reel: Manufacturer, ordering code, L value and tolerance, quantity, date of packing

Delivery mode and packing unit
- 16-mm blister tape, wound on 330-mm Ø reel
- Packing unit: 1500 pcs./reel

Please read Cautions and warnings and Important notes at the end of this document.
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SMD

Dimensional drawing and pin configuration

```
No polarity
```

```
1) Soldering area
```

Taping and packing
Blister tape

```
Dimensions in mm
```

```
Reel
```

Please read Cautions and warnings and Important notes at the end of this document.
## Technical data and measuring conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated voltage $V_R$</td>
<td>42 V AC (50/60 Hz) / 80 V DC</td>
</tr>
<tr>
<td>Rated temperature $T_R$</td>
<td>+60 °C</td>
</tr>
<tr>
<td>Rated current $I_R$</td>
<td>Referred to 50 Hz and rated temperature</td>
</tr>
</tbody>
</table>
| Rated inductance $L_R$             | Measured with Agilent 4284A, 0.1 mA, +20 °C  
|                                    | Measuring frequency: $L_R \leq 1$ mH = 100 kHz  
|                                    | $L_R > 1$ mH = 10 kHz                                                        |
| Inductance tolerance               | ±30% ($L_R \leq 0.47$ mH), −30/+50% ($L_R \geq 1$ mH) at +20 °C              |
| Inductance decrease $\Delta L/L$   | < 10% at DC magnetic bias with $I_R$, +20 °C                                 |
| Stray inductance $L_{\text{stray,typ}}$ | Measured with Agilent 4284A, 5 mA, +20 °C, typical values  
|                                    | Measuring frequency: $L_R \leq 1$ $\mu$H = 1 MHz  
|                                    | $L_R > 1$ $\mu$H = 100 kHz                                                   |
| DC resistance $R_{\text{typ}}$     | Measured at +20 °C, typical values, specified per winding                    |
| Solderability                      | SnPb: $(215 \pm 3)$ °C, $(3 \pm 0.3)$ s                                      |
|                                    | Sn96.5Ag3.0Cu0.5: $(245 \pm 5)$ °C, $(3 \pm 0.3)$ s                         |
|                                    | Wetting of soldering area ≥ 95%                                               |
|                                    | (to IEC 60068-2-58)                                                          |
| Resistance to soldering heat       | $(260 \pm 5)$ °C, $(10 \pm 1)$ s (to IEC 60068-2-58)                        |
| Climatic category                  | 40/125/56 (to IEC 60068-1)                                                   |
| Storage conditions (packaged)      | −25 °C … +40 °C, ≤ 75% RH                                                     |
| Weight                             | Approx. 0.25 g                                                                |
## Characteristics and ordering codes

<table>
<thead>
<tr>
<th>(L_R) mH</th>
<th>(L_{\text{stray,typ}}) nH</th>
<th>(I_R) mA</th>
<th>(R_{\text{typ}}) m(\Omega)</th>
<th>(V_{\text{test}}) V DC, 2 s</th>
<th>Ordering code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.005</td>
<td>40</td>
<td>1200</td>
<td>60</td>
<td>250</td>
<td>B82793C0502N201</td>
</tr>
<tr>
<td>0.011</td>
<td>50</td>
<td>800</td>
<td>80</td>
<td>250</td>
<td>B82793C0113N201</td>
</tr>
<tr>
<td>0.025</td>
<td>60</td>
<td>800</td>
<td>110</td>
<td>250</td>
<td>B82793C0253N201</td>
</tr>
<tr>
<td>0.025</td>
<td>1400</td>
<td>800</td>
<td>110</td>
<td>250</td>
<td>B82793S0253N201</td>
</tr>
<tr>
<td>0.051</td>
<td>70</td>
<td>800</td>
<td>140</td>
<td>250</td>
<td>B82793C0513N201</td>
</tr>
<tr>
<td>0.051</td>
<td>2300</td>
<td>800</td>
<td>140</td>
<td>250</td>
<td>B82793S0513N201</td>
</tr>
<tr>
<td>0.10</td>
<td>100</td>
<td>500</td>
<td>180</td>
<td>250</td>
<td>B82793C0104N201</td>
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<tr>
<td>0.47</td>
<td>100</td>
<td>700</td>
<td>170</td>
<td>750</td>
<td>B82793C0474N215</td>
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<tr>
<td>1.0</td>
<td>70</td>
<td>700</td>
<td>140</td>
<td>750</td>
<td>B82793C0105N265</td>
</tr>
<tr>
<td>2.2</td>
<td>120</td>
<td>500</td>
<td>400</td>
<td>750</td>
<td>B82793C0225N265</td>
</tr>
<tr>
<td>4.7</td>
<td>250</td>
<td>400</td>
<td>550</td>
<td>750</td>
<td>B82793C0475N265</td>
</tr>
</tbody>
</table>

For telecommunications

<table>
<thead>
<tr>
<th>(L_R) mH</th>
<th>(L_{\text{stray,typ}}) nH</th>
<th>(I_R) mA</th>
<th>(R_{\text{typ}}) m(\Omega)</th>
<th>(V_{\text{test}}) V DC, 2 s</th>
<th>Ordering code</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>300</td>
<td>100</td>
<td>1800</td>
<td>750</td>
<td>B82793C0206N265</td>
</tr>
<tr>
<td>47</td>
<td>1200</td>
<td>100</td>
<td>3700</td>
<td>750</td>
<td>B82793C0476N265</td>
</tr>
</tbody>
</table>
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**SMD**

**Insertion loss** $\alpha$ (typical values at $|Z| = 50 \, \Omega, +20 \, ^\circ\mathrm{C}$)

- - - - - asymmetrical, all branches in parallel (common mode)
- - - - symmetrical (differential mode)

$L_R = 0.005 \, \text{mH}$

$L_R = 0.011 \, \text{mH}$

$L_R = 0.025 \, \text{mH}$ (low $L_{\text{stray}}$)

$L_R = 0.025 \, \text{mH}$ (high $L_{\text{stray}}$)

Please read Cautions and warnings and Important notes at the end of this document.
**SMD**

**Insertion loss** $\alpha$ (typical values at $|Z| = 50 \, \Omega$, $+20 \, ^\circ\text{C}$)
- Solid line: asymmetrical, all branches in parallel (common mode)
- Dashed line: symmetrical (differential mode)

$L_R = 0.051 \, \text{mH (low L}_{\text{stray}})$

$L_R = 0.051 \, \text{mH (high L}_{\text{stray}})$

$L_R = 0.10 \, \text{mH}$

$L_R = 0.47 \, \text{mH}$

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**Data and signal line chokes**

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**SMD**

*Insertion loss* $\alpha$ (typical values at $|Z| = 50 \, \Omega$, +20 °C)

- - asymmetrical, all branches in parallel (common mode)
- - - - symmetrical (differential mode)

$L_R = 1.0 \, \text{mH}$

$L_R = 2.2 \, \text{mH}$

$L_R = 4.7 \, \text{mH}$

$L_R = 20 \, \text{mH}$

Please read *Cautions and warnings* and *Important notes* at the end of this document.
Insertion loss $\alpha$ (typical values at $|Z| = 50 \, \Omega$, +20 °C)

- asymmetrical, all branches in parallel (common mode)
- symmetrical (differential mode)

$L_R = 47 \, \text{mH}$

Current derating $I_{\text{op}}/I_R$ versus ambient temperature

$T_R = 60 \, ^\circ \text{C}$
Recommended reflow soldering curve
Pb containing solder material (based on CECC 00802 edition 2)

\[ \begin{align*}
T_1 & = 150 \, ^\circ C \\
T_2 & = 200 \, ^\circ C \\
T_3 & = 217 \, ^\circ C \\
T_4 & = 250 \, ^\circ C \\
t_1 & < 110 \, s \\
t_2 & < 90 \, s \\
t_3 & < 40 \, s \, @ \, T_4 - 5 \, ^\circ C \\
\end{align*} \]

Time from +25 °C to \( T_4 \): max 300 s
Maximal numbers of reflow cycles: 3

Pb-free solder material (based on JEDEC J-STD 020D)
Cautions and warnings

SMD

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.

- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation. Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.

- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.

- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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