## Nch 30V 13.5A Middle Power MOSFET

$V_{DSS}$	30V
R <sub>DS(on)</sub> (Max.)	10.4mΩ
I <sub>D</sub>	±13.5A
P <sub>D</sub>	15W

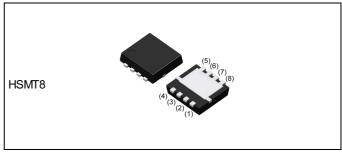
## Features

- 1) Low on resistance.
- 2) High Power Package (HSMT8).
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

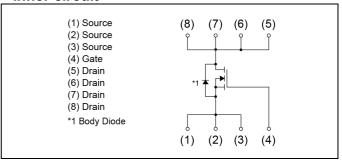
# Application

Switching

#### Outline



## ●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Basic ordering unit (pcs)	3000
	Taping code	ТВ
	Marking	E100BN

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	30	V
Continuous dusin suument	T <sub>c</sub> = 25°C	I <sub>D</sub> *1	±13.5	Α
Continuous drain current	T <sub>a</sub> = 25°C	I <sub>D</sub>	±10	Α
Pulsed drain current	I <sub>DP</sub> *2	±40	Α	
Gate - Source voltage	$V_{GSS}$	±20	V	
Avalanche current, single pulse		I <sub>AS</sub> *3	12	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	14	mJ
Dawar dissination		P <sub>D</sub> *1	15	W
Power dissipation		P <sub>D</sub> *4	2	W
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage te	T <sub>stg</sub>	-55 to +150	°C	

## ●Thermal resistance

Dorameter	Cymah al	Values			I India
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	-	8.2	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *4	-	-	62.5	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Cymahal	Symbol Conditions		Values			
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		-	21	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	1	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	V <sub>DS</sub> = 10, I <sub>D</sub> = 1mA	1.0	1	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-3	-	mV/°C	
Static drain - source	D *5	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A	-	7.7	10.4	mΩ	
on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A	-	11.0			
Gate resistance	$R_{G}$	f=1MHz, open drain	-	2.5	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *5	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	12	-	-	S	

<sup>\*1</sup>Tc=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  0.1mH,  $V_{DD}$  = 24V,  $R_G$  = 25 $\Omega$ , STARTING  $T_j$  = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Mounted on a ceramic boad (30×30×0.8mm)

<sup>\*5</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	1	1100	1		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	1	130	1	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	1	105	1		
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 15V, V_{GS} = 10V$	1	10	1		
Rise time	<b>t</b> <sub>r</sub> *5	I <sub>D</sub> = 5A	1	28	1	no	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ~ 1.67Ω	ı	44	ı	ns	
Fall time	t <sub>f</sub> *5	$R_G = 10\Omega$	-	10	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Doromotor	Cymahal	Conditions		Values			1.1
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Total mate about	O *5	$Q_g^{*5}$ $V_{DD} \simeq 15V$	V <sub>GS</sub> = 10V	-	22.0	-	
Total gate charge	$Q_g$			-	10.5	-	
Gate - Source charge	Q <sub>gs</sub> *5		V <sub>GS</sub> = 4.5V	-	3.0	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5			-	4.2	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Doromotor	Parameter Symbol Conditions -		Values			Unit
raiametei			Min.	Тур.	Max.	Uniit
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	1.67	Α
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	-	-	40	Α
Forward voltage	V <sub>SD</sub> *5	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.67A	-	-	1.2	V

### Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

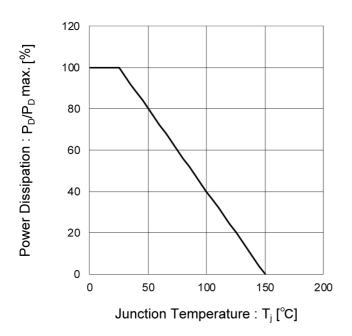
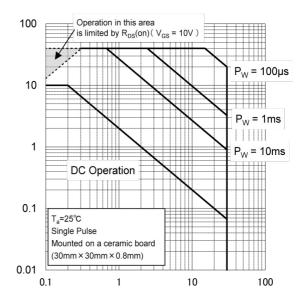


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

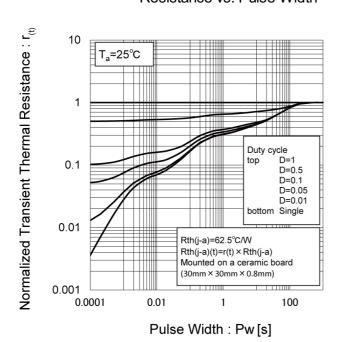
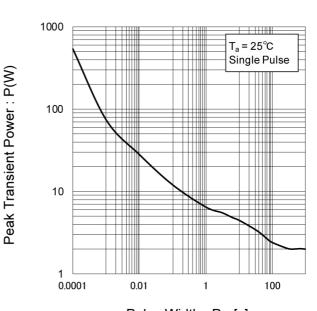


Fig.4 Single Pulse Maximum Power dissipation

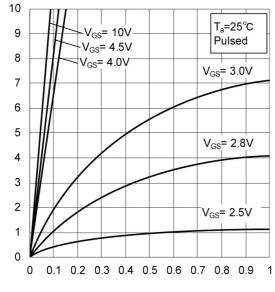


Pulse Width : Pw [s]

Drain Current : I<sub>D</sub> [A]

### • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)

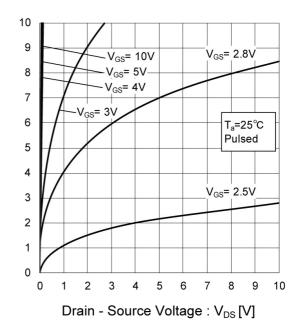


Fig.7 Breakdown Voltage vs. Junction Temperature

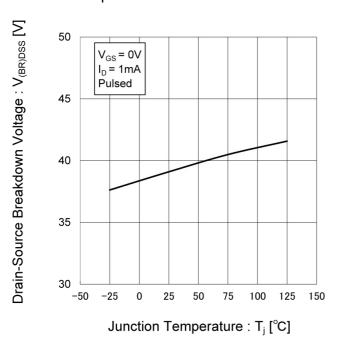
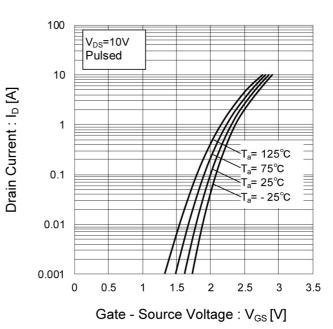


Fig.8 Typical Transfer Characteristics



## • Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs. Junction Temperature

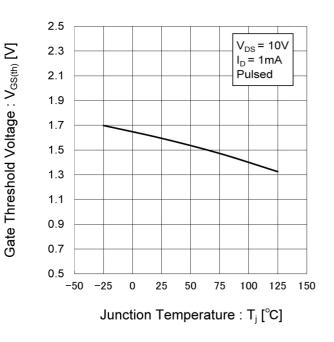


Fig.10 Forward Transfer Admittance vs. Drain Current

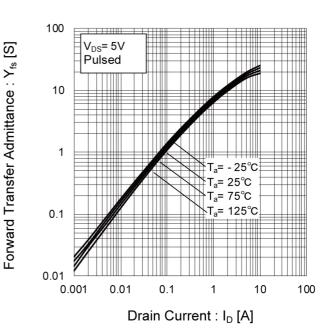


Fig.11 Drain Current Derating Curve

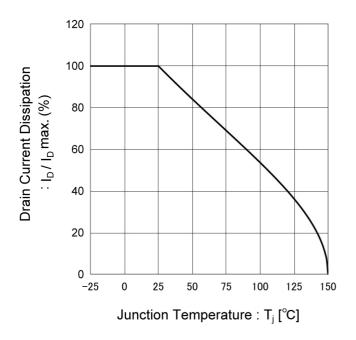
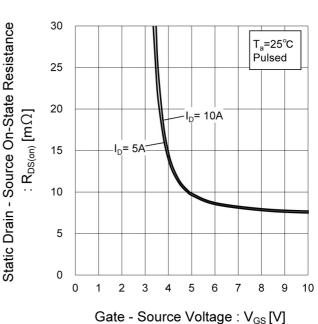


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



## • Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

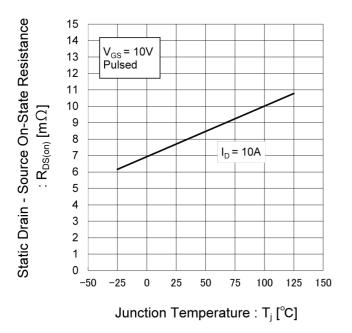


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

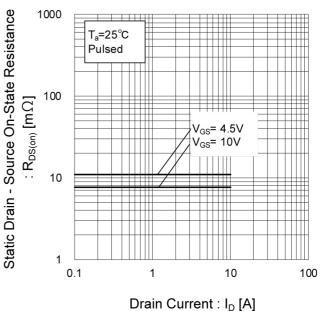


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

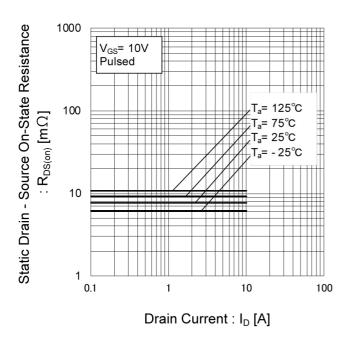
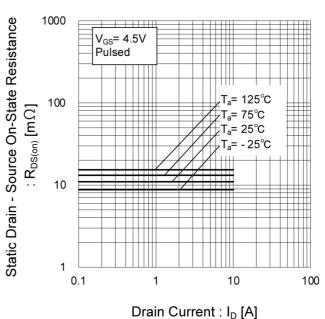


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)



## • Electrical characteristic curves

Fig.17 Typical Capacitance vs. Drain - Source Voltage

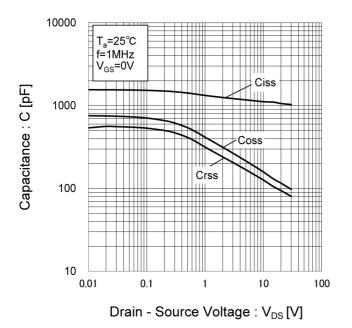


Fig.18 Switching Characteristics

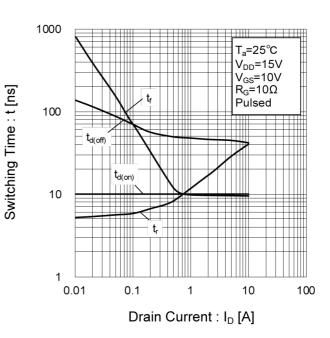


Fig.19 Dynamic Input Characteristics

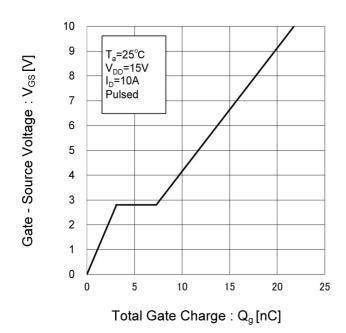
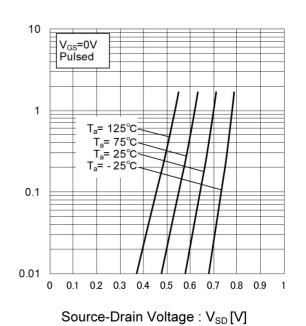


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

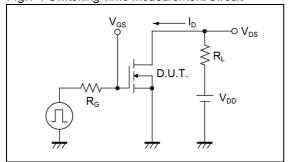


Fig.2-1 Gate Charge Measurement Circuit

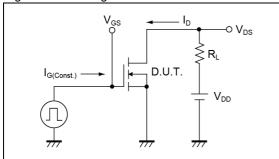


Fig.3-1 Avalanche Measurement Circuit

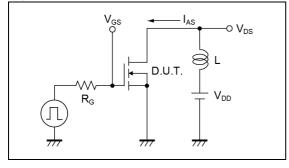


Fig.1-2 Switching Waveforms

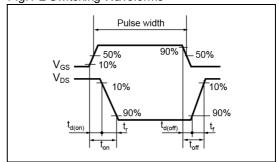


Fig.2-2 Gate Charge Waveform

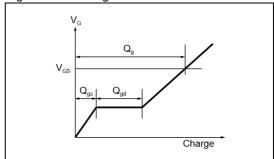
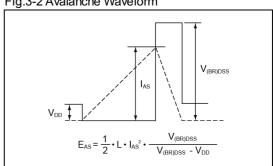


Fig.3-2 Avalanche Waveform



## Notice

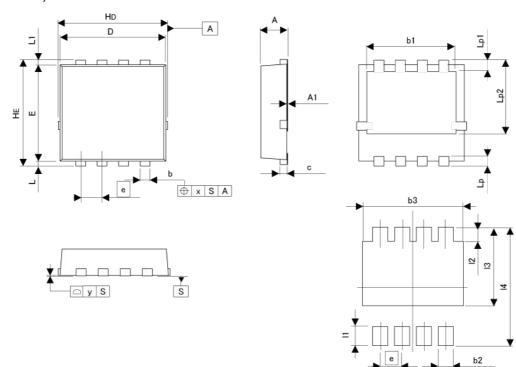
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



## Dimensions

## HSMT8

(3.3x3.3)



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INCI	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.70	0.90	0.028	0.035
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
b1	2.50	2.70	0.098	0.106
С	0.10	0.30	0.004	0.012
D	3.10	3.30	0.122	0.130
E	2.90	3.10	0.114	0.122
е	0.	65	0.0	)26
HD	3.20	3.40	0.126	0.134
HE	3.20	3.40	0.126	0.134
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.20	0.40	0.008	0.016
Lp1	0.25	0.45	0.010	0.018
Lp2	2.20	2.40	0.087	0.094
Х	-	0.10	-	0.004
у	(-)	0.10	-	0.004

-	DIM	MILIME	ETERS	INC	HES
ı	DIIVI	MIN	MAX	MIN	MAX
	b2	Ð	0.47	1	0.019
	b3	9-1	2.70	-	0.106
	l1	-	0.50	-	0.020
	12	-	0.55	-	0.022
	13		2.40	-	0.094
	14	-	3.40	-	0.134

Dimension in mm/inches



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CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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# RQ3E100BN - Web Page

**Distribution Inventory** 

Part Number	RQ3E100BN
Package	HSMT8
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes