

4V Drive Pch MOS FET

RSQ035P03FRA

●Structure

Silicon P-channel MOSFET

●Features

- 1) Low On-resistance. (65mΩ at 4.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive. (4V)

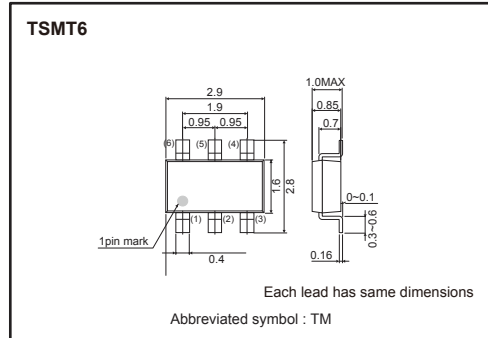
●Applications

DC-DC converter

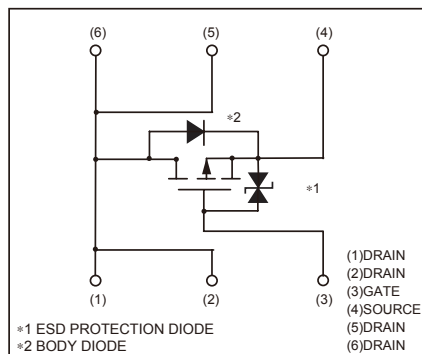
●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ035P03FRA		○

●External dimensions (Unit : mm)



●Equivalent circuit



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V_{DSS}	-30	V	
Gate-source voltage	V_{GSS}	± 20	V	
Drain current	Continuous	I_D	± 3.5	A
	Pulsed	I_{DP} *1	± 14	A
Source current (Body diode)	Continuous	I_S	-1	A
	Pulsed	I_{SP} *1	-4	A
Total power dissipation	P_D *2	1.25	W	
Channel temperature	T_{ch}	150	°C	
Range of Storage temperature	T_{stg}	-55 to +150	°C	

*1 $P_w \leq 10\mu s$, Duty cycle $\leq 1\%$

*2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	$R_{th(ch-a)}$ *	100	°C / W

* Mounted on a ceramic board.

Transistor

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	–	–	±10	μA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	–30	–	–	V	$I_D=-1mA, V_{GS}=0V$
Zero gate voltage drain current	I_{DSS}	–	–	–1	μA	$V_{DS}=-30V, V_{GS}=0V$
Gate threshold voltage	$V_{GS(th)}$	–1.0	–	–2.5	V	$V_{DS}=-10V, I_D=-1mA$
Static drain-source on-state resistance	$R_{DS(on)}$ *	–	45	65	mΩ	$I_D=-3.5A, V_{GS}=-10V$
		–	65	90	mΩ	$I_D=-3.5A, V_{GS}=-4.5V$
		–	70	95	mΩ	$I_D=-1.75A, V_{GS}=-4.0V$
Forward transfer admittance	$ Y_{fs} $ *	2.0	–	–	S	$V_{DS}=-10V, I_D=-1.75A$
Input capacitance	C_{iss}	–	780	–	pF	$V_{DS}=-10V, V_{GS}=0V$ $f=1MHz$
Output capacitance	C_{oss}	–	180	–	pF	
Reverse transfer capacitance	C_{rss}	–	130	–	pF	
Turn-on delay time	$t_{d(on)}$ *	–	15	–	ns	$I_D=-1.75A$ $V_{DD}=-15V$ $V_{GS}=-10V$ $R_L=8.6\Omega$ $R_G=10\Omega$
Rise time	t_r *	–	35	–	ns	
Turn-off delay time	$t_{d(off)}$ *	–	45	–	ns	
Fall time	t_f *	–	25	–	ns	
Total gate charge	Q_g	–	9.2	–	nC	$V_{DD}=-15V$ $V_{GS}=-5V$ $I_D=-3.5A$
Gate-source charge	Q_{gs}	–	2.2	–	nC	
Gate-drain charge	Q_{gd}	–	3.4	–	nC	

*PULSED

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_{SD}	–	–	–1.2	V	$I_S=-1A, V_{GS}=0V$

Transistor

●Electrical characteristic curves

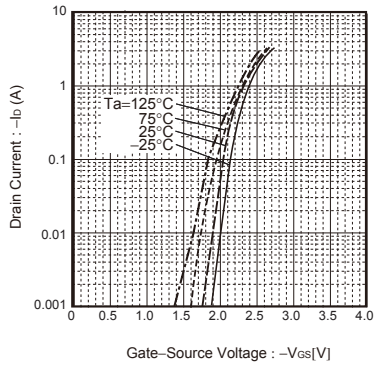


Fig.1 Typical Transfer Characteristics

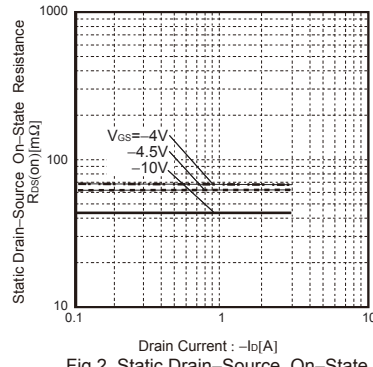


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

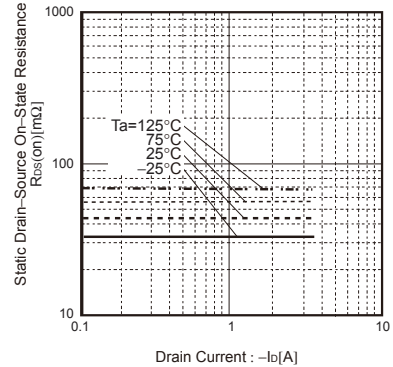


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

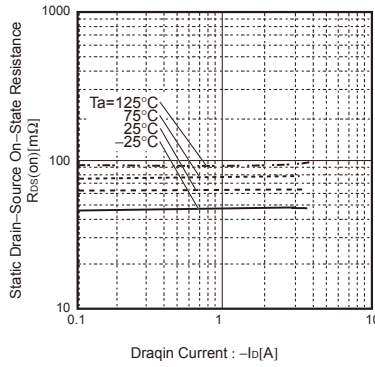


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

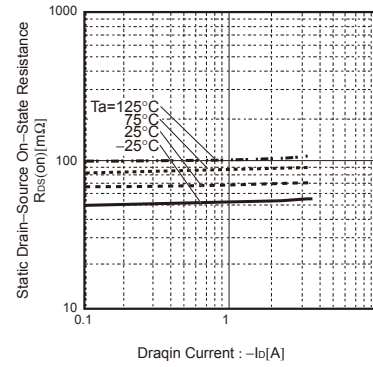


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

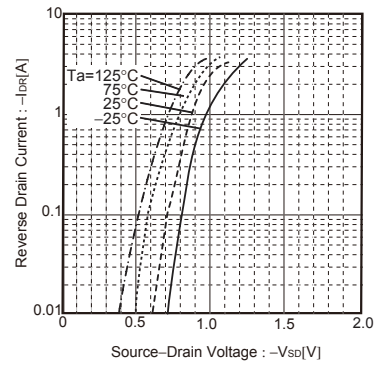


Fig.6 Reverse Drain Current Source-Drain Current

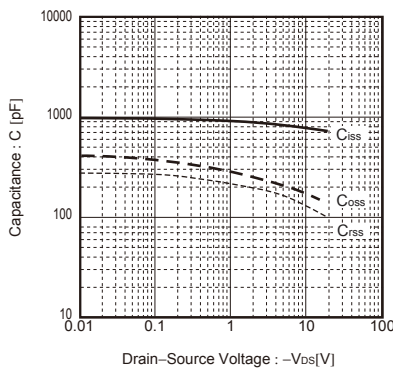


Fig.7 Typical Capacitance vs. Drain-Source Voltage

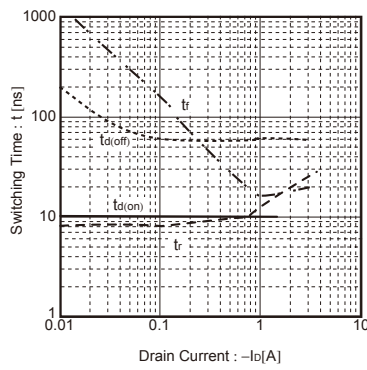


Fig.8 Switching Characteristics

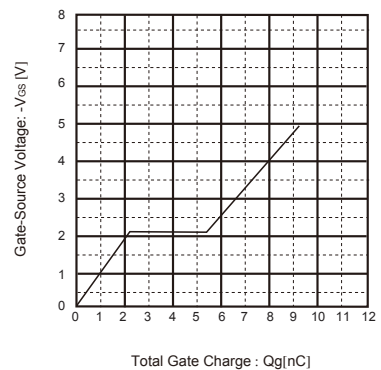


Fig.9 Dynamic Input Characteristics

Transistor

● Switching characteristics measurement circuits

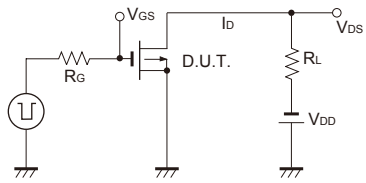


Fig.10 Switching Time Test Circuit

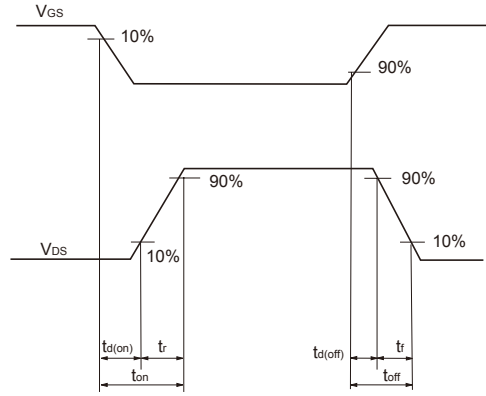


Fig.11 Switching Time Waveforms

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
4. The Products are not subject to radiation-proof design.
5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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[Distribution Inventory](#)

Part Number	RSQ035P03FRA
Package	TSMT6
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes